Time=0 | Floor: 0 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0000

--- TC1 ---

Time=35000 | Floor: 0 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0100

Time=65000 | Floor: 1 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 0100

Time=85000 | Floor: 2 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 0000

Time=95000 | Floor: 2 | Moving: 0 | Door: 1 | Dir: 1 | Reqs: 0000

Time=205000 | Floor: 2 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0000

Time=245000 | Floor: 2 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0010

Time=265000 | Floor: 2 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0010

Time=275000 | Floor: 1 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0000

Time=285000 | Floor: 1 | Moving: 0 | Door: 1 | Dir: 0 | Reqs: 0000

Time=395000 | Floor: 1 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0000

--- TC2 ---

Time=455000 | Floor: 1 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 1100

Time=475000 | Floor: 1 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 1100

Time=485000 | Floor: 2 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 1000

Time=495000 | Floor: 2 | Moving: 0 | Door: 1 | Dir: 1 | Reqs: 1000

Time=605000 | Floor: 2 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 1000

Time=655000 | Floor: 3 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 0000

Time=665000 | Floor: 3 | Moving: 0 | Door: 1 | Dir: 1 | Reqs: 0000

--- TC3 ---

Time=765000 | Floor: 3 | Moving: 0 | Door: 1 | Dir: 1 | Reqs: 0011

Time=775000 | Floor: 3 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0011

Time=815000 | Floor: 3 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0011

Time=825000 | Floor: 2 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0011

Time=845000 | Floor: 1 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0001

Time=855000 | Floor: 1 | Moving: 0 | Door: 1 | Dir: 0 | Reqs: 0001

Time=965000 | Floor: 1 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0001

Time=1015000 | Floor: 0 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0000

Time=1025000 | Floor: 0 | Moving: 0 | Door: 1 | Dir: 0 | Reqs: 0000

--- TC4 ---

Time=1075000 | Floor: 0 | Moving: 0 | Door: 1 | Dir: 0 | Reqs: 0010

Time=1135000 | Floor: 0 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0010

Time=1175000 | Floor: 0 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0010

Time=1185000 | Floor: 1 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 0000

Time=1195000 | Floor: 1 | Moving: 0 | Door: 1 | Dir: 1 | Reqs: 0000

Time=1285000 | Floor: 1 | Moving: 0 | Door: 1 | Dir: 1 | Reqs: 0101

Time=1305000 | Floor: 1 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0101

Time=1355000 | Floor: 2 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 0001

Time=1365000 | Floor: 2 | Moving: 0 | Door: 1 | Dir: 1 | Reqs: 0001

Time=1475000 | Floor: 2 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0001

Time=1515000 | Floor: 2 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0001

Time=1525000 | Floor: 1 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0001

Time=1545000 | Floor: 0 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0000

Time=1555000 | Floor: 0 | Moving: 0 | Door: 1 | Dir: 0 | Reqs: 0000

Time=1665000 | Floor: 0 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0000

--- TC5 ---

Time=1695000 | Floor: 0 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 1000

Time=1705000 | Floor: 0 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 1000

Time=1715000 | Floor: 1 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 1000

Time=1725000 | Floor: 1 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 1001

Time=1735000 | Floor: 2 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 1001

Time=1755000 | Floor: 3 | Moving: 1 | Door: 0 | Dir: 1 | Reqs: 0001

Time=1765000 | Floor: 3 | Moving: 0 | Door: 1 | Dir: 1 | Reqs: 0001

Time=1875000 | Floor: 3 | Moving: 0 | Door: 0 | Dir: 1 | Reqs: 0001

Time=1915000 | Floor: 3 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0001

Time=1925000 | Floor: 2 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0001

Time=1945000 | Floor: 1 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0001

Time=1965000 | Floor: 0 | Moving: 1 | Door: 0 | Dir: 0 | Reqs: 0000

Time=1975000 | Floor: 0 | Moving: 0 | Door: 1 | Dir: 0 | Reqs: 0000

Time=2085000 | Floor: 0 | Moving: 0 | Door: 0 | Dir: 0 | Reqs: 0000

--- DONE ---