

GATE EE2016, 11

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1. Consider the following circuit which uses a 2-to-1 multiplexer as shown in the figure below. The Boolean expression for output F in terms of A and B is

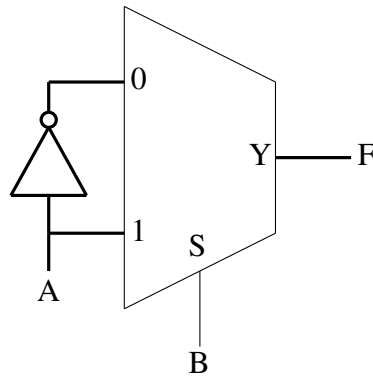


Figure 1

- (a) $A \oplus B$
- (b) $\overline{A + B}$
- (c) $A + B$
- (d) $\overline{A \oplus B}$