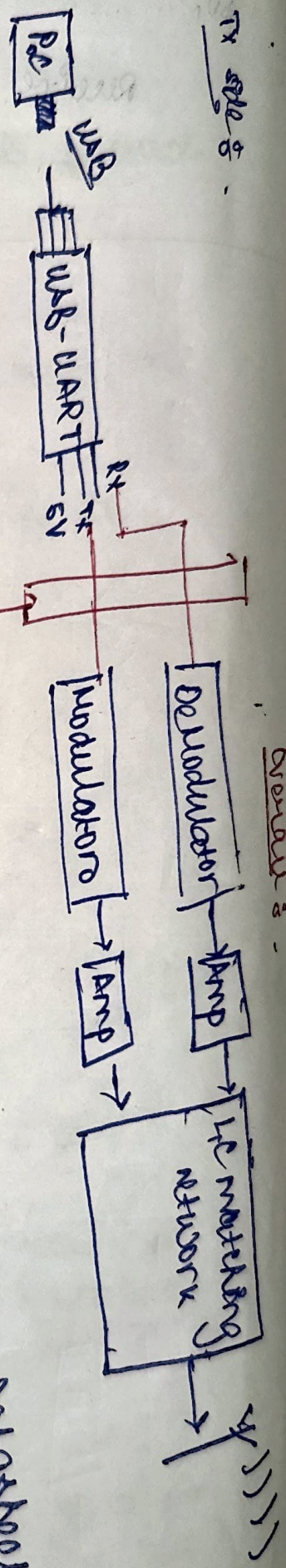


TX side



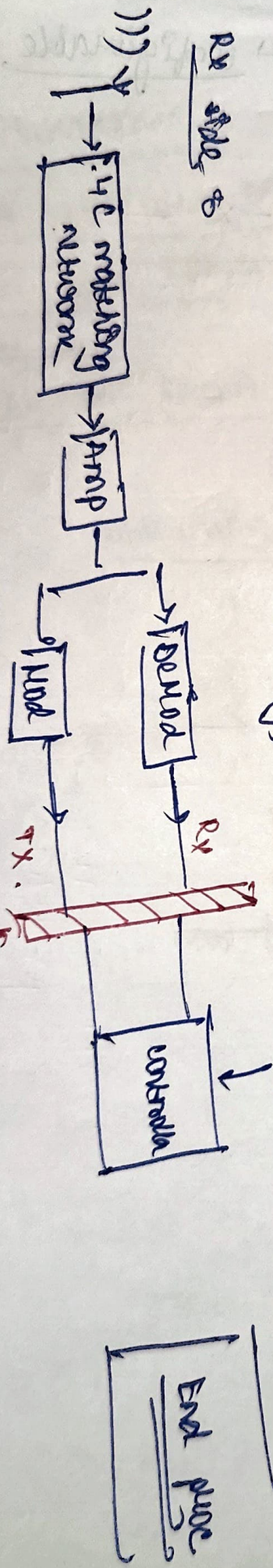
(level) → LP214 IC given in P1.1 datasheet

space for

(not needed actually)

for unreliability.

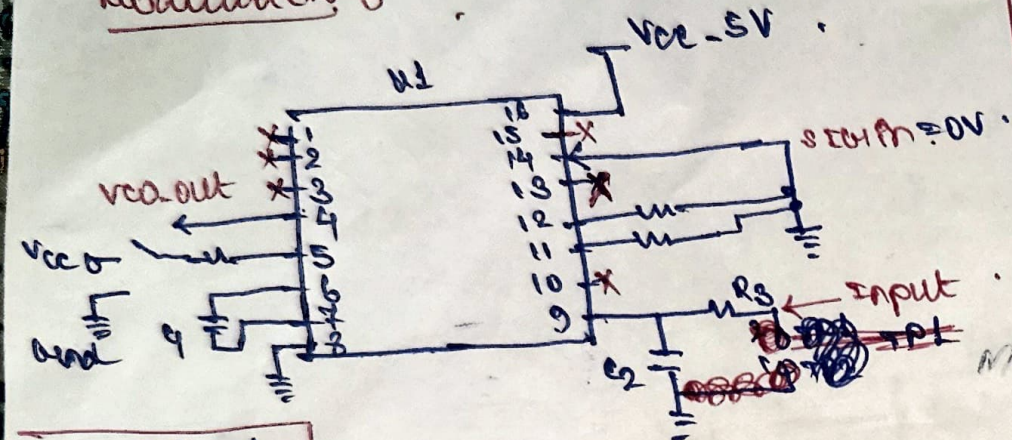
RE side



level offset (if required)

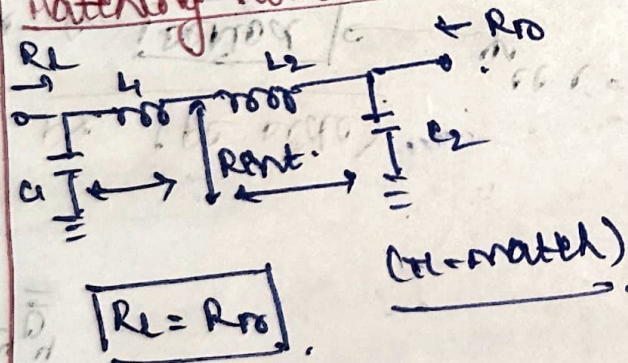
End proc

Modulator 8

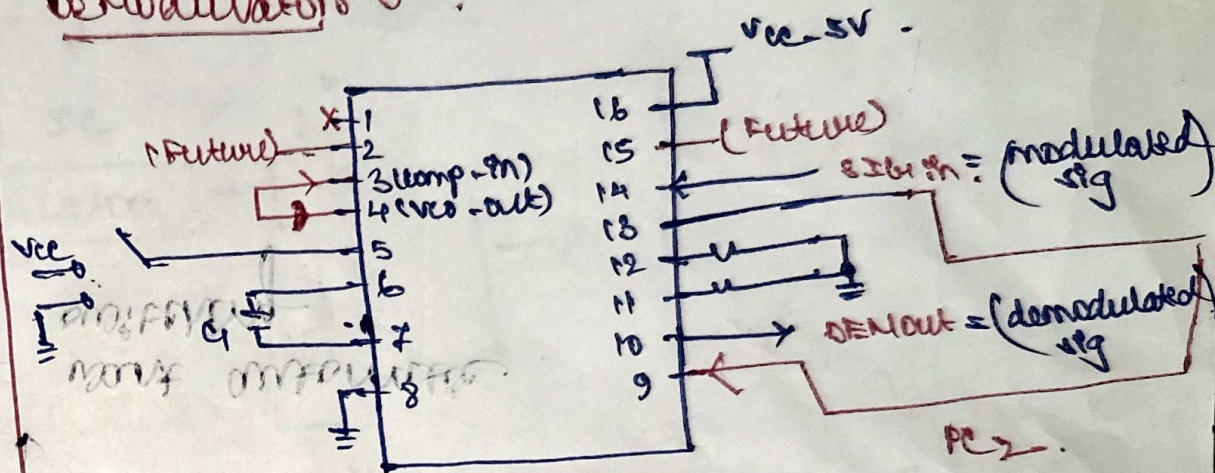


- 1 → PCOUT
- 2 → PCS out
- 3 → comp-in
- 4 → vcc out
- 5 → INH
- 6 → CIA
- 7 → CIB
- 8 → GND
- 9 → vcc in
- 10 → DEMOUT
- 11 → R1
- 12 → R2
- 13 → PCOUT
- 14 → SIGAN
- 15 → PCS out
- 16 → VCC

Matching Network



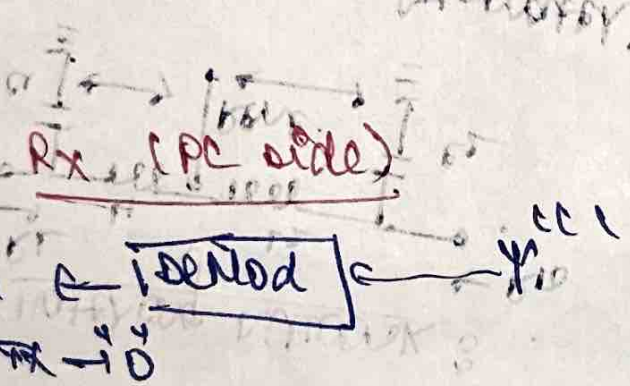
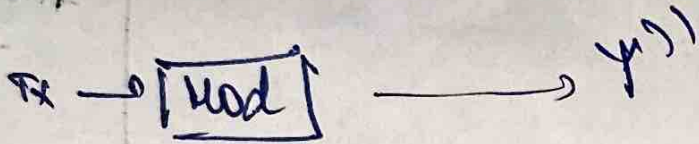
Demodulator 8



Future : Scheme 1 ↔ Scheme 2
shifting for efficient
B.O.W and power

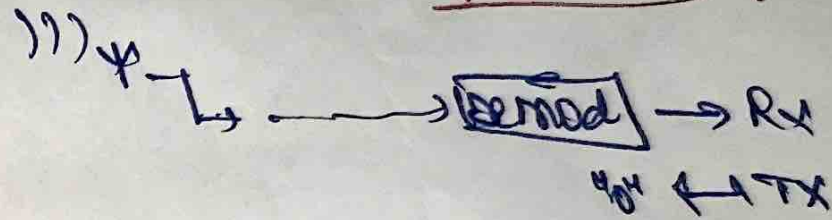
Tx (PC side)

- 1 → 150000
- 2 → 150000
- 3 → 150000
- 4 → 150000
- 5 → 150000
- 6 → 150000
- 7 → 150000
- 8 → 150000
- 9 → 150000
- 10 → 150000
- 11 → 150000
- 12 → 150000
- 13 → 150000
- 14 → 150000
- 15 → 150000
- 16 → 150000
- 17 → 150000
- 18 → 150000
- 19 → 150000
- 20 → 150000
- 21 → 150000
- 22 → 150000
- 23 → 150000
- 24 → 150000
- 25 → 150000
- 26 → 150000
- 27 → 150000
- 28 → 150000
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- 67 → 150000
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- 84 → 150000
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- 87 → 150000
- 88 → 150000
- 89 → 150000
- 90 → 150000
- 91 → 150000
- 92 → 150000
- 93 → 150000
- 94 → 150000
- 95 → 150000
- 96 → 150000
- 97 → 150000
- 98 → 150000
- 99 → 150000
- 100 → 150000

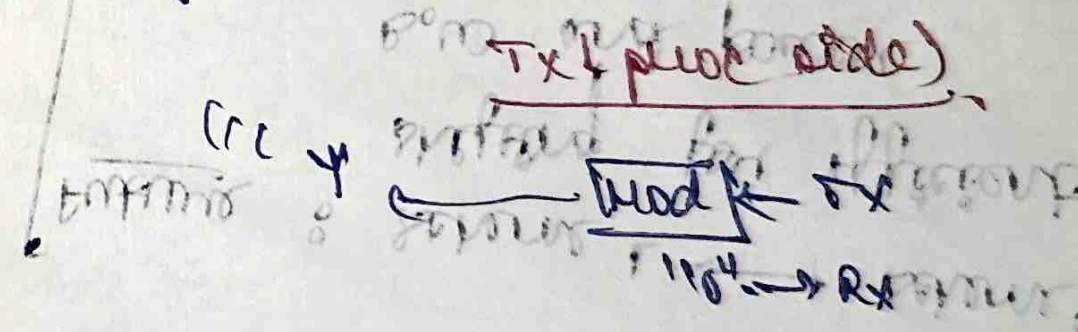


(All taken at some fixed time)

Rx (proc side)



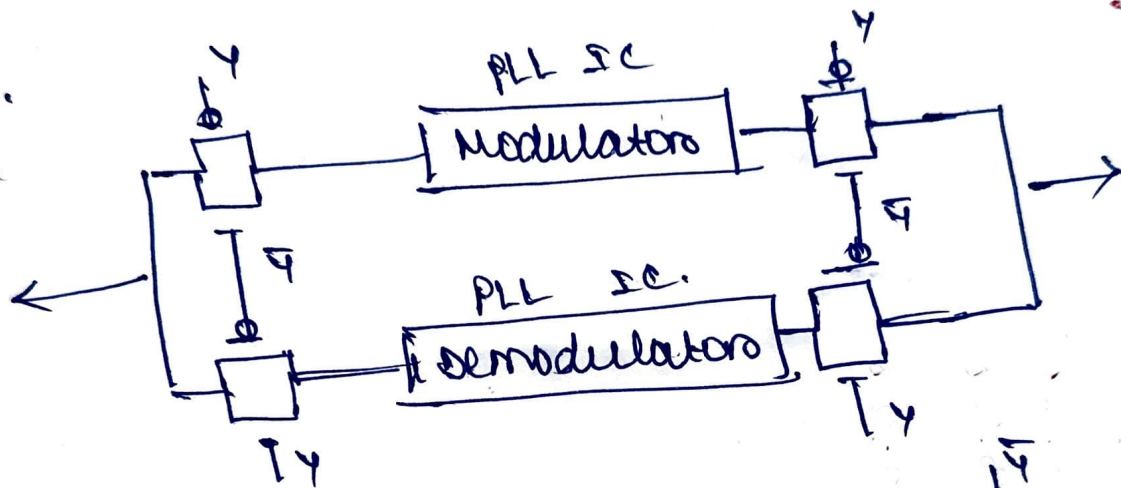
switching mode



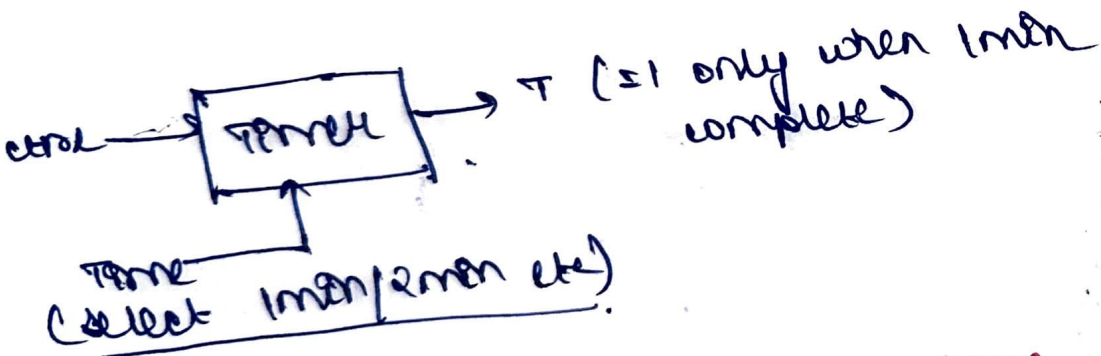
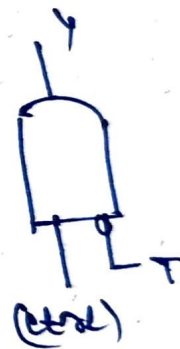
we want automatic switching

switching network

ctrl = 1
 ↳ go for demod on proc side.



→ For proc side



(→ For PC side → same circuit → just $Y \leftrightarrow \bar{Y}$)