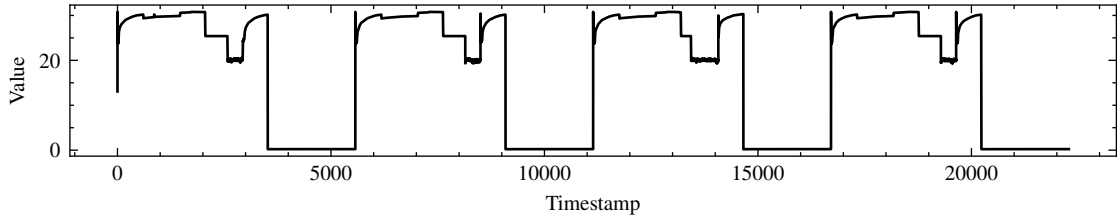
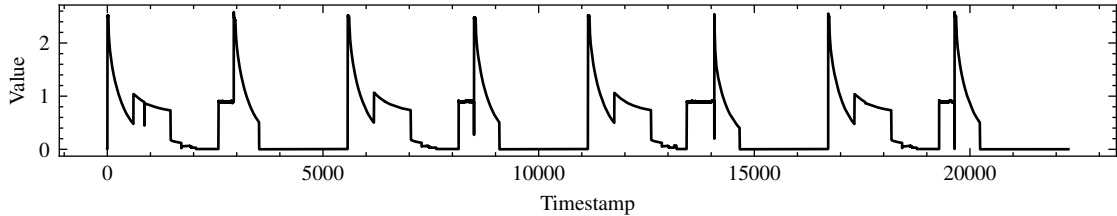


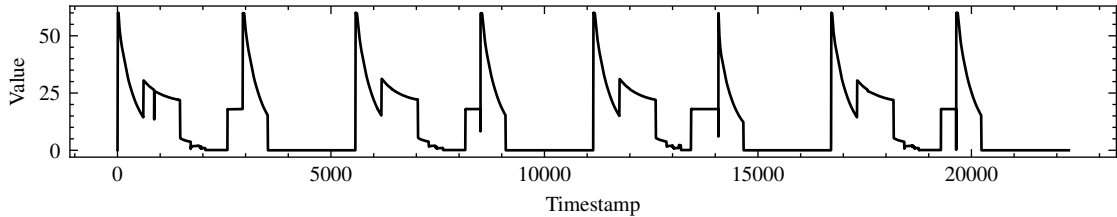
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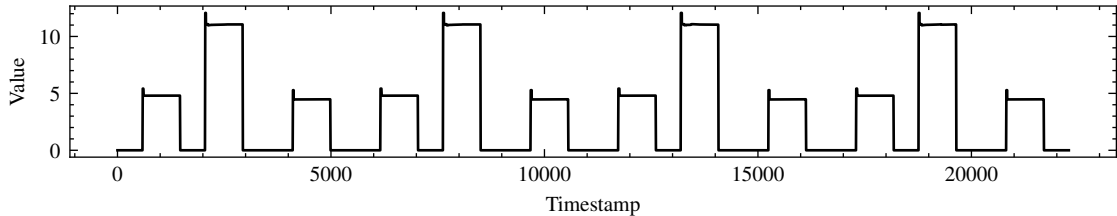
LSA



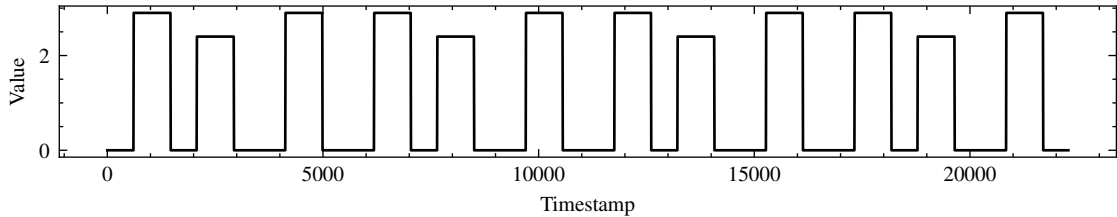
P_SA



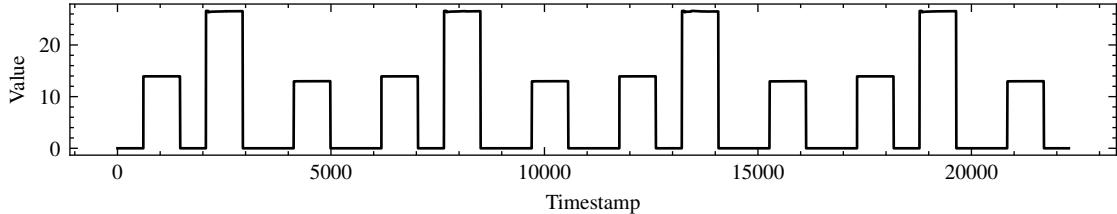
U_Load_output



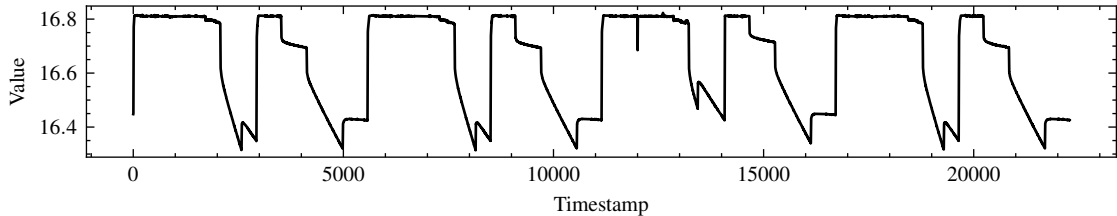
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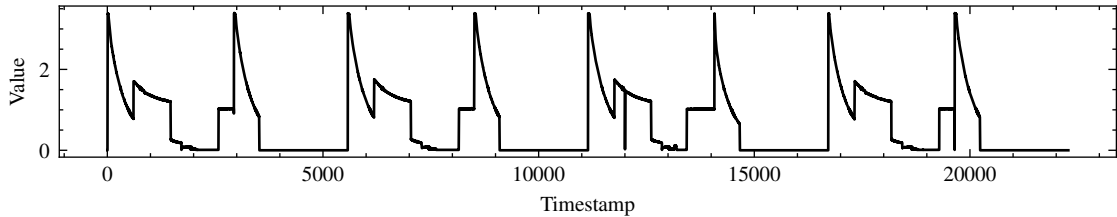
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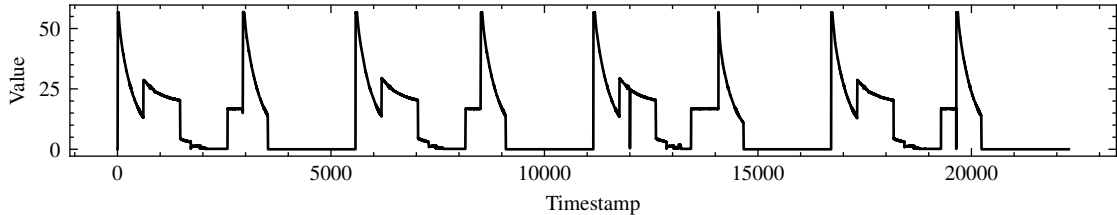
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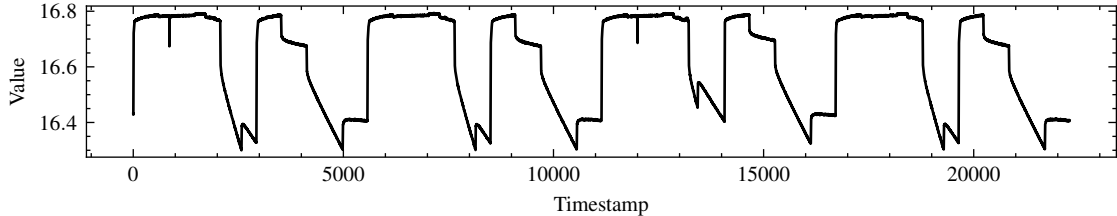
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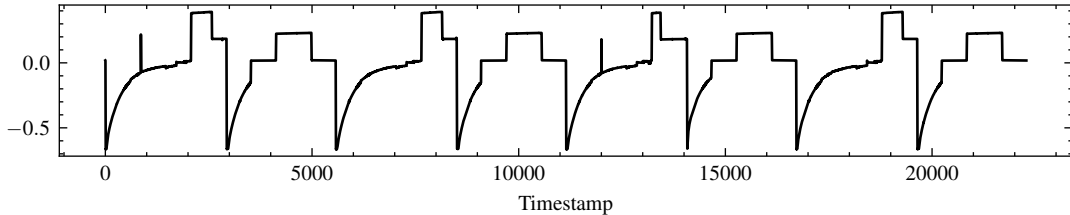
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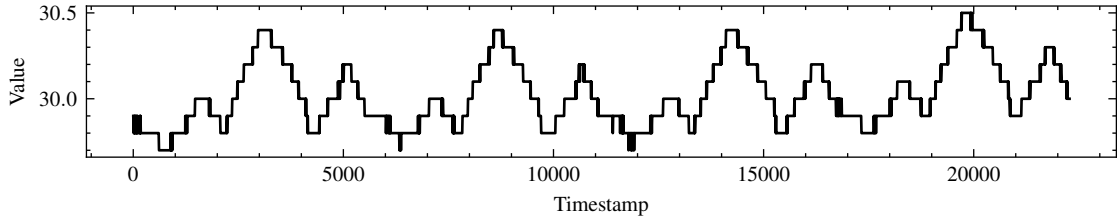
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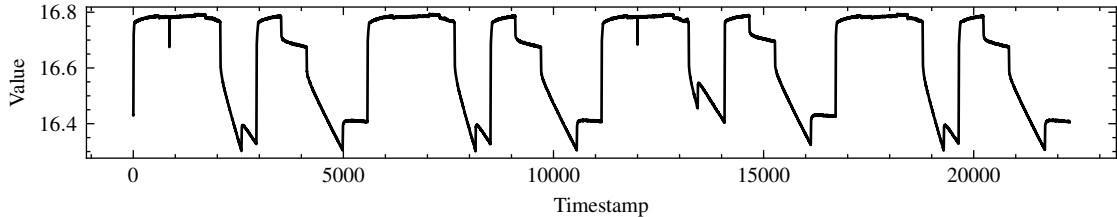
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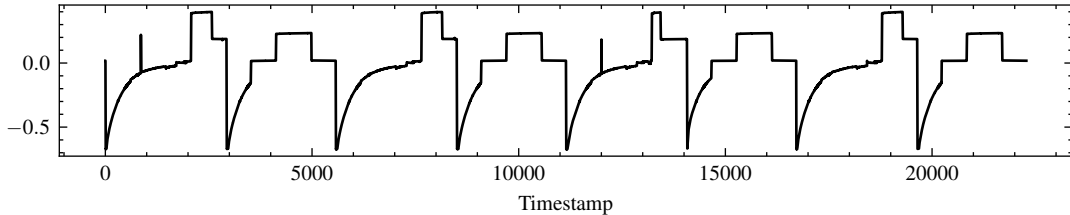
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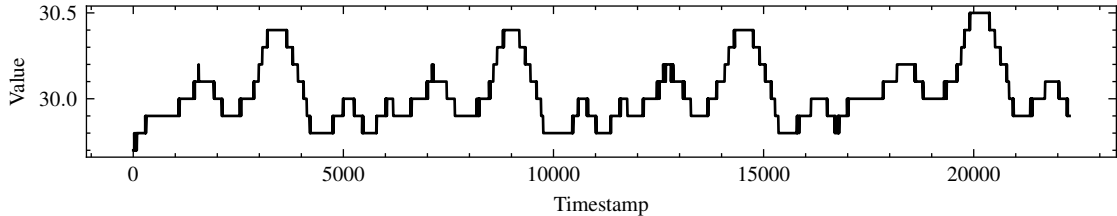
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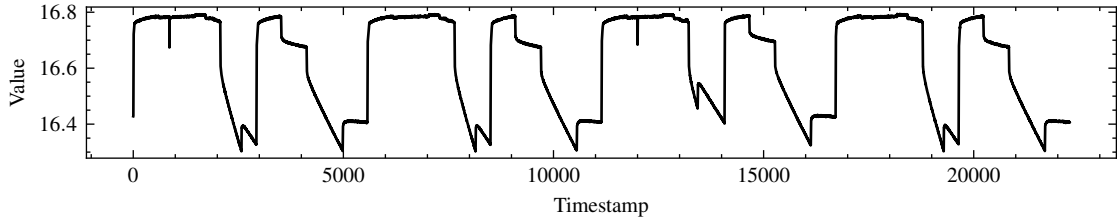
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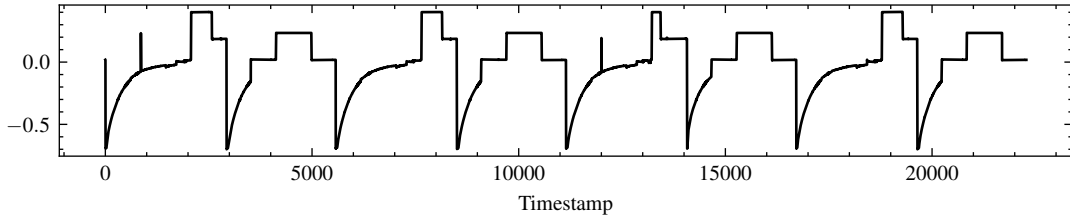
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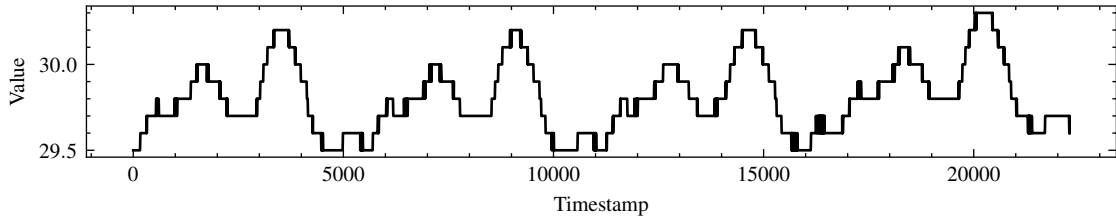
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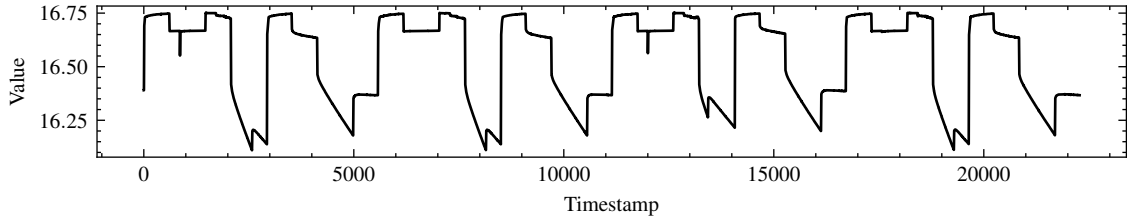
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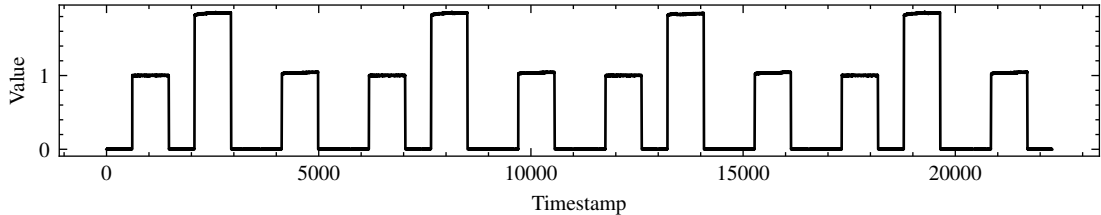
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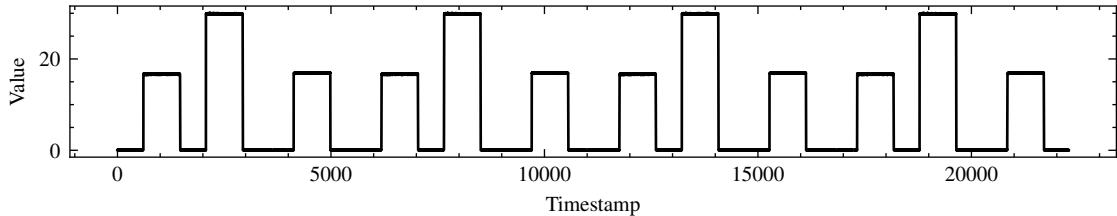
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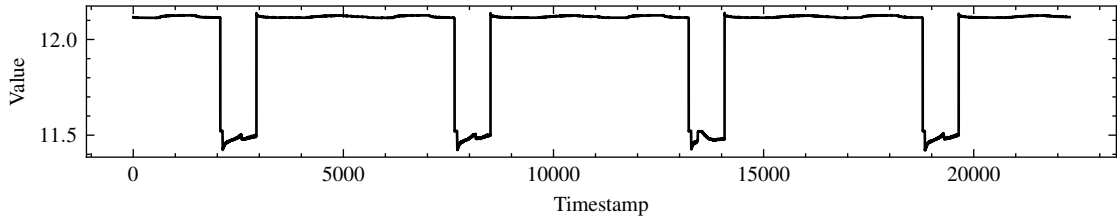
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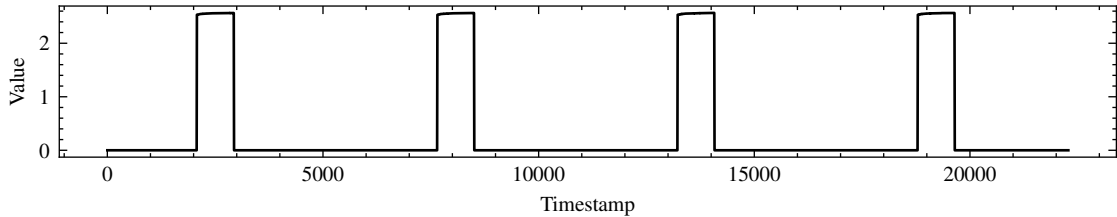
P_Bus



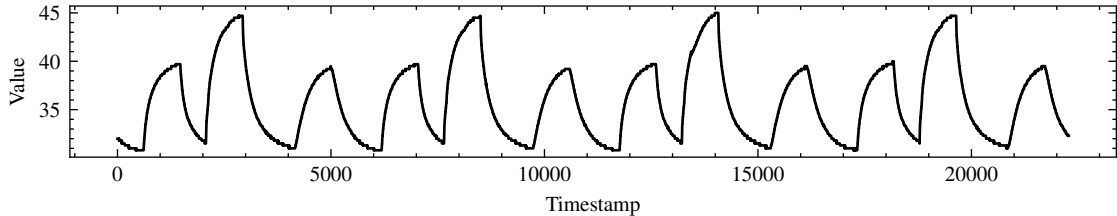
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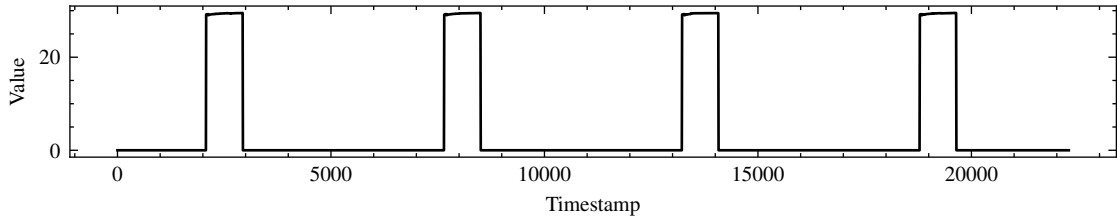
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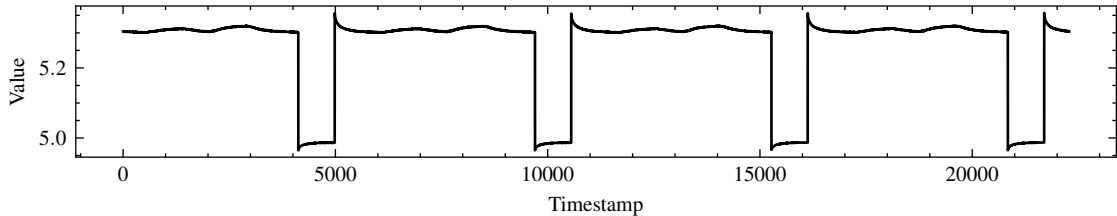
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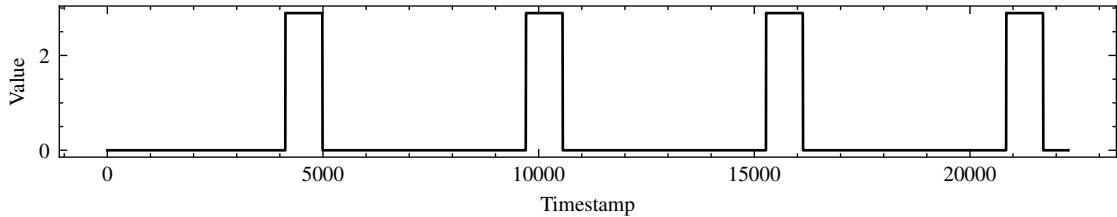
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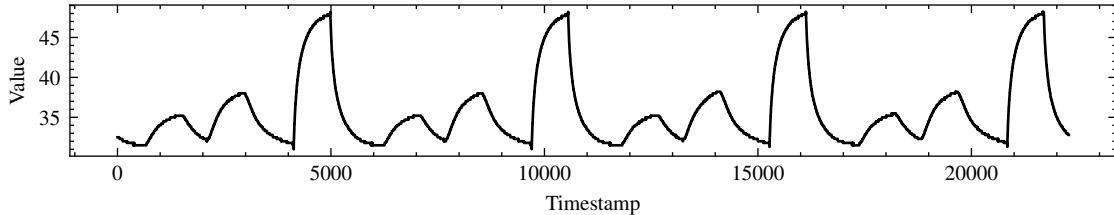
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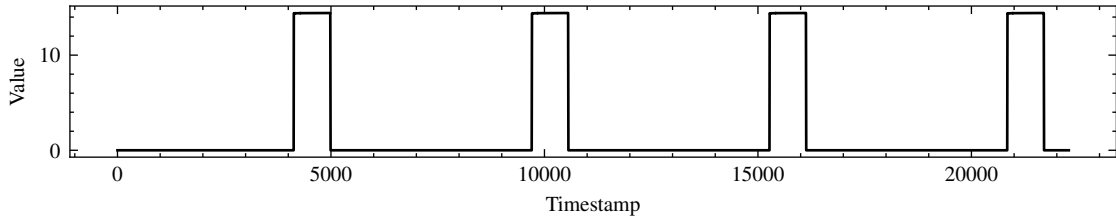
I_Load2



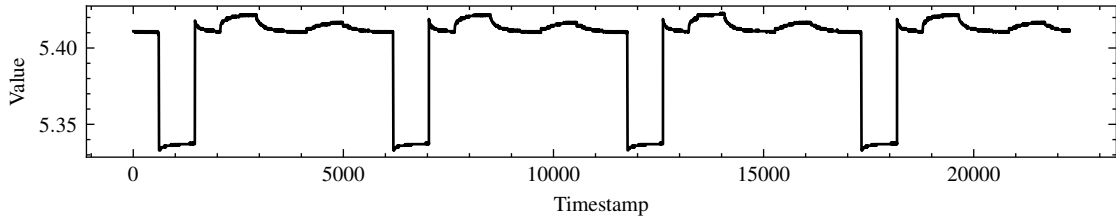
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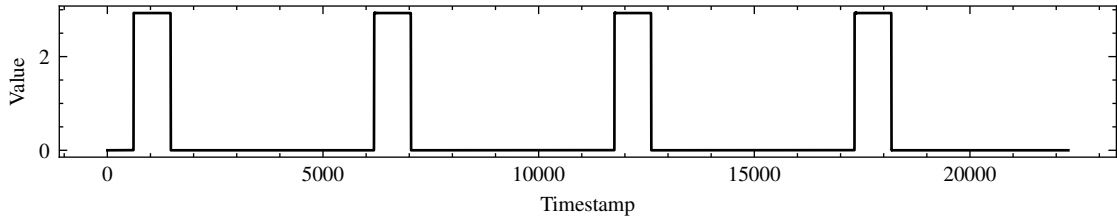
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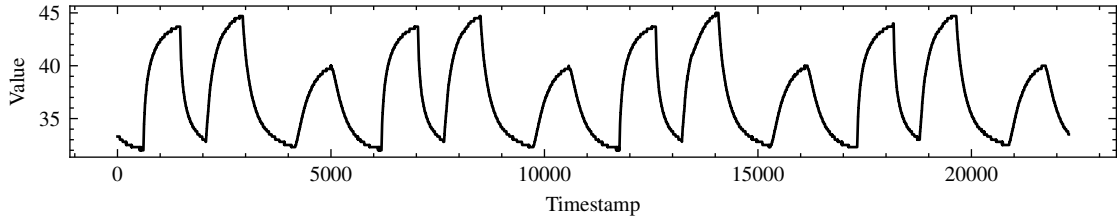
U_Load3



I_Load3



T_Load3



P_Load3

