ToDos:
1. Wie viel Spannung liegt maximal an C20 an? >16V notwendig?
2. Status LED muss an Vreg zu V Batt – und WDT muss mit 1MOhm an Vreg

Using the ADBMS1818 with Fewer than 18 Cells
Cells can be connected in a conventional bottom (C1) to top (C18) sequence with all unused C inputs either shorted to the highest connected cell or left open.
The unused S pins can simply be left disconnected.

Place test points 2cm apart on pcb to prevent shorts

Fragen

Watchdog Timer Output Pin.
This is an open drain negative metal–oxide semiconductor (NMOS) digital output. WDT can be left disconnected or connected with a 1 M resistor to VREG. If the ADBMS1818 does not receive a valid command within 2 seconds, the watchdog timer circuit resets the ADBMS1818 and the WDT pin goes high impedance.

Isolated Interface Current Bias.
Tie IBIAS to V– through a resistor divider to set the interface output current level. When the isoSPI interface is enabled, the IBIAS pin voltage is 2 V.
The IPA and IMA or IPB and IMB output current drive is set to 20 times the current, IB, sourced from the IBIAS pin.

Isolated Interface Comparator Voltage Threshold Set.
Tie ICMP to the resistor divider between IBIAS and V– to set the voltage threshold of the isoSPI receiver comparators. The comparator thresholds are set to half the voltage on the ICMP pin.

mcp6001 op amp für ref spannung aus Vreg
alternativ an Vref 1 o 2 mit 0 ohm wiederstand rauslötbar

https://www.sprut.de/electronic/temeratur/temp.htm

Ic = 72V/100 = 720mA

ggfs nicht notwendig

Versort iC mit Strom wenn nicht im sleep mode?

External resistors RB1 and RB2 create the reference current, IB. This current sets the drive strength of the transmitter.

ISOMD = Serial Interface Mode. Connecting ISOMD to VREG configures Pin 53, Pin 54, Pin 61, and Pin 62 of the ADBMS1818 for 2–wire isoSPI mode.