

Name	Value	1, 000 ns	1, 050 ns	1, 100 ns	1, 150 ns	1, 200 ns	1, 250 ns	1, 300 ns	1, 350 ns	1, 400 ns	1, 450 ns
RegDst	0										
ExtSel	1										
RegWrite	1										
MemWrite	0										
ALUSrcA	0										
ALUSrcB	1										
ALUOp[2:0]	0	6				0		1		0	
MemToReg	0										
Branch	0										
Jump	0										
Zero	0										
PCWrite	1										
currPC[31:0]	00000000	00000020		00000024		00000028		0000002c		00000028	
nextPC[31:0]	00000004	00000024		00000028		0000002c		00000028		0000002c	
instruction[31:0]	08010008	70460004		70c70000		08e70008		c0e1ffff		08e70008	
alu_res[31:0]	00000008	00000001		00000000		00000008	00000010	00000000		00000010	00000018
d1[31:0]	00000000	00000002		00000001		00000000	00000008				
d2[31:0]	00000008	00000004		00000000		00000008					
clk	1										
reset	0										
		slti \$6,\$2,4		slti \$7,\$6,0		addiu \$7,\$7,8		beq \$7,\$1,-2		addiu \$7,\$7,8	
		d1 = Reg[2] =2 d2 = imm =4 alu = d1<d2 =1 Reg[6] <- db =1		d1 = Reg[6] =1 d2 = imm =0 alu = d1<d2 =0 Reg[7] <- db =0		d1 = Reg[7] =0 d2 = imm =8 alu = d1+d2 =8 Reg[7] <- db =8		d1 = Reg[8] =8 d2 = Reg[1] =8 alu= d1-d2 =0 0==0 jump		d1 = Reg[7] =8 d2 = imm =8 alu = d1+d2 =10 Reg[7] <- db =10	