




















Name		Value	1, 500 ns	1, 550 ns	1, 600 ns	1, 650 ns	1, 700 ns	1, 750 ns	1, 800 ns	1, 850 ns	1, 900 ns	1, 950 ns
 RegDst		0										
 ExtSel		1										
 RegWrite		1										
 MemWrite		0										
 ALUSrcA		0										
 ALUSrcB		1										
>  ALUOp[2:0]		0	1		0							
 MemToReg		0										
 Branch		0										
 Jump		0										
 Zero		0										
 PCWrite		1										
>  currPC[31:0]		00000000	0000002c		00000030		00000034		00000038		0000003c	
>  nextPC[31:0]		00000004	00000030		00000034		00000038		0000003c		00000040	
>  instruction[31:0]		08010008	c0e1ffff		98220004		9c290004		080afffe		094a0001	
>  alu_res[31:0]		00000008	00000008		0000000c				fffffffe		ffffffff	00000000
>  d1[31:0]		00000000	00000010				00000008		00000000		fffffffe	
>  d2[31:0]		00000008	00000008				00000004		fffffffe		00000001	
 clk		1										
 reset		0										
			beq \$7,\$1,-2		sw \$2,4(\$1)		lw \$9,4(\$1)		addiu \$10,\$0,-2		addiu \$10,\$10,1	
			d1 = Reg[7] =10 d2 = Reg[1] =8 alu= d1-d2 =2		d1 = Reg[1] =8 d2 = imm =4 alu = d1+d2 =C Mem[12] <- db =Reg[2]=2		d1 = Reg[1] =8 d2 = imm =4 alu = d1+d2 =C Reg[9] <- db =Mem[12]=2		d1 = Reg[0] =0 d2 = imm =-2 alu = d1+d2 =-2 Reg[10] <- db=-2		d1 = Reg[10] =-2 d2 = imm =1 alu = d1+d2 =-1 Reg[10] <- db =-1	