Name	Value
RegDst	0
ExtSel	1
RegWrite	1
MemWrite	0
ALUSrcA	0
ALUSrcB	1
ALUOp[2:0]	0
MemToReg	0
Branch	0
Jump	0
Zero	0
PCWrite	1
currPC[31:0]	00000000
nextPC[31:0]	00000004
instruction[31:0]	08010008
alu_res[31:0]	00000008
d1[31:0]	00000000
d2[31:0]	00000008
clk	1
reset	0