## SUPPLEMENTARY MATERIAL

# Thermal Image Super-Resolution Challenge - PBVS 2021

Rafael E. Rivadeneira, Angel D. Sappa, Boris X. Vintimilla,
Sabari Nathan, Priya Kansal, Armin Merhi, Parichehr Behjati Ardakani,
Anurag Dalal, Aparna Akula, Darshika Sharma, Shashwat Pandey, Basant Kumar,
Jiaxin Yao, Rongyuan Wu, Kai Feng, Ning Li, Yongqiang Zhao,
Heena Patel, Vishal Chudasama, Kalpesh Prajapati, Anjali Sarvaiya,
Kishor P. Upla, Raghavendra Ramachandra, Kiran. Raja, Christoph Busch,
Feras Almasri, Thomas Vandamme, Olivier Debeir, Nolan B. Gutierrez, Quan H. Nguyen, William J. Beksi

### 1. Introduction

This document presents the eleven architectures proposed by the nine teams that participated in the second Thermal Image Super-Resolution challenge. The challenge has been organized in the framework of the PBVS 2021 workshop, CVPR 2021.

## 2. TISR 2021 Teams and Affiliations

**Members:** Rafael Rivadeneira<sup>1</sup> (rrivaden@espol.edu.ec), Angel Sappa<sup>1,2</sup> and Boris Vintimilla<sup>1</sup>

**Affiliations:** <sup>1</sup>Escuela Superior Politécnica del Litoral, ESPOL, Facultad de Ingeniería en Electricidad y Computación, CIDIS, Campus Gustavo Galindo Km. 30.5 Vía Perimetral, P.O. Box 09-01-5863, Guayaquil, Ecuador. <sup>2</sup>Computer Vision Center, Bellaterra, Barcelona, Spain.

#### A.1. COUGER AI:

**Members:** Sabari Nathan (sabari@couger.co.jp) and Priya Kansal.

Affiliation: Couger Inc, Japan.

#### **A.2. CVC**:

**Members:** Armin Mehri (amehri@cvc.uab.es) and Parichehr Behjati Ardakani.

**Affiliation:** Computer Vision Center, Campus UAB, Bellaterra, Barcelona, Spain.

## A.3. ISESL-CSIO:

**Members:** Anurag Dalal<sup>1,2</sup> (anurag.dalal59@gmail.com) and Aparna Akula<sup>2</sup>.

**Affiliations:** <sup>1</sup>Central Scientific Instruments Organization, Chandigarh; <sup>2</sup>Indian Institute of Engineering Science and Technology, Shibpur.

#### A.4. MNNIT:

**Members:** Darshika Sharma (darshika@mnnit.ac.in), Shashwat Pandey and Basant Kumar.

Affiliation: MNNIT Allahabad, Prayagraj, India.

#### A.5. NPU-MPI-LAB:

**Members:** Jiaxin Yao, Rongyuan Wu (rongyuanwu@mail.nwpu.edu.cn), Kai Feng, Ning Li and Yongqiang Zhao.

Affiliation: Northwestern Polytechnical University.

#### A.6. SVNIT NTNU:

**Members:** Heena Patel<sup>1</sup> (hpatel1323@gmail.com), Vishal Chudasama<sup>1</sup>, Kalpesh Prajapati<sup>1</sup>, Anjali Sarvaiya<sup>1</sup>, Kishor P. Upla<sup>1</sup>, Raghavendra Ramachandra<sup>2</sup>, Kiran. Raja<sup>2</sup> and Christoph Busch<sup>2</sup>.

**Affiliations:**  $^{1}$ SVNIT, Surat, India;  $^{2}$ NTNU, Gjøvik, Norway.

## A.7. ULB-LISA:

**Members:** Feras Almasri (falmasri@ulb.ac.be), Thomas

Vandamme and Olivier Debeir.

Affiliation: Universite Libre de Bruxelles, Belgium.

## **A.8. UTA-RVL-1:**

Members: Nolan B. Gutierrez (nolan.gutierrez@mavs.uta.edu) and William J. Beksi. **Affiliation:** University of Texas at Arlington, United States.

#### **A.9. UTA-RVL-2:**

Members: Quan H. Nguyen (quan.nguyen4@mavs.uta.edu) and William J. Beksi.

Affiliation: University of Texas at Arlington, United States.

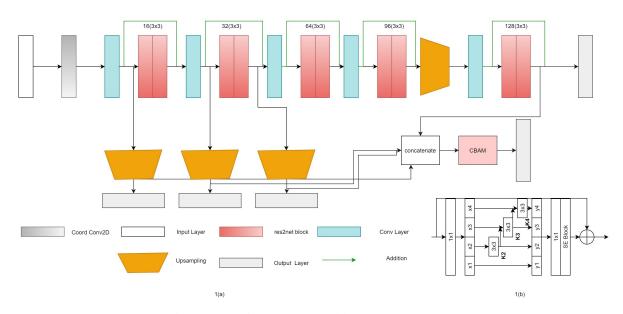


Figure 1: Architecture proposed by COUGER AI team.

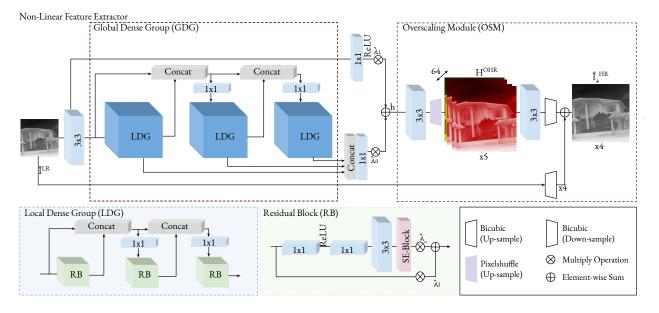


Figure 2: Architecture proposed by CVC team.

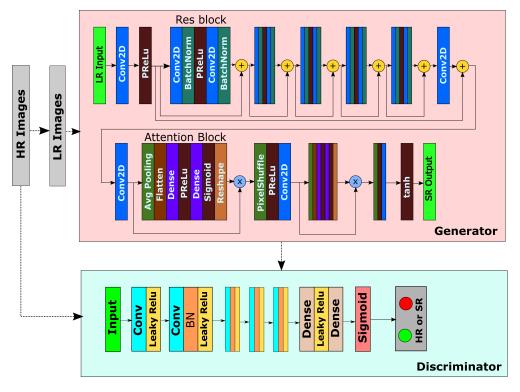


Figure 3: Architecture proposed by ISESL-CSIO team.

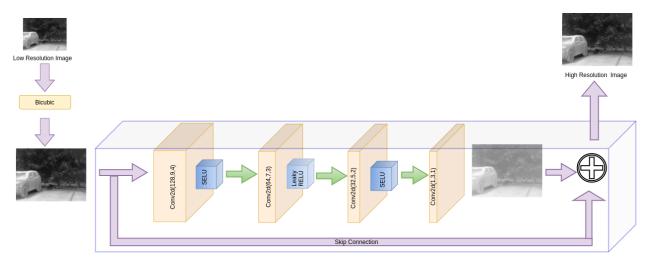


Figure 4: Architecture proposed by MNNIT team.

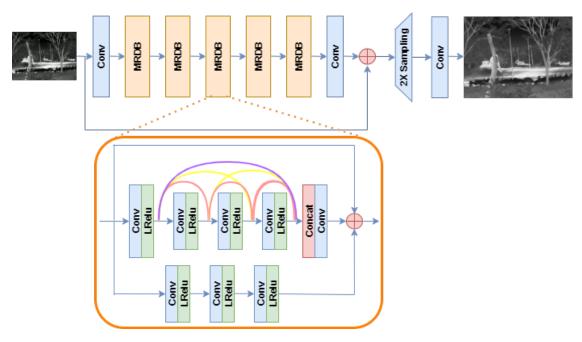


Figure 5: Architecture proposed by NPU-MPI-LAB team for evaluation 1.

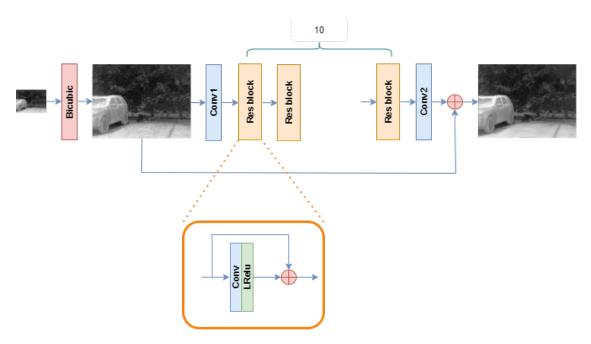
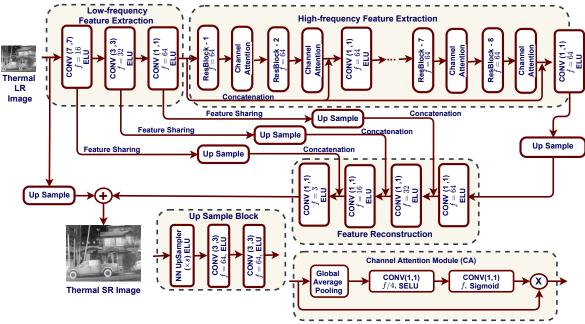
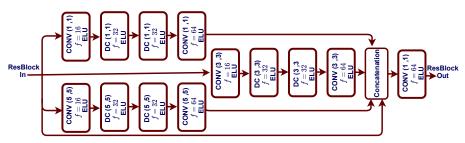


Figure 6: Architecture proposed by NPU-MPI-LAB team for evaluation 2.



(a) The block schematic of the proposed architecture for scaling factors  $\times 4$  and  $\times 2$  (i.e., Track-1 & Track-2)



(b) The design of the ResBlock used in the proposed model.

Figure 7: First architecture proposed by SVNIT\_NTNU team (winner at evaluation 1).

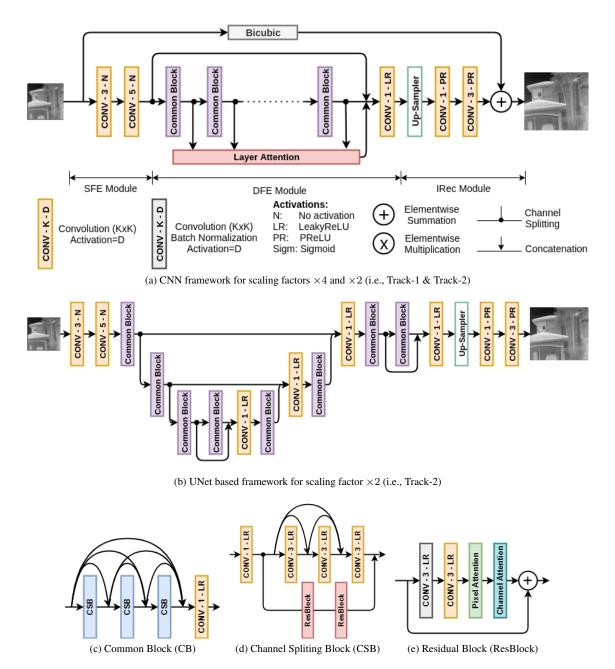


Figure 8: Second architecture proposed by SVNIT\_NTNU team.

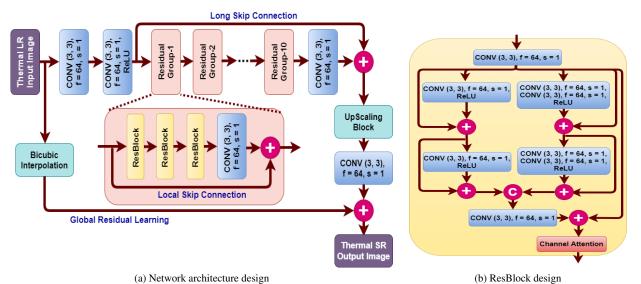
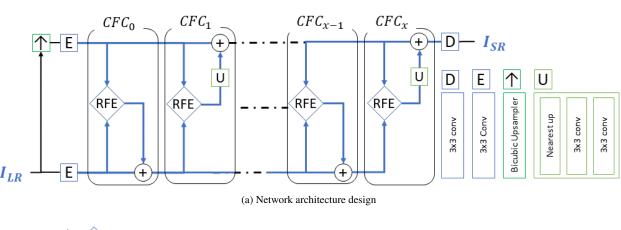


Figure 9: Third architecture proposed by SVNIT\_NTNU team.



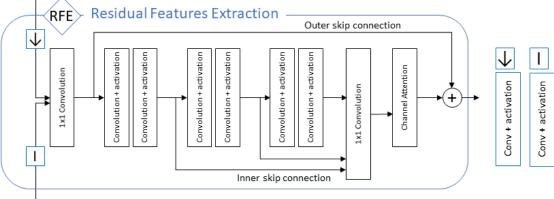


Figure 10: Architecture proposed by ULB-LISA team (winner at evaluation 2).

(b) Residual Feature Extraction Module

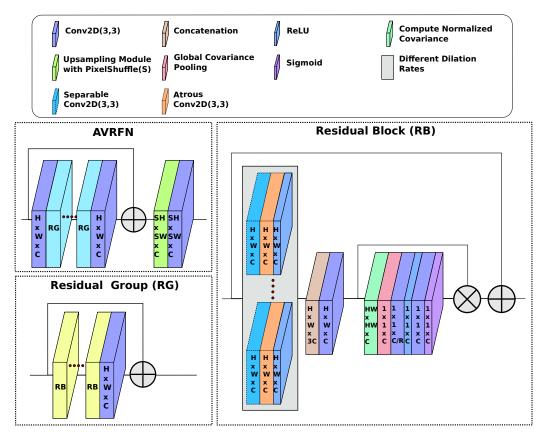


Figure 11: Architecture proposed by RVL-UTA-1 team.

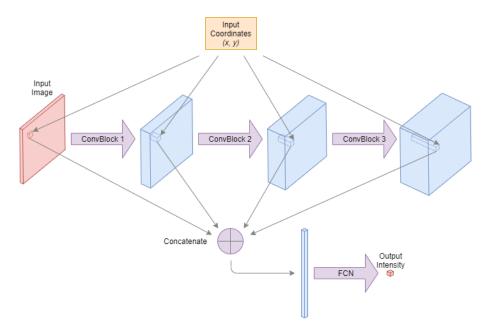


Figure 12: Architecture proposed by RVL-UTA-2 team.