

MegaScale-MoE: Large-Scale Communication-Efficient Training of Mixture-of-Experts Models in Production

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Abstract

We present MegaScale-MoE, a production system tailored for the efficient training of large-scale mixture-of-experts (MoE) models. MoE emerges as a promising architecture to scale large language models (LLMs) to unprecedented sizes, thereby enhancing model performance. However, existing MoE training systems experience a degradation in training efficiency, exacerbated by the escalating scale of MoE models and the continuous evolution of hardware.

Recognizing the pivotal role of efficient communication in enhancing MoE training, MegaScale-MoE customizes communication-efficient parallelism strategies for attention and FFNs in each MoE layer and adopts a holistic approach to overlap communication with computation at both inter- and intra-operator levels. Additionally, MegaScale-MoE applies communication compression with adjusted communication patterns to lower precision, further improving training efficiency. When training a 352B MoE model on 1,440 NVIDIA Hopper GPUs, MegaScale-MoE achieves a training throughput of 1.41M tokens/s, improving the efficiency by $1.88\times$ compared to Megatron-LM. We share our operational experience in accelerating MoE training and hope that by offering our insights in system design, this work will motivate future research in MoE systems.

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1 Introduction

As the size of Large Language Models (LLMs) [7, 16, 47] grow, so does the scale of their training regimes. The escalation in training scale has made efficiency improvements not just desirable but crucial [17]. As a company building AI products for billions of users, we remain committed to training LLMs with hundreds of billions of parameters on thousands of GPUs. Consequently, even marginal gains in training efficiency can significantly reduce computational resource consumption and training time, directly influencing the feasibility and sustainability of developing state-of-the-art LLMs.

Within the landscape of LLM architectures, Mixture-of-Experts (MoE) models stand out for their sparse activation [7, 9, 16, 44], which dynamically routes input tokens to a selected set of specialized network components, known as experts, rather than to all parameters. This design leads to sub-linear scaling of FLOPs required as the model size increases, thereby significantly reducing the computational cost. Recent industrial advancements [2, 3, 8, 25, 40] have demonstrated the potential of MoE models, achieving an order-of-magnitude reduction in training cost compared to dense models with equivalent model quality.

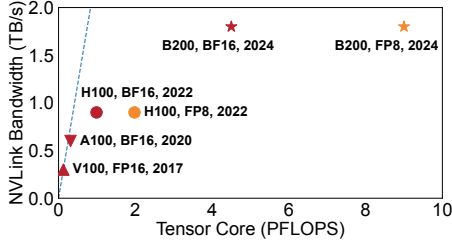


Figure 1 Evolution of NVIDIA GPUs.

Despite the lower training costs of MoE models, we observe a **critical performance bottleneck** during training from a systems perspective—**communication**. For instance, when training an internal model on NVIDIA Hopper GPUs, communication accounts for 43.6% of the total time during the forward pass and **32% over the entire training process**. Two primary factors contribute to this bottleneck. First, MoE models inherently **introduce more communication overhead**. Compared to dense model training, MoE model training requires distribution across more GPUs for model parallelism due to its **larger parameter size**. Second, enabling sparse computation requires **two extra all-to-all communications** in both the forward and backward passes to dispatch and aggregate tokens, respectively, which hinders ongoing computation.

Moreover, **as hardware advances, the imbalance between computation and communication becomes increasingly pronounced**, with communication overhead growing more dominant. Alongside improvements in model architectures, hardware capabilities have evolved rapidly, with GPUs achieving significantly higher processing speeds (Figure 1). Concurrently, reductions in training precision have been adopted to enhance efficient and cost-effective training [25, 36]. These trends lead to a scenario where the raw computation time decreases, making the relative impact of communication overhead a more critical bottleneck. For instance, simply extending existing tensor parallelism to multi-node setups has been observed to push communication overhead beyond 50% in certain cases. As a result, **optimizing communication is essential for sustaining and improving the scalability of MoE model training**, particularly in distributed environments where frequent data synchronization across multiple GPUs is required.

In this paper, we present the **design, implementation, and operational experience of MegaScale-MoE**, a production system optimized for efficient large-scale MoE training. By meticulously addressing the communication bottleneck, MegaScale-MoE strives to push the boundaries of MoE training, achieving significant improvements in performance and efficiency.

MegaScale-MoE addresses the communication problem in MoE training from three key aspects. First, MegaScale-MoE **reduces the communication volume by customizing parallelism strategies for the attention and FFN modules in each MoE layer**. We compare the parallelism strategies in existing LLM training frameworks, comprehensively considering their impact on large-scale training, including the **communication volume and whether communication can be effectively overlapped** (i.e., whether it lies on the critical path). Based on this analysis, we **select the optimal combination of parallelism strategies for MoE training**.

Second, MegaScale-MoE **fully overlaps communication with computation at the operator level**. MegaScale-MoE **partitions** the forward and backward passes of each MoE layer into distinct computation and communication operators. For inter-operator overlap, MegaScale-MoE employs a **holistic scheduling strategy** that carefully **reorders communication and computation operators** during both forward and backward propagation, hiding communication within independent computations. This approach also optimizes GPU memory usage. MegaScale-MoE utilizes **selective activation rematerialization**, retaining only a subset of activations in GPU memory during the forward pass, and **recomputing or re-communicating to obtain the required activations** during the backward pass. With this holistic scheduling, MegaScale-MoE effectively hides the rematerialization overhead, achieving comparable performance while storing only half of the activations.

To overlap communication on the critical paths, MegaScale-MoE employs a **fine-grained approach** that **splits communication into tiles and aligns with the GPU compute pattern**, fusing these tile-level communications into the compute kernels. For MoE models with token dispatch, MegaScale-MoE **fuses an efficient local scatter operation into the kernel** and reorganizes the computation tasks along the scattered dimension to mitigate communication bottlenecks from multiple data sources. This fine-grained overlap occurs within each node, leveraging the high-bandwidth connectivity between GPUs.

Third, MegaScale-MoE leverages **communication compression** to further enhance MoE training efficiency. Specifically, for widely-used BF16 mixed-precision training, MegaScale-MoE **reduces the inter-node parameter synchronization precision from FP32 to BF16**, halving the associated overhead. In FP8 training, MegaScale-MoE replaces BF16 reduce-scatter with FP8 communication, incorporating tai-

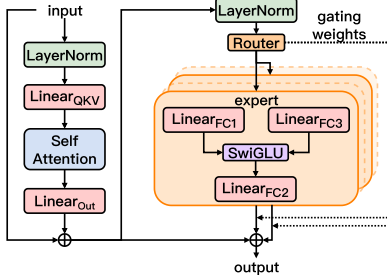


Figure 2 Mixture-of-Experts (MoE) layer.

lored quantization strategies and FP32 reduction to decrease communication volume while preserving convergence stability.

MegaScale-MoE is deployed in our datacenters to train MoE models for our products. Compared to the state-of-the-art open-source LLM training framework, Megatron-LM [46], MegaScale-MoE achieves up to $1.88\times$ higher MFU (Model FLOPs Utilization) when training a 352B MoE model on 1,440 NVIDIA Hopper GPUs. With comprehensive communication optimizations, MegaScale-MoE powers large-scale training in our production, efficiently scaling to trillions of parameters and thousands of GPUs while saving millions of GPU hours.

2 Background

2.1 Mixture-of-Experts for Transformer

The Mixture of Experts (MoE) mechanism is an advanced approach designed to boost the performance of Transformer [49] models, which are increasingly pivotal in the realm of LLMs [2, 7, 16, 25]. It extends the Transformer architecture by integrating multiple expert networks within the feed-forward network (FFN) component. As illustrated in Figure 2, MoE models dynamically route input tokens to the most relevant experts based on their characteristics. This routing is managed by a trainable gating mechanism that selects the best-suited experts for each token. This architectural innovation enables MoE models to scale in capacity without a proportional increase in inference costs, as only a subset of experts is activated for each input.

2.2 Large-scale LLM Training

Training large language models at scale on tens of thousands of GPUs is a complex system engineering challenge that requires multiple systems techniques. To distribute the training workload, a combination of parallelism strategies such as data, tensor, and pipeline parallelism is necessary [17, 41, 46], as each

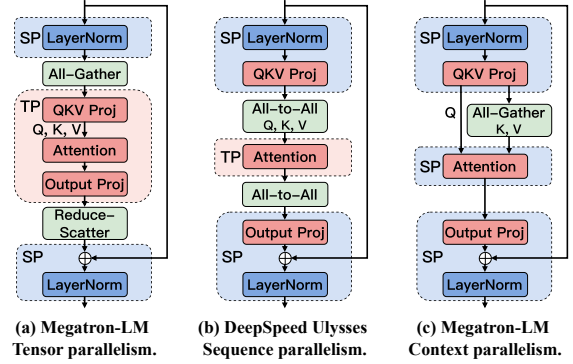


Figure 3 Different parallelism strategies for self-attention. "TP" denotes partitioning along the dimension of hidden size, while "SP" denotes partitioning along the dimension of sequence length.

approach has limitations that prevent relying on a single method for effective scaling.

Data parallelism uniformly distributes the training data across all devices, with each device replicating the model parameters and optimizer states. To synchronize the parameters after each training iteration, data parallelism performs an all-reduce communication operation. Zero Redundancy Optimizer (ZeRO) [38] improves over data parallelism by distributing model states across all participating devices. ZeRO unfolds across three progressive stages, each designed to increasingly conserve memory, though this comes with the trade-off of elevated communication.

Tensor parallelism distributes compute-intensive tensor operations over multiple devices, enabling parallel computation and significantly accelerating the training process. The specific partitioning strategy and the dependencies among operators within the model dictate that tensor parallelism may necessitate gathering split inputs (all-gather) or merging outputs (reduce-scatter). In LLM training, operators like LayerNorm and Dropout, though less compute-intensive, require substantial activation memory. To tackle this problem, a variant of tensor parallelism known as **sequence parallelism** [18] is proposed, which partitions these operators along the dimension of sequence length. For long-context training, several works [1, 14, 46] apply sequence parallelism or tensor parallelism to different operators in self-attention. Figure 3 illustrates the mainstream parallelism strategies for attention, namely tensor, sequence, and context parallelism (TP, SP, and CP), which we analyze in §3.1.

Pipeline parallelism enhances efficiency by dividing

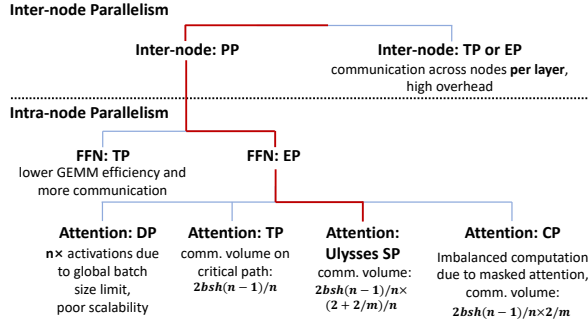


Figure 4 Design space for large-scale MoE training.

model layers into stages that are processed on different devices, enabling pipelined execution. Each batch is split into several micro-batches for this purpose. To minimize pipeline bubbles, various scheduling strategies have been developed, e.g., GPipe [12], PipeDream 1F1B [31] and Interleaved 1F1B [32], etc. Megatron-LM adopts Interleaved 1F1B pipeline scheduling, further dividing each stage on one device into multiple virtual stages to reduce the pipeline bubble rate.

Expert parallelism is tailored for training MoE models by distributing experts across multiple devices, alleviating memory pressure and enabling parallel processing. To efficiently assign tokens to the appropriate experts and retrieve their outputs, all-to-all communication is typically employed.

3 Communication-Efficient Parallelism

With the rise of MoE models and the evolution of hardware compute capabilities, communication overhead has become increasingly critical in MoE training in production. In this section, we delve into the parallelism strategies employed to reduce communication volume and meet other training requirements, such as high GEMM (General Matrix Multiplication) efficiency.

Figure 4 shows the design space of parallelism strategies for large-scale MoE training, excluding the outermost data parallelism. We start with **inter-node parallelism**. **Expert parallelism** alleviates memory pressure from MoE models’ large parameter size by distributing experts across nodes but incurs per-layer cross-node communication, harming training efficiency. Similarly, **tensor parallelism**’s high communication overhead makes it more efficient to limit TP to a single node. Following prior work [17], we adopt **pipeline parallelism** to distribute model parameters,

Symbol	Description
b	micro-batch size
s	sequence length
h	hidden dimension size
n	model parallelism (TP, SP, or EP) size
m	the ratio between the number of query heads and that of key-value heads
k	number of experts that each token is routed to

Table 1 Description of symbols.

reduce communication, and overlap communication of different micro-batches.

Prior large-scale MoE training systems, such as Megatron-LM [46] and DeepSpeed-MoE [40], incorporate tensor parallelism to scale up training by partitioning the model parameters within the node. However, in our practice, we observe two issues with this approach: (1) **TP partitions the expert dimension, which negatively impacts GEMM efficiency**; and (2) **TP introduces significant communication overhead, which remains constant as the parallelism size increases, eventually causing communication to exceed computation** on modern hardware.

To address these issues, we tailor parallelism strategies for MoE model components. For **feed-forward networks** (i.e., experts), we **replace tensor parallelism with expert parallelism and use custom communication modes optimized for varying top-k and expert sizes**, ensuring communication overhead stays lower than tensor parallelism. For other components, we **apply sequence parallelism**, partitioning along the sequence dimension instead of the batch dimension, allowing **scaling without increasing global batch size**. This also **reduces communication on critical paths compared to tensor parallelism**. The additional memory and DP communication overhead remain manageable due to the parameter asymmetry across components. We detail the rationale and analysis of this intra-node parallelism strategy in the following sections. Table 1 lists the key symbols.

3.1 Sequence Parallelism for Attention

Due to the inherent parallelizability of the expert components in MoE models, most prior work on MoE training [20, 40] focuses on optimizing expert parallelism, while data parallelism (DP) is typically applied to the non-MoE components such as **attention**. However, when scaling up MoE training, this approach proves **insufficient due to the $n \times$ activation memory consumption**. This issue arises because DP splits the batch dimension both across and within nodes. Compared to other intra-node parallelism strategies shown in Figure 4, applying DP to attention forces each GPU within a node to process one

micro-batch simultaneously, increasing the activation size by $8\times$, which often results in out-of-memory issues.

To enable scalable MoE training, implementing intra-node parallelism for the attention module is crucial. Tensor parallelism (TP) is commonly employed to parallelize attention operations within nodes. However, it introduces inevitable communication costs due to all-gathering and reduce-scattering activations along the critical path. With the increasing gap between computational FLOPs and communication bandwidth, we find that the TP communication overhead can even surpass the computation time of self-attention. This communication-dominated bottleneck limits the ability to overlap communication and computation, ultimately reducing training efficiency.

We adopt sequence parallelism (SP), as proposed in DeepSpeed-Ulysses [14], to scale MoE training and effectively reduce communication along the critical path. SP is commonly used in long-context training to address memory challenges associated with long inputs. We find it also works well in large-scale MoE training. First, it significantly reduces communication overhead compared to TP, especially when using grouped-query attention [4]. Second, while it introduces some parameter redundancy and increased communication overhead during parameter synchronization, the unique characteristics of MoE models make these trade-offs manageable and acceptable.

Communication efficiency. When utilizing TP, the communication volume in attention is

$$2bsh(n-1)/n. \quad (1)$$

With SP, the communication volume decreases to

$$2bsh(n-1)/n \times (2+2/m)/n, \quad (2)$$

where m represents the ratio between the number of query heads and that of key-value heads. Assuming the model is trained on a NVIDIA Hopper GPU workstation with an NVLink domain of size 8, the communication latency for sequence parallel attention can be reduced to about one-fourth of that required by tensor-parallel attention.

Data communication & memory overhead. A notable difference between SP and TP attention is how parameters are distributed across devices: TP shards the attention weights, while SP replicates them. This raises the concern about the potential increase in communication overhead for synchronizing gradients and parameters. Counterintuitively, given the intra- and

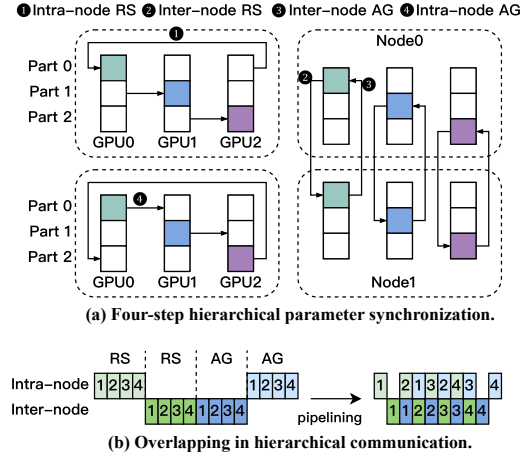


Figure 5 Hierarchical communication for parameter synchronization in SP attention.

inter-node bandwidth asymmetry and the adoption of hierarchical communication operations in modern communication libraries [33] as shown in Figure 5 and analyzed in Appendix A.1, although SP attention requires synchronization of $n\times$ more parameters compared to TP attention, the difference in communication overhead is minimal in practical scenarios.

On the other hand, the additional GPU memory consumption introduced by SP attention is minimal in MoE training. For large-scale MoE models with tens to hundreds of experts, the majority of GPU memory is consumed by the expert parameters. Our experiments, detailed in §6.2, confirm that the extra parameter synchronization and memory overhead of SP attention remain manageable.

Balanced vs. imbalanced. In addition to the Ulysses-style SP attention, we also explored other forms, including context parallelism (CP) [1], which partitions all activations along the sequence dimension. CP attention, however, faces workload imbalance due to causal masking in attention, as each token only attends to previous tokens. To mitigate this, we attempted the zigzag strategy by grouping the head and tail partitions of the sequence on the same GPU, although achieving perfect balance remains challenging. Consequently, in large-scale training, the entire training process is often constrained by the most imbalanced data batch. Moreover, this imbalance disturbs the training pipeline, thereby reducing overall training efficiency.

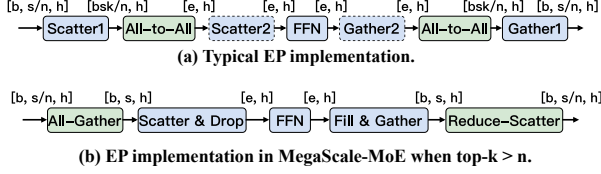


Figure 6 Communication-efficient expert parallelism. e represents the number of tokens routed to the worker.

3.2 Expert Parallelism for Feed-forward Network

In the choice of parallelism strategies for the feed-forward network component, expert parallelism (EP) consistently outperforms tensor parallelism. TP partitions the hidden dimension of each expert, reducing GEMM efficiency, whereas EP maintains full expert computation on each device. Theoretically, the communication cost for EP is

$$2k/n \times bsh(n-1)/n, \quad (3)$$

while for TP it is

$$2bsh(n-1)/n. \quad (4)$$

Although their relative efficiency depends on the ratio k/n , we design an adaptive communication strategy for different top- k values to minimize the communication volume of EP.

Efficient communication pattern. Figure 6 compares the typical EP implementation with MegaScale-MoE’s approach. The standard EP implementation requires two all-to-all communications for token dispatch and aggregation. Additionally, a scatter operation may be required before sending and after receiving tokens to ensure that tokens assigned to the same expert reside in a contiguous memory space.

When the top- k value exceeds n , we replace traditional all-to-all communication with all-gather and reduce-scatter. First, an all-gather operation collects tokens from all workers. Then, a local scatter operation discards unneeded tokens, retaining only those required by the experts on the current worker. After expert computation, the tokens are assembled into a complete tensor. This approach enables a gather operation before communication, followed by a reduce-scatter to produce the final result, ensuring that EP’s communication overhead remains equal to or lower than TP’s.

In practical training, all-to-all communication is less efficient than all-gather and reduce-scatter, as it requires each worker to communicate with all others,

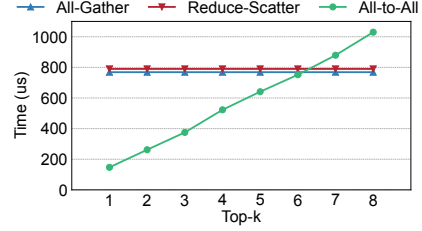


Figure 7 Comparison of AG, RS, and A2A for token dispatch.

whereas all-gather and reduce-scatter follow a ring-based communication pattern with only neighboring workers. As shown in Figure 7, the communication time for these three operations in Mixtral-8×7B reveals that when top- $k > 6$, the all-gather-based EP implementation is more efficient.

Efficient operators. Instead of using `torch.scatter_add` and `torch.gather` for tensor scattering and gathering like Megatron-LM, we develop efficient scatter and gather operators directly using CUDA. Based on the token routing results, we pre-calculate the mapping from each row of the input tensor (representing a token) to the corresponding row in the output tensor. The scatter and gather operators then perform data transfers efficiently according to this mapping.

Load balance. A well-known challenge in MoE model training is load balancing across experts [20, 24]. To address this, we use auxiliary loss and token dropping to balance the workload across GPUs within each node. Similar to DeepSeek-V2 [24], we treat the experts placed on the same GPU as a group and calculate the balance loss and computational capacity for each device rather than for each individual expert.

4 Communication-computation Overlap

After optimizing parallelism strategies to minimize communication volume, we further reduce the communication overhead to nearly zero using comprehensive communication-computation overlapping techniques. Training large models involves integrating various techniques, which increases the complexity of communication overlap. For instance, at any given moment, the device might concurrently handle computation and communication kernels, overlap PP and DP communications, and manage data transfers between the device and host. Existing frameworks like Megatron-LM assemble attention and FFN modules into MoE layers and rely on the `torch.autograd` package for

backward propagation, which limits the flexibility of communication overlap. In contrast, MegaScale-MoE decomposes the attention and FFN modules of each MoE layer into operators that run as GPU kernels, enabling fine-grained communication overlap through flexible scheduling.

4.1 Inter-operator Overlap

We overlap communication operators with independent computation operators by executing them asynchronously on different CUDA streams. To achieve optimal performance during the training process, we adopt a specifically hand-tailored, holistic scheduling strategy.

Holistic scheduling. From the caller’s perspective, we implement a unified macro module to execute the entire MoE layer’s forward and backward passes, thereby expanding our scheduling flexibility. For instance, during the backward pass, various communication operators can be overlapped with dependency-free computations, such as activation recomputation, to improve efficiency. From the runtime perspective, a key challenge is efficiently managing concurrent communication tasks by resolving resource conflicts to prevent blocking and maximize throughput. This requires careful coordination, such as determining the number of SMs allocated to each communication operator, to minimize interference and optimize overall throughput.

Selective activation rematerialization. The holistic scheduling strategy also helps reduce memory usage without compromising training speed. Compared to dense models with equivalent computational requirements, MoE models exert significantly higher memory pressure during training due to their parameter count being several times larger. In addition to employing ZeRO optimizations [38] to eliminate redundant optimizer states across DP groups, we further optimize memory usage through selective activation rematerialization. This approach reduces activation memory requirements by re-performing computation and communication operators that can be overlapped with other necessary operators.

Figure 8a illustrates the forward pass of a Mixtral [16] MoE layer and highlights key activations produced during this process. MegaScale-MoE strategically retains activations that are computationally expensive to recompute, while recalculating others generated by memory-intensive operations or communication operations. This minimizes dependencies on backward computation, enabling rematerialization operations

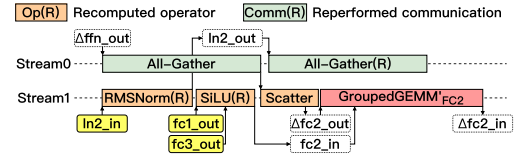
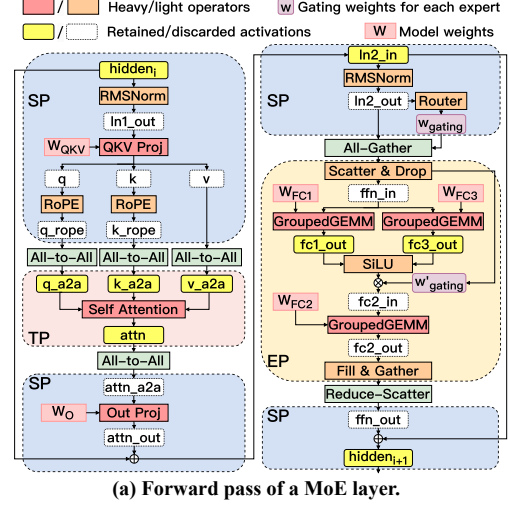


Figure 8 Selective activation rematerialization.

to overlap with other computations and communications, avoiding delays in the critical path. For example, as shown in Figure 8b, the backward pass of the GroupedGEMM operator for FC2 requires the activation $fc2_in$ and the gradient of $fc2_out$ (denoted as $\Delta fc2_out$) as inputs. MegaScale-MoE recomputes $fc2_in$ and overlaps this operator with gradient communication (i.e., all-gather for Δffn_out). Similarly, ffn_in is obtained through re-performing RMSNorm and all-gather, with these operators hidden within the preceding communication and the FC2 GroupedGEMM, respectively. MegaScale-MoE also places the weighted sum of ffn_out immediately after the SwiGLU [43] activation function to eliminate the need to store ffn_out . This reordering ensures computational consistency by avoiding operators that cross non-linear boundaries.

Our analysis in Appendix A.2 and experiments in §6.2 show that MegaScale-MoE reduces the activation memory by $\sim 50\%$ while maintaining the same training speed.

4.2 Intra-operator Overlap

Although inter-operator overlap effectively hides communication latency, squeezing all bubbles in the execution timeline remains non-trivial—especially in the forward pass, where no rematerialization or gradient computation operators exist to overlap with communication. Some forward operators directly depend

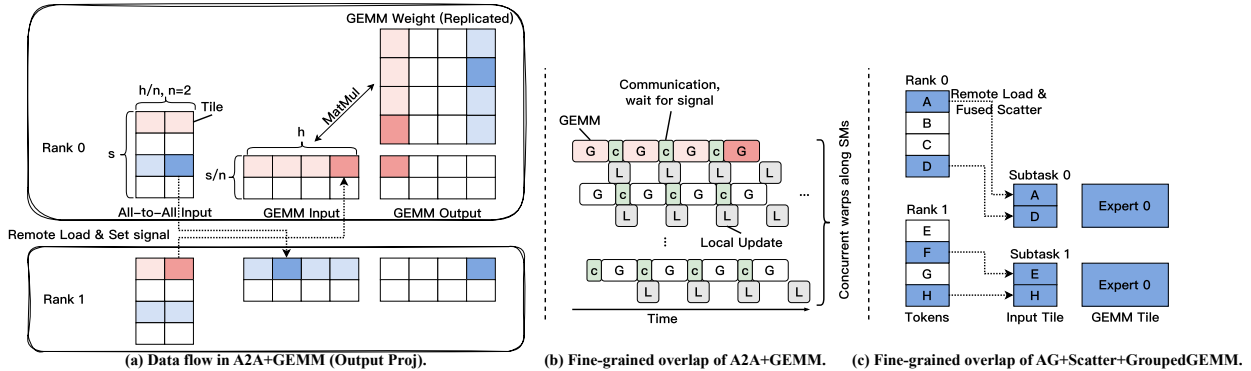


Figure 9 Fine-grained intra-operator communication-computation overlap.

on communication, such as token dispatch for expert computation, making overlap impossible unless another micro-batch is introduced, which increases memory pressure.

A widely adopted solution [17, 48, 50] is to decompose operators into smaller parallel ones to enable pipelining by executing them on separate CUDA streams. However, this approach introduces non-negligible overhead: (i) complex stream control, involving host interference and causing random bubbles due to the non-deterministic feature of CPU control; (ii) imperfect tail computation, increasing overall computation latency.

To address the above issues, we use intra-operator overlap to exploit the parallelism for communication and computation operators with direct dependency. The core idea is to fuse these operators and break down the workloads into tiles. Following prior work [5, 15, 51, 53], we implement barriers in device memory between communication and computation operators. These barriers allow fine-grained notifications between computation and communication at the tile level. Additionally, as the barriers reside in device memory, they eliminate the need for host interference, enabling further performance improvements. We implement two types of kernels, overlapping with GEMMs and overlapping with MoE GroupedGEMMs, for the attention and FFN modules, respectively.

Overlapping with GEMMs. We first introduce the intra-operator communication-computation overlap for GEMM kernels. Specifically, we implement all-to-all(A2A)+GEMM and GEMM+A2A kernels for Output and QKV Projections in SP attention, respectively, where X+Y means Y executed after X. Figure 9 shows the data flow and overlapping pattern in A2A+GEMM. The GEMM on local data and

communication for remote data starts simultaneously. We leverage dedicated GPU copy engines for data transfer, ensuring that all SMs (streaming multiprocessors) are fully utilized for computation. Once a remote data tile arrives at local memory, a signal notifies the GEMM kernel to continue its computation on the arrived tile. For GEMM+A2A, the all-to-all operation is fused into the GEMM kernel. Each tile of GEMM computation ends with a remote data transfer that writes the output data tile to remote ranks. We also implement all-gather+GEMM and GEMM+reduce-scatter kernels for tensor parallelism, which are similar to A2A+GEMM and GEMM+A2A.

For A2A+GEMM and GEMM+A2A, we allocate a small number of SMs for communication as all-to-all is more complex than all-gather and reduce-scatter. The number of SMs for communication is tuned to make communication and computation exhibit similar latency. Moreover, multiple ranks may simultaneously read from or write to the same device, potentially causing contention in NVLink. To mitigate this, we apply swizzling [5, 51, 53] to reorder tile communication and computation so that the arrival of communication tiles aligns with the pace of computation tiles.

Overlapping with GroupedGEMMs For expert parallelism with token dispatch and combine, we aim to overlap communication with GroupedGEMMs. We implement two types of overlapping kernels: all-gather+scatter+GroupedGEMM and GroupedGEMM+gather+reduce-scatter. Unlike the overlapping techniques for GEMM kernels, MoE GroupedGEMMs require token shuffling (scatter/-gather). As a result, each computation tile may depend on tokens from multiple ranks. To effectively overlap computation with communication, we sort the token order to minimize the number of dependent ranks for each computation tile. Additionally, since

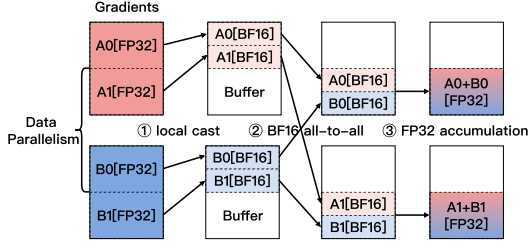


Figure 10 DP communication compression.

each tile has its own dependencies, the signal control for each tile varies depending on the MoE routing, which is determined dynamically.

In detail, for AG+scatter+GroupedGEMM, we reorder tokens along the sequence dimension based on their routed expert index. Then, for each expert, we sort the routed tokens according to their source rank index. Finally, we slice the sorted sequence into blocks and perform GroupedGEMM using a sequence of computation tiles. Specifically, as shown in Figure 9c, we fuse the local scatter into the kernel by selecting rows of input data based on the index mapping. The GroupedGEMM computation for each expert is divided into tiles, with each tile depending on only a subset or even a single source rank. This reduces the overall waiting time for each computation block, avoids redundant loading of expert parameters, and improves the overlap between computation and communication tiles.

5 Communication Compression

We further reduce communication overhead by applying communication compression. To maintain convergence stability, mixed-precision training frameworks typically transfer tensors awaiting reduction in higher precision, such as FP32, to ensure more accurate accumulation. A common example of this is gradient reduce-scatter in data parallelism.

DP communication compression. As MoE model parameters increase, so does the communication overhead for parameter and gradient synchronization in data parallelism. Prior work has explored gradient compression to mitigate this cost. In our BF16 mixed-precision training, we carefully apply FP32-to-BF16 precision reduction for gradient synchronization, balancing efficiency and convergence stability.

Specifically, as shown in Figure 10, we retain the main gradients in FP32 during local gradient accumulation in pipeline parallelism. After each model stage completes accumulation, instead of relying solely on reduce-scatter for gradient synchronization, we cast

Name	#layers	h	#heads	m	h_{ffn}	#experts	top- k
Internal-352B	60	4096	32	4	14336	32	3
Mixtral-8×7B	32	4096	32	4	14336	8	2
Mixtral-8×22B	56	6144	48	6	16384	8	2
Hunyuan-Large	64	6400	80	10	18304	16	1
Phi-3.5-MoE	32	4096	32	4	6400	16	2
DeepSeekMoE	28	2048	16	1	1408	64	6

Table 2 Model configurations in evaluation.

gradients to BF16 and perform all-to-all communication within the data parallel group to gather the required gradient shards, which are then locally aggregated in FP32. Our results show that this approach introduces negligible precision loss compared to directly performing reduce-scatter with FP32, while reducing gradient communication overhead by 50%.

This approach minimizes risk for two key reasons. First, it performs a one-time conversion of accumulated gradients to BF16 during communication, while the local gradient accumulation is maintained in FP32 precision. Second, instead of using ring-style reduce for BF16 gradient communication, it employs all-to-all communication, with the final reduction computed using FP32 summation. This design prevents precision loss that could arise from repeated accumulation of BF16 values in ring-based reductions.

We observe that casting large gradients and performing all-to-all communication increases peak memory consumption, potentially causing out-of-memory errors. To mitigate this, we develop a memory-efficient operator that in-places BF16 gradients into half of the FP32 input buffer while using the remaining half as the output buffer for BF16 all-to-all communication, preventing peak memory growth.

Communication compression for FP8 training. In low-precision FP8 training, the proportion of communication time increases due to reduced computation time. To mitigate communication overhead, we explore compressing communication volume using FP8 precision with appropriate quantization techniques. Currently, we apply communication compression in FP8 MoE training with tensor parallelism, focusing on reduction scenarios prone to overflow or underflow. For example, we adopt the E4M3 format (4-bit exponent and 3-bit mantissa) for all tensors. Similar to DP reduce-scatter compression, we replace BF16 TP reduce-scatter with FP8 all-to-all in forward propagation and perform reduction in FP32 precision. In the corresponding backward propagation, we apply FP8 all-gather for gradients. Notably, simply reducing precision leads to loss misalignment with BF16 training. To mitigate this, we apply per-token activation quantization for forward communication and

System	#GPUs	Iteration Time (s)	Throughput (tokens/s)	Training Time for 1T Tokens (days)
Megatron-LM	240	39.94	151.1k	76.61
	480	19.56	301.1k	38.38
	720	13.70	430.5k	26.88
	960	10.82	550.2k	21.23
	1440	7.90	746.6k	15.50
MegaScale-MoE	240	21.61	272.9k (1.81×)	42.41
	480	11.83	498.6k (1.65×)	23.21
	720	7.97	740.1k (1.72×)	15.64
	960	6.12	963.8k (1.77×)	12.01
	1440	4.19	1407.7k (1.88×)	8.22

Table 3 Strong-scaling training performance for the 352B MoE model on NVIDIA H800 GPUs. The number in parentheses in the throughput column represents the speedup of MegaScale-MoE compared to Megatron-LM.

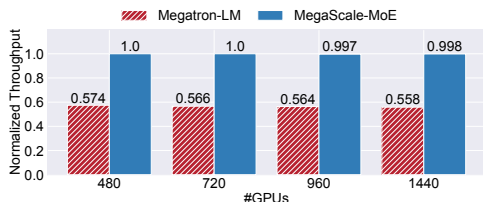


Figure 11 Weak-scaling training performance for the 352B MoE model on NVIDIA H800 GPUs.

per-channel quantization for backward communication. In backward propagation, we further group quantization along the token dimension using a small group size (e.g., 128).

6 Evaluation

In this section, we present a comprehensive evaluation of MegaScale-MoE, covering overall training performance (§6.1), ablation studies of MegaScale-MoE’s key optimizations (§6.2), and the effectiveness of the precision-communication co-design (§6.3). Table 2 lists the configurations of the MoE models used in our evaluation, detailing hidden size (h), FFN intermediate size (h_{ffn}), number of experts, and top- k values. The evaluation is conducted on NVIDIA H800 GPUs unless otherwise specified, with the specifications provided in Table 4.

6.1 Training Performance

MegaScale-MoE is built on top of Megatron-LM [46], a state-of-the-art open-source LLM training system that supports 3D parallelism strategies and is continuously updated to incorporate the latest optimizations from the community. Our evaluation uses the Megatron-LM on GitHub [30] with commit hash f1f03922, selected for its stability at the commencement of our experiments months ago. For fair comparison, we use the same global batch size for Megatron-LM and MegaScale-MoE and choose the optimal parallelism configurations for the two systems, re-

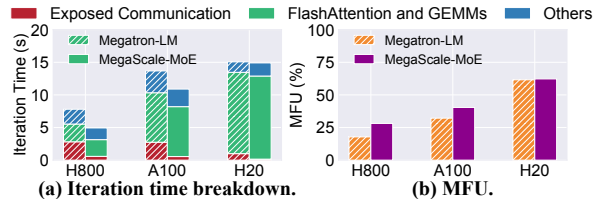


Figure 12 Performance breakdown of training Mixtral-8×7B on different GPUs.

GPU	Compute Capability (TFLOPS)	Memory Spec.		NVLink Bw. (GB/s)
		Cap. (GB)	Bw. (TB/s)	
H800	989	80	3.4	400
A100	312	80	2.0	600
H20	148	96	4.0	900

Table 4 Specifications of different NVIDIA GPUs.

spectively. Specifically, MegaScale-MoE employs SP attention and EP within each node, while Megatron-LM adopts TP within each node, with both systems configured with a PP size of 15. Sequence length is 8,192 and vocabulary size is 65,536.

Scalability. Table 3 compares the strong-scaling training performance of Megatron-LM and MegaScale-MoE on the 352B MoE model. We scale the number of GPUs while keeping the global batch size fixed at 720. Across all settings, MegaScale-MoE achieves 1.65–1.88× speedups over Megatron-LM. As the number of GPUs increases, the MFU (Model FLOPs Utilization) of MegaScale-MoE declines from 32.48% to 27.89%. This is expected, as the batch size is fixed and the number of micro-batches for each pipeline decreases with more GPUs, leading to more bubbles.

Figure 11 presents the weak-scaling training performance of Megatron-LM and MegaScale-MoE on the same model. We scale the global batch size from 360 to 1,080 in proportion to the number of GPUs (from 480 to 1,440). MegaScale-MoE achieves a 1.74–1.79× training throughput compared to Megatron-LM. As the scale increases, Megatron-LM’s throughput drops by 2.74% due to increased communication overhead, whereas MegaScale-MoE exhibits near-linear scalability, benefiting from comprehensive communication overlap.

Performance breakdown on different GPUs. We conduct a deep dive into MegaScale-MoE to further understand the performance of training a MoE model in production environments. We train Mixtral-8×7B on 32 NVIDIA H800, H20, and A100 GPUs, respectively. The specifications of GPUs we used are listed in Table 4. We set the DP size as four, the TP size as eight for Megatron-LM, and the SP and EP size

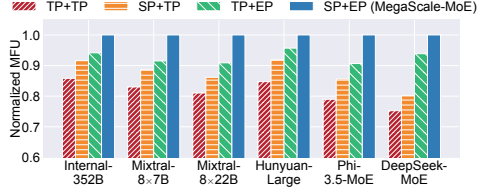


Figure 13 Parallelism efficiency for different models.

as eight for MegaScale-MoE. As shown in Figure 12b, across the four kinds of GPUs, MegaScale-MoE consistently outperforms Megatron-LM by up to $1.58\times$ in MFU. Figure 12a demonstrates the iteration time breakdown of Megatron-LM and MegaScale-MoE. Exposed communication time represents the communication time that is not overlapped with computation operations. FlashAttention and GEMMs are the operations we count when calculating MFU. The performance gain primarily results from MegaScale-MoE’s communication-efficient parallelism strategies and fine-grained overlapped communication.

Note that the MFU value decreases as GPU compute capability increases. This is because, unlike dense models, MoE models involve many memory-intensive operations like routing, local scatter, and gather, which remain time-consuming since memory bandwidth does not scale as quickly as compute capabilities. Additionally, GEMM efficiency declines with increasing compute capability, as it also relies on memory loading, constrained by memory bandwidth.

6.2 Ablation Study

Parallelism strategy. We first compare the training efficiency under various intra-node parallelism strategies using a single node with eight NVIDIA H800-SXM GPUs. We denote parallelism strategies as $X+Y$, where X represents the parallelism strategy for attention, and Y corresponds to that for experts. The available parallelism strategies for attention include TP and our SP, whereas for experts, the choices are TP and EP. To isolate the performance benefits of optimized parallelism, we disable other system optimizations.

We measure the training MFU of one internal and five open-source MoE models with diverse model configurations as listed in Table 2. The global batch size is set to 32, and we adjust the number of layers for each model to fit within the GPU memory. Figure 13 shows that MegaScale-MoE’s parallelism strategy, SP+EP, consistently outperforms the other three parallelism strategies, achieving 14.9%-32.9% higher MFU compared to TP+TP. The performance gains are attributed to two main factors. First, as discussed

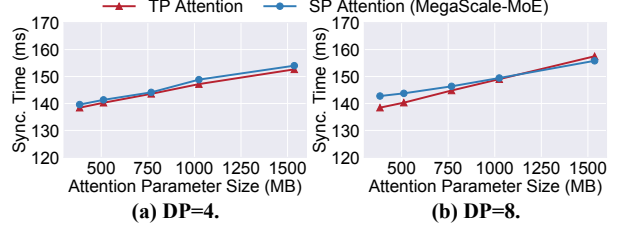


Figure 14 Parameter synchronization time under SP and TP attention.

in §3, SP and EP effectively reduce the communication volume compared to TP, thereby decreasing communication overhead. Second, TP partitions the FFN module along the intermediate size dimension, which results in lower GEMM efficiency.

To provide a more comprehensive evaluation of the parallelism strategy, we also report the additional overhead introduced by the replicated attention parameters in SP. In terms of memory usage, SP incurs a 1.2%-5.4% higher memory footprint compared to TP, requiring 1.7%-8.1% more memory to store parameters, gradients, and optimizer states across all seven models. This overhead is manageable considering the significant performance gains achieved by SP.

For the parameter synchronization time, we follow large-scale training setups and set the size of the TP or SP to 8, effectively parallelizing each layer within a single node. The attention parameter size on each GPU is varied from 384 MB to 1536 MB, while the FFN parameter size is fixed at 10 GB per GPU, reflecting typical real-world training setups. We run MegaScale-MoE with SP and TP attention, using 4 and 8 DP groups, which correspond to a total of 32 and 64 GPUs, respectively. Figure 14 shows that the synchronization times for SP and TP attention are consistently comparable, differing by only 0.3%-3.1%. This aligns with our hypothesis that SP and TP would exhibit similar performance characteristics in DP communication latency.

Intra-operator communication overlap. We then measure the duration of four key communication and the corresponding computation operators in the forward pass: (i) QKV Projection paired with all-to-all, (ii) all-to-all with Output Projection, (iii) all-gather with scatter and GroupedGEMM, and (iv) GroupedGEMM with gather and reduce-scatter, as depicted in Figure 8. Figure 15 demonstrates that across all six models, MegaScale-MoE achieves a $1.2\text{--}4.7\times$ reduction in the combined time of communication and computation operators compared

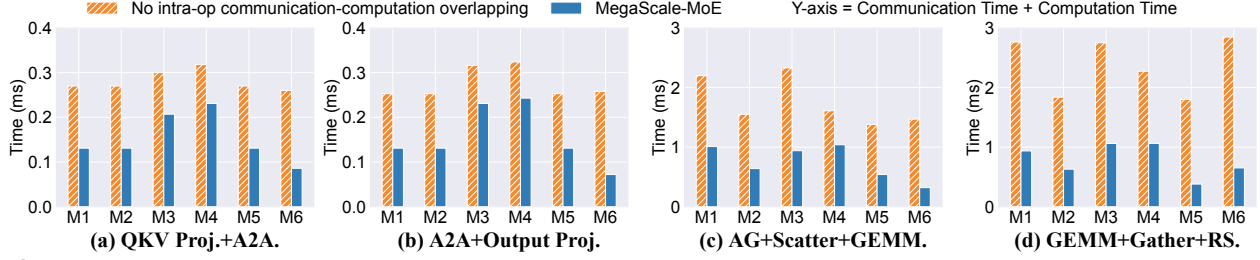


Figure 15 Overlapped communication-computation time vs. non-overlapped time of each layer. M1-M6 represent the six models listed from top to bottom in Table 2; A2A, AG, and RS refer to all-to-all, all-gather, and reduce-scatter, respectively.

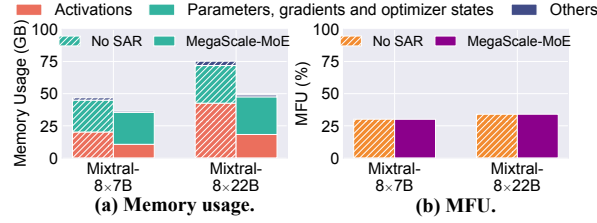


Figure 16 Ablation study of selective activation rematerialization (SAR).

to the baseline lacking fine-grained overlap. And MegaScale-MoE reduces the training iteration time by 7.1%-12.9% due to intra-operator communication-computation overlap.

Selective activation rematerialization. We compare MegaScale-MoE to a baseline that disables selective activation rematerialization (No SAR), which stores all activations in GPU memory during training. We evaluate both methods by training Mixtral-8×7B and Mixtral-8×22B on 128 NVIDIA H800 GPUs. Figure 16 shows the memory usage breakdown and the training MFU. Compared to No SAR, MegaScale-MoE reduces activation memory consumption by 45.5% and 57.2% for the two models, respectively, resulting in overall memory reductions of 21.3% and 35%, while maintaining the training performance difference within 0.5%.

Data parallelism communication compression. We validate the effectiveness of our communication compression technique by training a 7B MoE model using BF16 all-to-all DP communication and FP32 reduce-scatter communication, as described in §5. Figure 17 illustrates the training loss curves, which are nearly identical. This optimization compresses only the accumulated gradients of the batch and performs conversions between BF16 and FP32 exclusively during communication, introducing minimal risk.

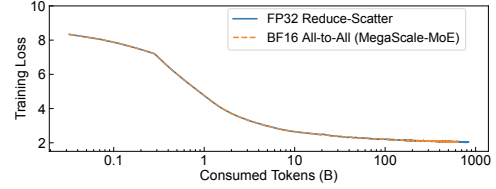


Figure 17 The training loss curve of MegaScale-MoE with DP communication compression.

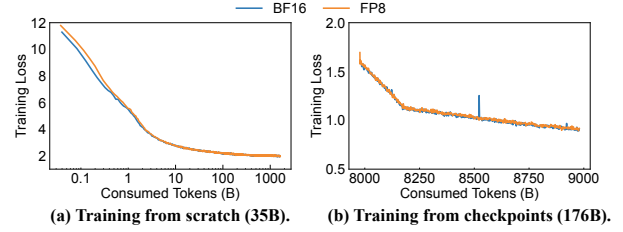


Figure 18 The loss curve of MegaScale-MoE in FP8 and BF16.

6.3 Model Convergence

We evaluate model convergence with MegaScale-MoE. Figure 18 demonstrates the loss curves of training a 35B MoE model from scratch and continuing training a 176B MoE model from a checkpoint, with results shown for both BF16 and FP8 precision. MegaScale-MoE ensures stable convergence and consistent training loss across BF16 and FP8 formats.

7 Experience

In this section, we describe our deployment and operational experience of MegaScale-MoE.

Deployment experience. MegaScale-MoE has been deployed in our production environment and is responsible for the majority of large-scale MoE training tasks within our company. It enables the training of models with trillions of parameters, supports single training jobs scaling beyond 10,000 GPUs, with individual training tasks running for several months. By combining the aforementioned techniques, MegaScale-

MoE minimizes idle communication time and optimizes memory usage in MoE training without compromising model performance, ultimately saving millions of GPU hours in large-scale MoE training. Figure 19 shows the model convergence from a real production run, which trains a proprietary MoE model with 200B parameters, 20B activated for each token. This run uses over 10,000 GPUs and lasts for months. The loss continues to converge with a stable training process.

FP8 training. We have made extensive efforts to maintain the convergence stability of FP8 training. For example, we observe that the SwiGLU operator significantly expands the numerical range. To address this, we replace per-tensor quantization with higher-precision per-token quantization ($1 \times h$). Additionally, since multiplying SwiGLU with the gating weight further amplifies the dynamic numerical range, we shift the gating weight multiplication back to after the FC2 output, reducing quantization errors.

Beyond ensuring training convergence, we introduce additional engineering optimizations. Existing FP8 training implementations [23, 48] store model parameters in BF16, requiring frequent FP8 conversion for GEMM computations, adding casting and transpose overhead. To address this, we use a multi-precision optimizer to store model parameters directly in FP8, while keeping main parameters in FP32 with separate buffers for different data types. This lowers memory consumption and halves parameter all-gather communication in data parallelism.

Scale up. When training MoE models, an intriguing engineering question arises: can we indefinitely scale the training size by increasing model parameters without raising computational load? This approach is impractical in tensor parallelism, as scaling up the model necessitates a higher TP degree to accommodate additional parameters. While increased TP reduces per-GPU computation, the communication overhead remains constant, as shown in Formula 1 and 4, leading to progressively longer communication times and reduced training efficiency. In other words, TP has inherent scalability limitations and often relies on high-speed intra-node links to mitigate communication delays.

In contrast, when scaling training with SP and EP, the communication volume decreases as the parallel size n increases, as shown in Formula 2 and 3. This implies that, in theory, this parallelism strategy can scale to significantly larger sizes. However, in practical hierarchical infrastructures, a critical challenge emerges: can this approach maintain training

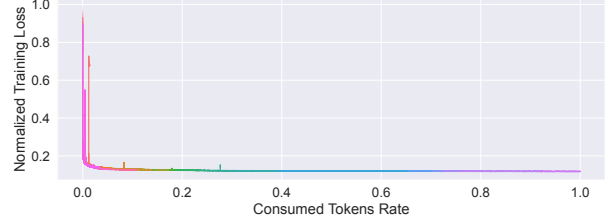


Figure 19 The normalized training loss curve of a real production run on more than 10,000 GPUs for months, training a MoE model with 20B activated and 200B total parameters on multi-trillion tokens. Different colors indicate training restarts.

efficiency when scaling beyond the NVLink domain, where bandwidth drops to RDMA levels?

Formally, for a SwiGLU structure incorporating a MoE mechanism, the ratio R between computation time and communication time is defined as:

$$\text{comm_time} = \frac{2k \times bsh(n-1)/n/n}{\text{bandwidth}}, \quad (5)$$

$$\text{comp_time} = \frac{3k \times bsh \times h_{ffn}/n}{\text{peak}}. \quad (6)$$

$$R = \frac{\text{comp_time}}{\text{comm_time}} \quad (7)$$

$$= 3/2 \times h_{ffn} \times \frac{\text{bandwidth}}{\text{peak}} \times n/(n-1) \quad (8)$$

$$\approx 3/2 \times h_{ffn} \times \frac{\text{bandwidth}}{\text{peak}} \quad (9)$$

To sustain training efficiency, the FFN’s computation time must exceed the communication time, ensuring effective overlap of communication overhead. Therefore, our goal is to maintain $R > 1$, leading to two key insights:

- The value of R is independent of the number of experts, top- k , hidden dimension, parallelism size, or input size, providing flexibility in selecting algorithm parameters.
- R is solely determined by the expert’s intermediate dimension, computational peak, and communication bandwidth. Consequently, on fixed hardware, as long as the expert dimension is sufficiently large, the MoE model can be scaled while maintaining training efficiency from an engineering perspective.

Holistic vs. automatic. We have invested substantial engineering efforts in inter-operator communication-computation overlap, including determining operator execution order, concurrency of communication and computation, and SM allocation for communication.

These manual interventions provide deeper insights into training dynamics, enabling targeted optimizations. As training progresses and experience accumulates, we seek to automate operator scheduling within the search space to optimize the training process at a fine-grained level and achieve optimal performance. We leave automatic optimization for future work.

8 Related Work

Large model training. LLM research has led to the development of scalable, efficient, and robust training techniques to meet the substantial computational demands of these models. DeepSpeed [41] features the Zero Redundancy Optimizer (ZeRO) [38, 39, 42], which shards model parameters, gradients, and optimizer states across participating GPUs in data parallelism, enabling the scaling of LLMs with manageable memory consumption. Megatron-LM [46] focuses on intra-layer model parallelism techniques, partitioning the parameters and computation of each layer. Pipeline parallelism assigns the parameters and computation of a contiguous subset of layers to each GPU [12, 31], breaks a batch into micro-batches, and processes the micro-batches in a pipelined fashion. MegaScale [17] shows how combining tensor, pipeline, and data parallelism can be an efficient strategy to train large multi-billion parameter models at unprecedented scale.

Mixture-of-Expert training. To address the computational challenges of training advanced neural networks, the machine learning field has increasingly adopted Mixture-of-Experts architectures. Subsequently, a number of parallel deep learning frameworks have been proposed for training or running inference on MoEs on multi-GPU clusters. DeepSpeed-MoE [40] significantly reduces training costs through model architecture designs and compression techniques. HetuMoE [34] utilizes a hierarchical all-to-all communication strategy to achieve performance speedup. SE-MoE [45] distinguishes itself by focusing on scalable and efficient training with heterogeneous resources like CPU memory and SSDs. Furthermore, Tutel [13] offers a dynamic solution for MoE models, employing adaptive parallelism and pipelining. FasterMoE [11] introduces a comprehensive suite of optimizations such as dynamic shadowing, fine-grained scheduling, and congestion-avoiding expert selection strategies. Janus [28] proposes a data-centric paradigm shift for MoE models, aiming to lower communication demands and boost training efficiency. Recently, DeepSeek-V3 [25] proposes a series of algorithm-system co-optimizations, such

as efficient cross-node all-to-all and DualPipe, to enhance the efficiency of large-scale MoE training. We are also exploring algorithm-system co-design to further reduce training costs.

Long-context training. While Megatron-LM [18, 46] opts to partition only specific operations along the sequence dimension, various methods of sequence parallelism [19, 22, 27] have been explored for training models requiring long contexts. The Blockwise Parallel Transformer [26] method implements blockwise computation of self-attention and the fusion of FFNs based on online softmax calculations. Ring Attention [22, 27] introduces a ring-style communication mechanism integrated with self-attention calculations, facilitating the exchange of key and value chunks. We adopt the all-to-all style of SP attention from DeepSpeed Ulysses [14], which partitions attention by heads rather than sequence length, due to its reduced communication volume and balanced computation pattern.

Communication-computation overlap. Several frameworks [10, 21, 29, 37, 52] focus on overlapping communication with computation in distributed deep learning training with a single parallelism strategy. Some compiler-style work [15, 35, 50] provides fine-grained overlap among kernels, but excessive partitioning of GEMM kernels can result in low GPU utilization. Centauri [6] enhances communication overlap for LLM training with 3D parallelism by communication partitioning and hierarchical scheduling. Similar to Centauri, our inter-operator communication overlap hides communication within independent computation by reordering operators. We further conceal communication on critical paths through intra-operator overlap, without compromising GPU utilization. DualPipe, proposed by DeepSeek-V3 [25], overlaps communication and computation within different forward and backward chunks and requires storing $2\times$ the model parameters. In contrast, MegaScale-MoE achieves this overlap within a single forward or backward chunk without incurring additional memory overhead.

9 Conclusion

In this paper, we offer an in-depth look at the design, implementation, and deployment of MegaScale-MoE, a production-grade system built to efficiently train MoE models. MegaScale-MoE exploits communication-efficient approaches, including parallelism strategies with lower communication

volume, inter- and intra-operator communication-computation overlap, and communication compression with adjusted communication patterns to unleash the compute capabilities of high-performance GPUs. MegaScale-MoE achieves 1.41M tokens/s in throughput when training a 352B MoE model on 1,440 NVIDIA Hopper GPUs, a $1.88\times$ improvement over Megatron-LM. By sharing our insights on accelerating large-scale MoE training, we hope our work will inspire future research.

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A Appendix

A.1 Hierarchical Communication for Parameter Synchronization

Let the full attention weights size be P , the dimension of model parallelism (TP or SP) be n , and the data parallel size be d . Typically, GPUs for model parallelism are located on the same node, requiring intra-node communication, whereas data parallelism spans across nodes, requiring inter-node communication. Consider a data parallelism group containing d devices, each holding the identical partition of the parameter.

For parameter synchronization in TP attention, communication involves data of size P/n across d devices in two primary steps in LLM training:

- inter-node **reduce-scatter** operation, where the data size is P/n , on d devices.
- inter-node **all-gather** operation, where the data size is P/n , on d devices.

leading to primarily inter-node communication, with a communication volume of $2P/n(d-1)/d$.

With SP attention, the parameter synchronization involves the entire data of size P across $n \times d$ devices. Considering the discrepancy between intra-node and inter-node network bandwidth, this process can be implemented by four-step hierarchical communication, where the replicated parameters are first reduced within a node and then reduced across nodes, before being distributed back to each device. Figure 5a illustrates a hierarchical communication example where $n = 3$ and $d = 2$. The detailed steps are as follows.

- intra-node **reduce-scatter** operation, where the data size is P , on n devices.
- inter-node **reduce-scatter** operation, where the data size is P/n , on d devices.
- inter-node **all-gather** operation, where the data size is P/n , on d devices.
- intra-node **all-gather** operation, where the data size is P , on n devices.

The inter-node communication volume in SP attention remains at $2P/n(d-1)/d$, with additional intra-node volume of $2P(n-1)/n$.

Moreover, due to the distinct resources for intra-node and inter-node communications, these steps can be segmented into small chunks and pipelined to efficiently hide each other as shown in Figure 5b.

Activation	Shape	Obtained From
hidden	$[b, s/n, h]$	# Input
ln1_out	$[b, s/n, h]$	# RMSNorm(hidden)
qkv	$[b, s/n, h(1+2/m)]$	# MatMul(ln1_out, qkv_weight)
q_rop	$[b, s/n, h]$	# RopeEmbedding(q)
k_rop	$[b, s/n, h/m]$	# RopeEmbedding(k)
qkv_a2a	$[b, s, h(1+2/m)/n]$	# All-to-All(q_rop, k_rop, v)
attn	$[b, s, h/n]$	# SelfAttention(qkv_a2a)
attn_a2a	$[b, s/n, h]$	# All-to-All(attn)
attn_out	$[b, s/n, h]$	# MatMul(attn_a2a, out_weight)
ln2_in	$[b, s/n, h]$	# Add(hidden, attn_out)
ln2_out	$[b, s/n, h]$	# RMSNorm(ln2_in)
ln2_out_ag	$[b, s, h]$	# All-Gather(ln2_out)
ffn_in	$[b*s*k/n, h]$	# Scatter(ln2_out_ag)
fc1_out	$[b*s*k/n, fh]$	# GroupedGEMM(ffn_in, fc1_weight)
fc3_out	$[b*s*k/n, fh]$	# GroupedGEMM(ffn_in, fc3_weight)
fc2_in	$[b*s*k/n, fh]$	# SiLU(fc1_out, fc3_out)
fc2_out	$[b*s*k/n, h]$	# GroupedGEMM(fc2_in, fc2_weight)
fc2_out_rs	$[b, s, h]$	# Gather(fc2_out)
ffn_out	$[b, s/n, h]$	# Reduce-Scatter(fc2_out_rs)
hidden(next)	$[b, s/n, h]$	# Add(ln2_in, ffn_out)

Figure 20 Activation shapes in rematerialization.

The ratio of inter-node communication latency and intra-node communication latency is

$$\frac{1}{n} \times \frac{\text{intra-node bandwidth}}{\text{inter-node bandwidth}} \times \frac{n(d-1)}{d(n-1)} \quad (10)$$

Consider a typical training scenario involving an H100 SXM machine, where the NVLink bandwidth is 450 GB/s, and the inter-device NIC communication bandwidth is 50 GB/s. In this context, the latency of inter-node communication can easily surpass that of intra-node communication. This implies that the communication within a node can overshadow that between nodes. Consequently, in such scenarios, the synchronization of gradients and parameters with SP attention is, in fact, consistent with TP attention.

A.2 Selective Activation Rematerialization

Figure 20 illustrates the shapes of the key activations produced during forward propagation, with the highlighted activations retained for backward propagation. Let the model parallelism size within one MoE layer be n and the intermediate hidden size of one expert be fh . The total activation of a single MoE layer is

$$(2n + 2k + 3kf + 12 + 5/m)bsh/n,$$

which we have reduced to

$$(2kf + 4 + 2/m)bsh/n.$$