# Equalizer++

Signal Processing

Mikkel Krogh Simonsen Poul Hoang Kasper Kiis Jensen

# Copyright © Aalborg University 2015 This report is compiled in LATEX, originally developed by Leslie Lamport, based on Donald Knuth's TEX. The main text is written in Computer Modern pt 11, designed by Donald Knuth. Flowcharts and diagrams are made using Microsoft Visio, Inkscape and Tikz, a TEXpackage for

ii 15 gr 514

generating graphics.



#### Institute of Electronic Systems

Fredrik Bajers Vej 7 DK-9220 Aalborg  $\emptyset$ 

#### **AALBORG UNIVERSITY**

#### STUDENT REPORT

Title:

Flexible Equalizer for use in active speaker systems

Theme:

Digital Real-Time Signal Processing

**Project Period:** 

6. Semester

**Project Group:** 

16 gr 514

Participants:

Kasper Kiis Jensen Poul Hoang Mikkel Krogh Simonsen

Supervisor:

Sofus

**External Contact:** 

Copies: 5

Page Numbers: xx

Date of Completion:

May 26, 2016

#### Abstract:

This is a box for an Abstract waiting to be written.

The content of this report is freely available, but publication (with reference) may only be pursued due to agreement with the author.

#### Contents

Part I Preanalysis		
1	Introduction	2
2	Problem Analysis 2.1 Problem Statement	<b>3</b>
3	Technical Analysis         3.1 Gate          3.2 Equalizer          3.3 Limiter          3.4 why a DSP?          3.5 Part Conclusion	4 4 4 4 4
Pa	art II Design & implementation	5
4	Overview	6
5	Requirements	7
6	Choice of hardware	8
7	Interface	9
8	Total Overview	10
9	Gate  9.1 Requirements & Interface 9.2 Design 9.3 Simulation 9.4 Implementation 9.5 Test	11 11 11 11 11
	Equalizer         10.1 Requirements & Interface          10.2 Design          10.3 Simulation          10.4 Implementation          10.5 Test	12 12 12 12 12 12

iv 15 gr 514

11.1 Requirements & Interface	13
11.2 Design	13
11.3 Simulation	13
11.4 Implementation	13
11.5 Test	13
Part III Acceptance Test & Conclusion	14
12 Conclusion	15
13 Optimization	16
14 Perspectivation	17
15 Discussion	18
Part IV Appendix	19
A Appendix	20

15 gr 514 v

vi 15gr514

#### Preface

This report has been carried out during spring of 2016 as a 6. semester Electronics and IT student bachelor project at Aalborg University by group 16grXXX. The project concerns the development of Somehting extremely awesome.

Aalborg University, May 27, 2016

Poul Hoang
phoang13@student.aau.dk

Mikkel Krogh Simonsen
mksi13@student.aau.dk

Kasper Kiis Jensen
rsader13@student.aau.dk

15gr514 vii

viii 15 gr 514

## Part I

Preanalysis

15gr514 1 of 20

#### 1 | Introduction

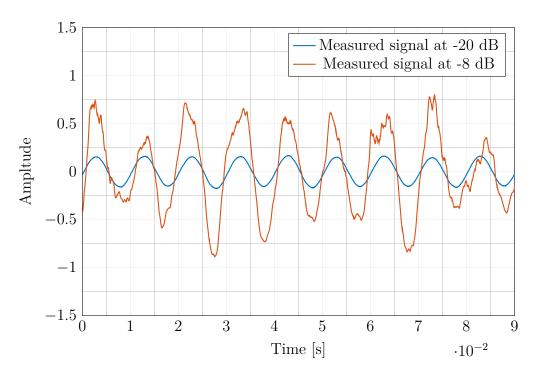


Figure 1.1: test.

## 2 | Problem Analysis

#### 2.1 Problem Statement

15gr514 3 of 20

#### 3 | Technical Analysis

- 3.1 Gate
- 3.2 Equalizer
- 3.3 Limiter
- 3.4 why a DSP?
- 3.5 Part Conclusion

#### Part II

Design & implementation

15gr514 5 of 20

# 4 | Overview

# 5 | Requirements

15gr514 7 of 20

## 6 | Choice of hardware

# 7 | Interface

15gr514 9 of 20

## 8 | Total Overview

#### 9 | Gate

- 9.1 Requirements & Interface
- 9.2 Design
- 9.3 Simulation
- 9.4 Implementation
- 9.5 Test

15gr514 11 of 20

#### 10 | Equalizer

- 10.1 Requirements & Interface
- 10.2 Design
- 10.3 Simulation
- 10.4 Implementation
- 10.5 Test

#### 11 | Limiter

- 11.1 Requirements & Interface
- 11.2 Design
- 11.3 Simulation
- 11.4 Implementation
- 11.5 Test

15gr514 13 of 20

#### Part III

#### Acceptance Test & Conclusion

# 12 | Conclusion

15gr514 15 of 20

## 13 | Optimization

## 14 | Perspectivation

15gr514 17 of 20

# 15 | Discussion

#### Part IV

# Appendix

15gr514 19 of 20

#### Appendix

## A | Appendix

Somehting nice to know