

## Variables

no terminales

S -> L1 V1

V1 -> O V

V -> D | L

terminales

O -> =

L1 -> v

L -> a...z

D -> 0...9

## Función main y condicional

no terminales

S -> F A1

A1 -> T A2

A2 -> CA A3

A3 -> C CR

C -> U C1

C1 -> PA C2

C2 -> RC C3

C3 -> PC C4

C4 -> CA C5

C5 -> I CR

RC -> RC1 D

RC1 -> D CC

I -> I1 I2

I2 -> PA I3

I3 -> H I4

I4 -> L I5

I5 -> H PC

terminales

I1 -> impr

H -> “

F -> public fnc main

T -> ()

CA -> {

CR -> }

PA -> (

PC -> )

U -> si

CC -> <

ciclo

no terminales

S -> Y T1

T1 -> PA R1

R1 -> KD R2

R2 -> KA R3

KA -> D KA1

KA1 -> CC D

R3-> KS R4

R4 -> PC J

J -> CA J3

J3 -> I1 J4

J4 -> PA J5

J5 -> H J6

J6 -> L J7

J7 -> H J8

J8 -> PC CR

terminales

Y -> for

KD -> i=0;

K1 -> “

KS -> i++;

funcion

S -> LL B4

B4 -> T B6

B6 -> CA CR

LL -> t