

Name	Path	Data Type	Logical Address
Start_PB	Inputs	Bool	%I0.0
A_LS_Ret	Inputs	Bool	%I0.1
A_LS_Ext	Inputs	Bool	%I0.2
B_LS_Ret	Inputs	Bool	%I0.3
B_LS_Ext	Inputs	Bool	%I0.4
C_LS_Ret	Inputs	Bool	%I0.5
C_LS_Ext	Inputs	Bool	%I0.6
EStop_OK	Inputs	Bool	%I0.7
Auto_Cycle	Inputs	Bool	%I1.0
Part_Sensor	Inputs	Bool	%I1.1
CmdA_Ext	Outputs	Bool	%Q0.0
CmdA_Ret	Outputs	Bool	%Q0.1
CmdB_Ext	Outputs	Bool	%Q0.2
CmdB_Ret	Outputs	Bool	%Q0.3
CmdC_Ext	Outputs	Bool	%Q0.4
CmdC_Ret	Outputs	Bool	%Q0.5
CmdVac_On	Outputs	Bool	%Q0.6
M1_Run	Outputs	Bool	%Q0.7
M2_Run	Outputs	Bool	%Q1.0
S0_Idle	Marks	Bool	%M0.0
S10	Marks	Bool	%M0.1
S20	Marks	Bool	%M0.2
S30	Marks	Bool	%M0.3
S40	Marks	Bool	%M0.4
S50	Marks	Bool	%M0.5
S60	Marks	Bool	%M0.6
S100_End	Marks	Bool	%M0.7
S999_Fault	Marks	Bool	%M1.0
Start_PE	Marks	Bool	%M1.1
Busy	Marks	Bool	%M1.2
Done	Marks	Bool	%M1.3
Fault	Marks	Bool	%M1.4
Global_Enabl	Marks	Bool	%M1.5