

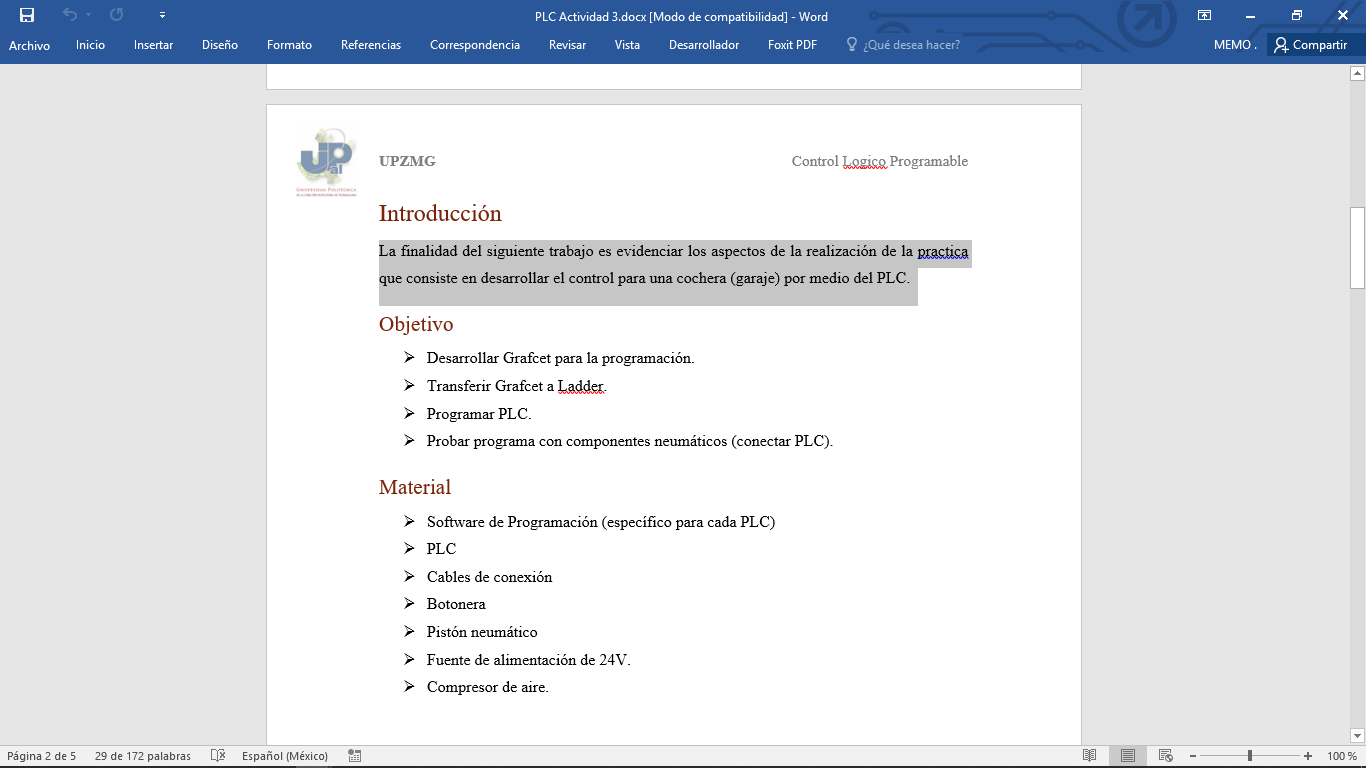
Actividad 3

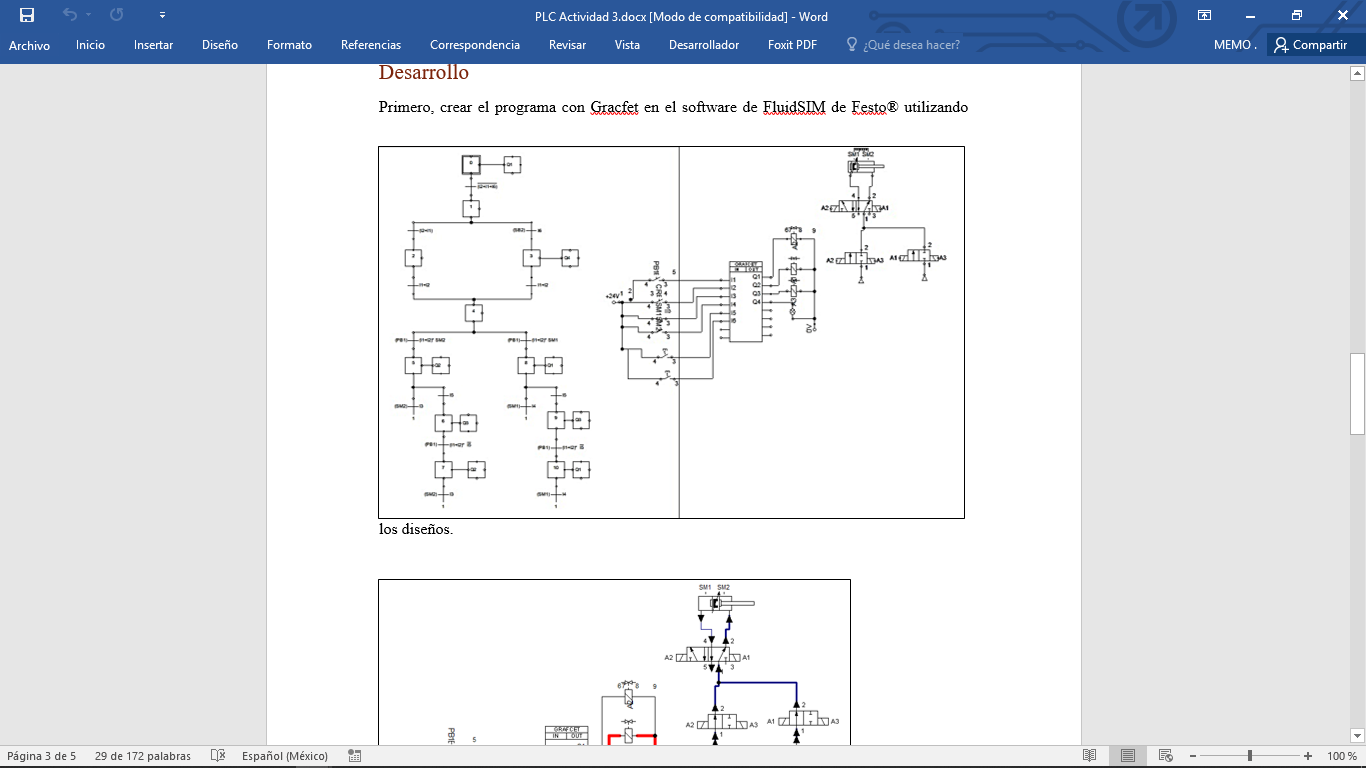
Controladores lógicos programables

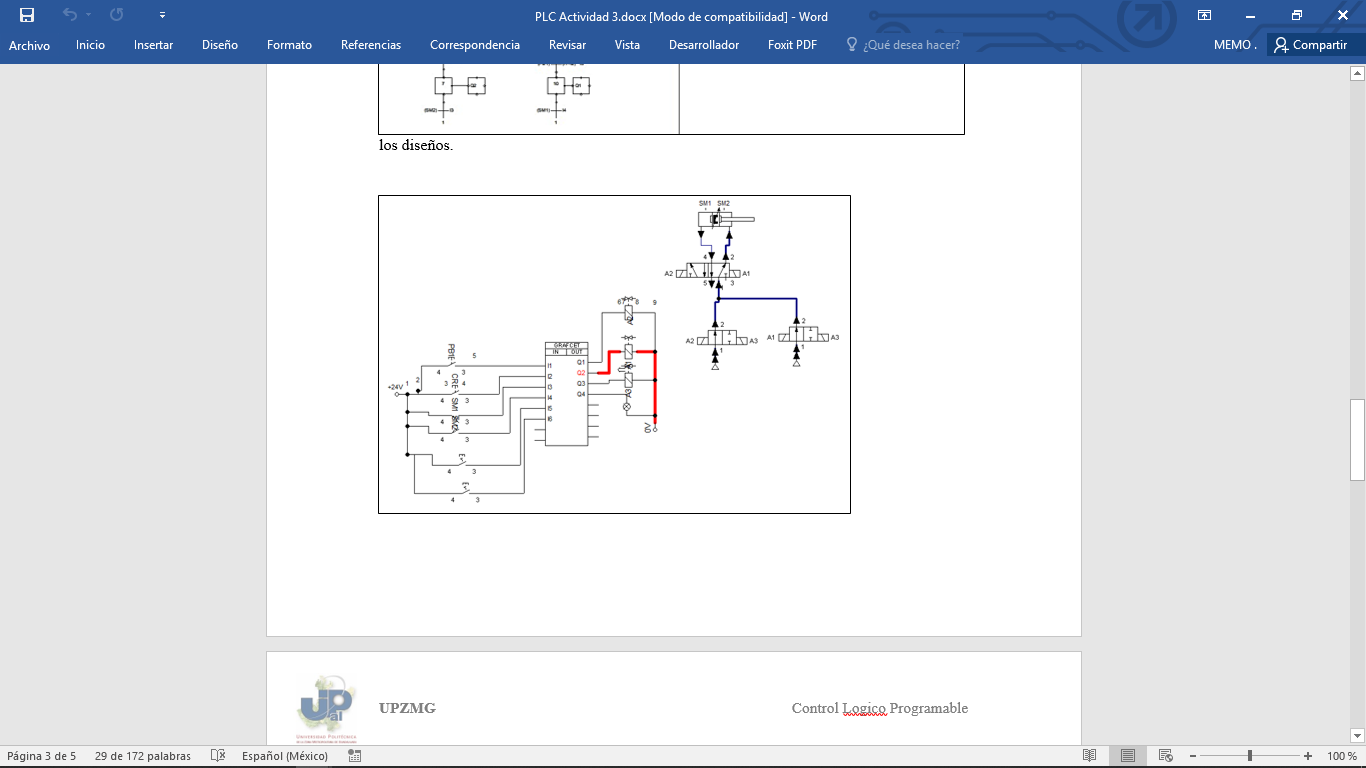
Diego Armando Becerra Iñiguez

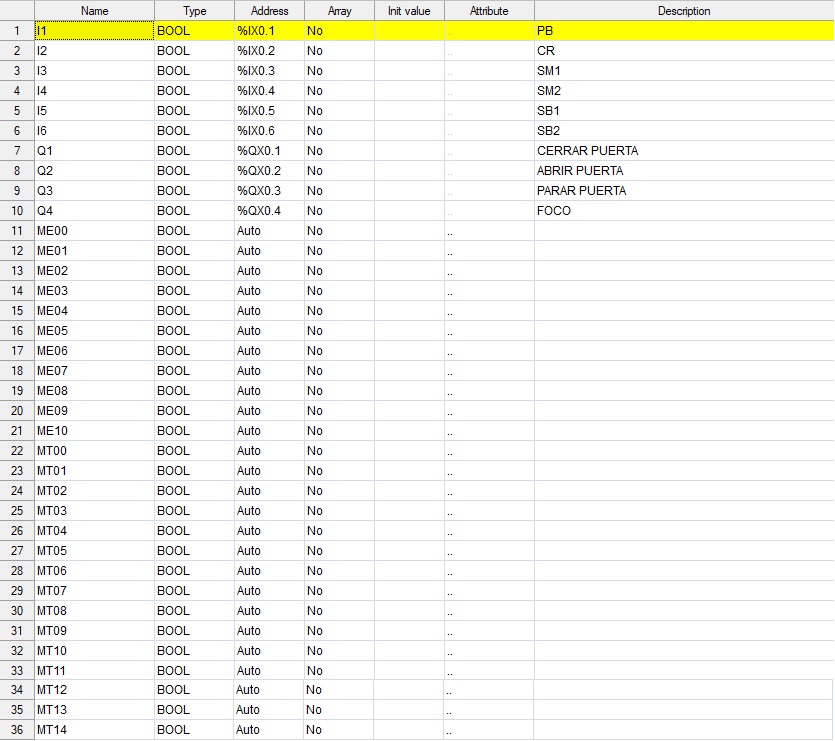
5-A

15/03/19

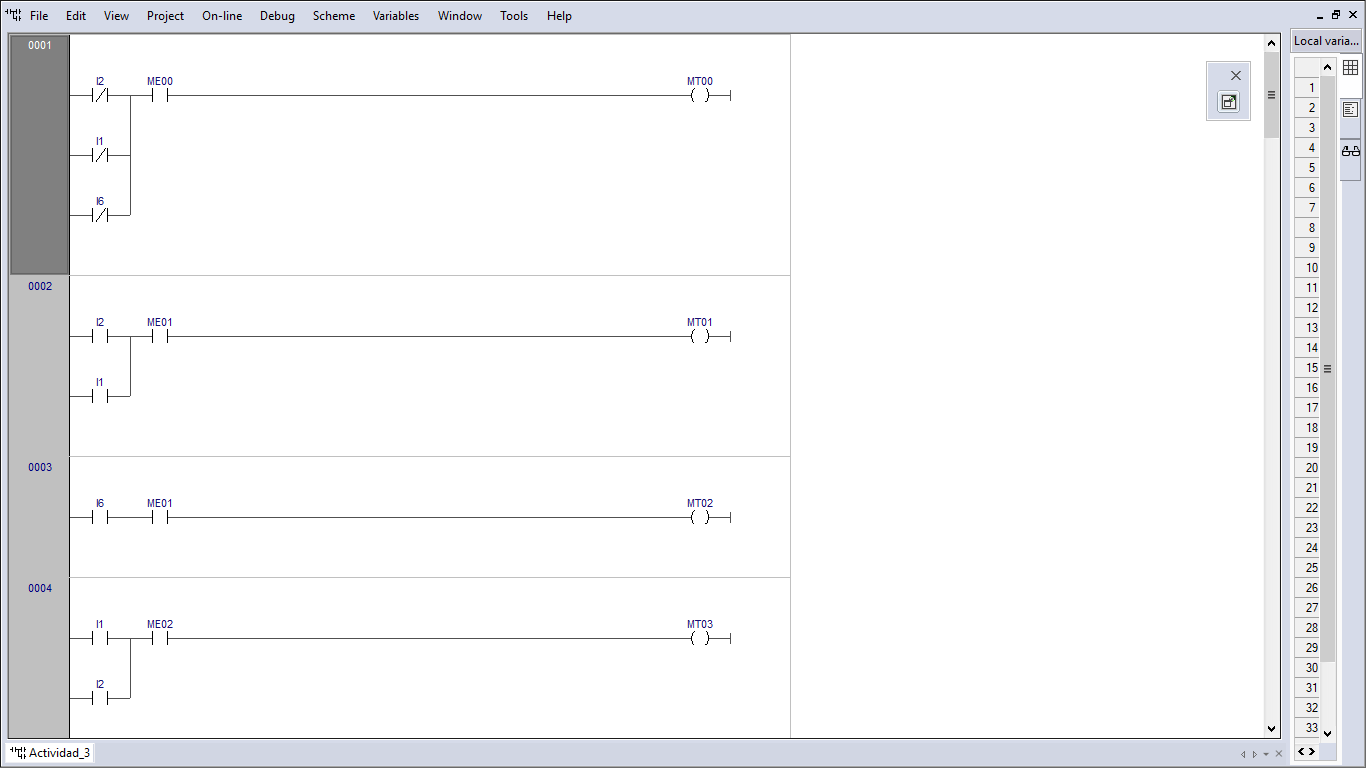


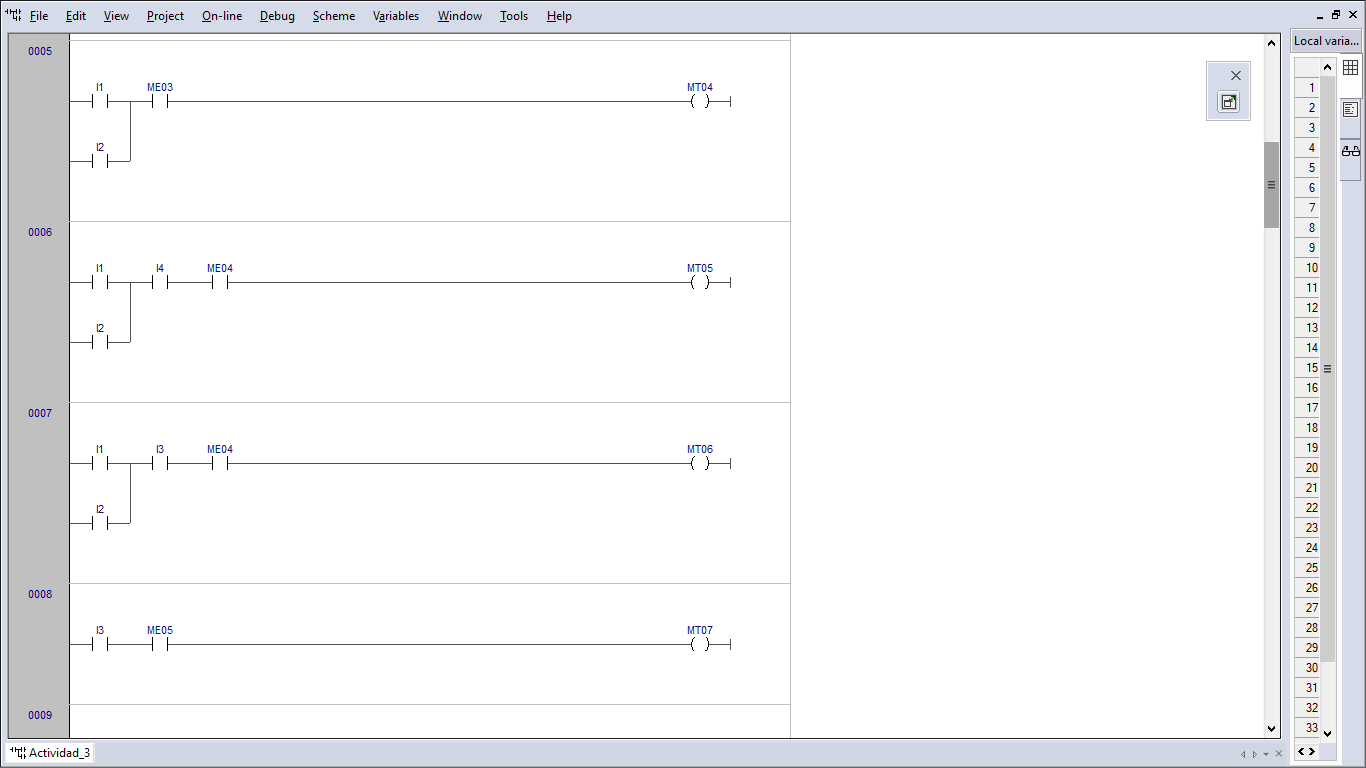
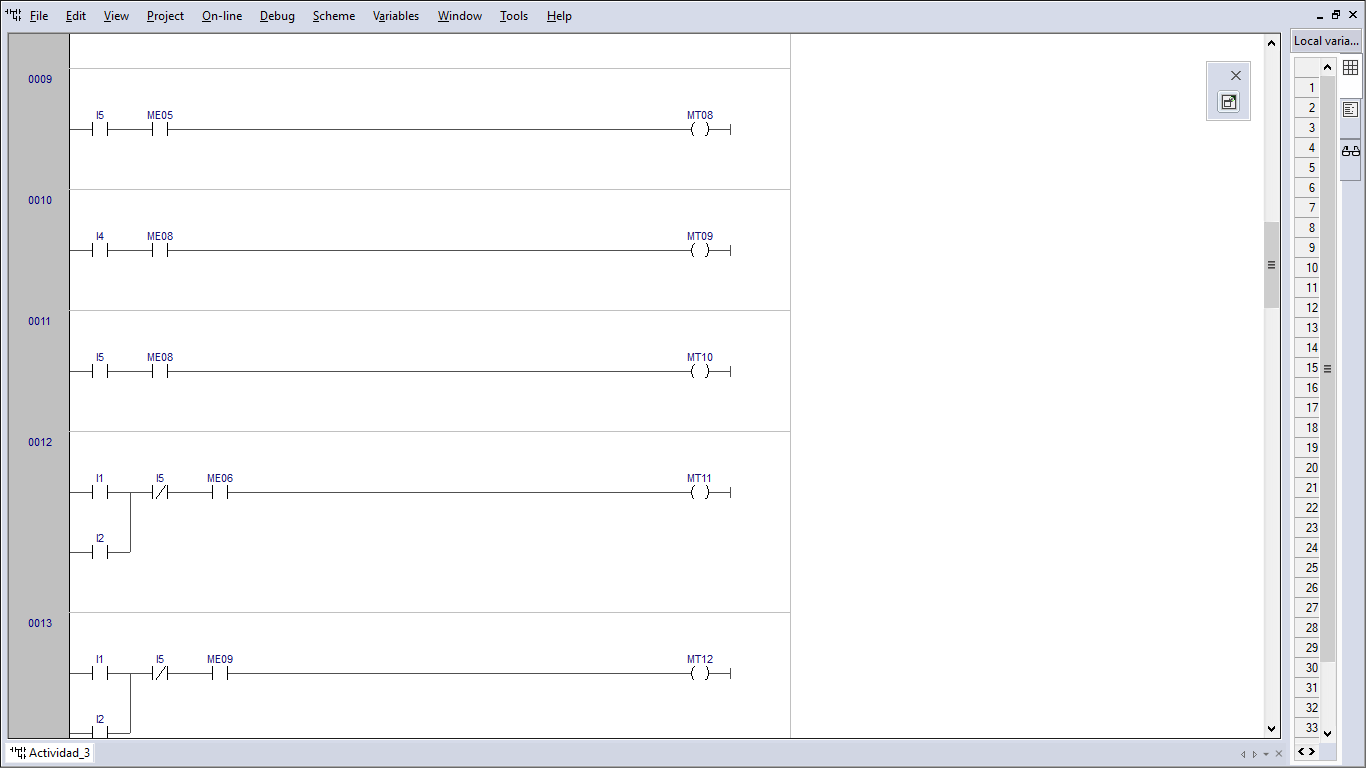


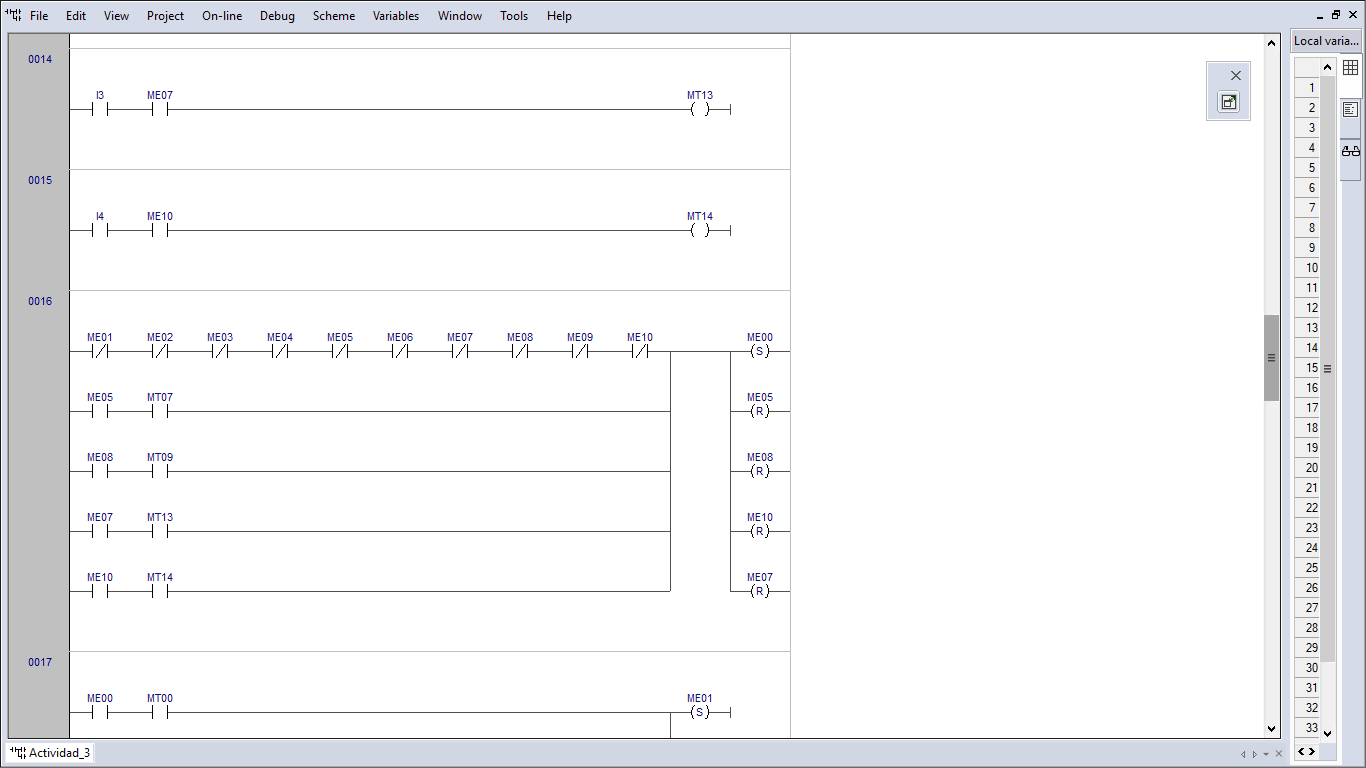
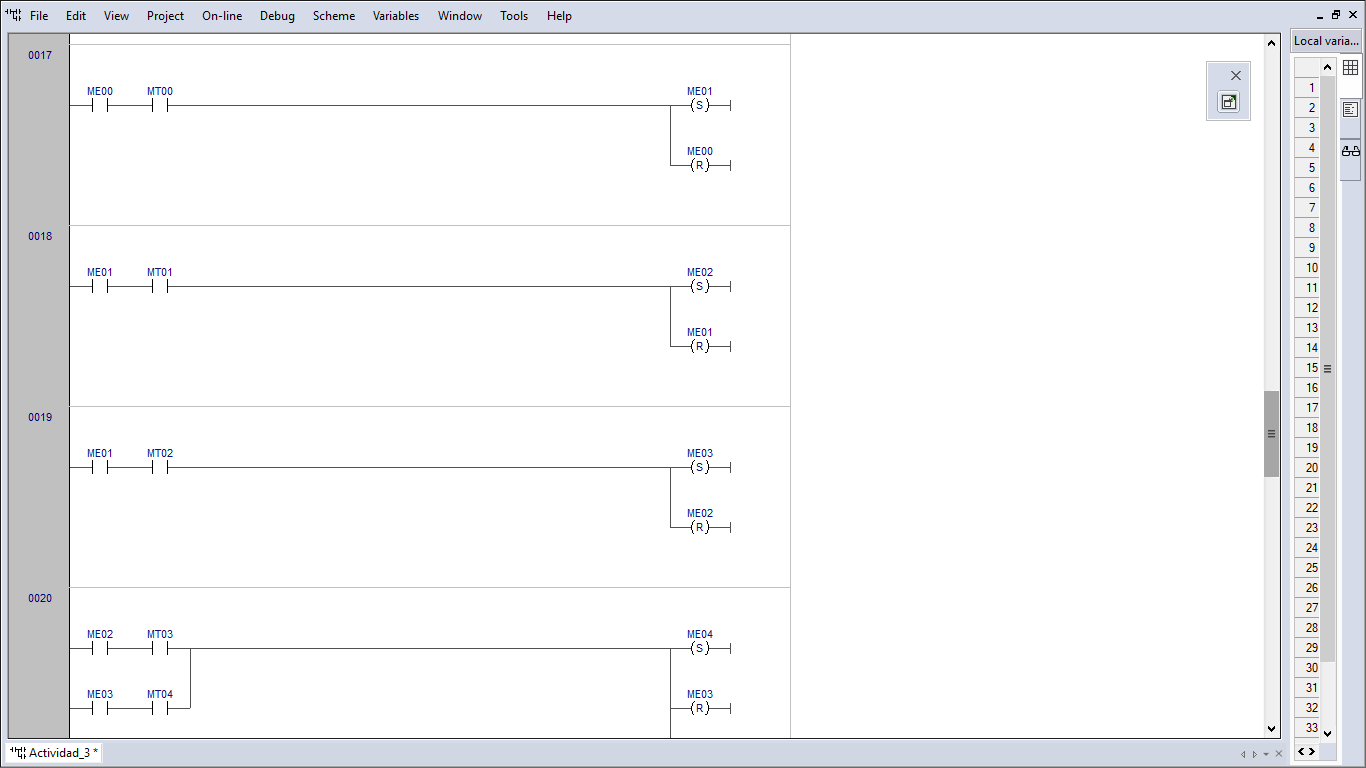


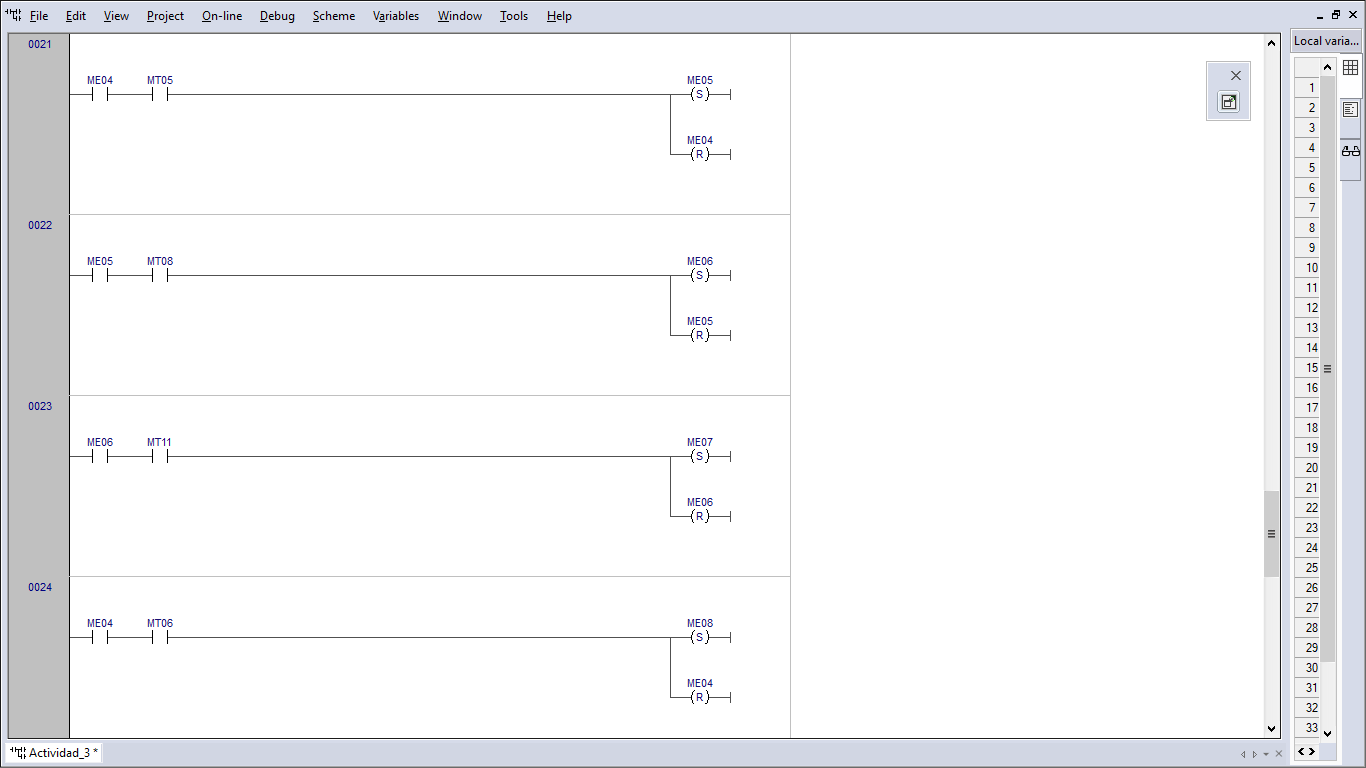
Entradas, salidas y memorias.

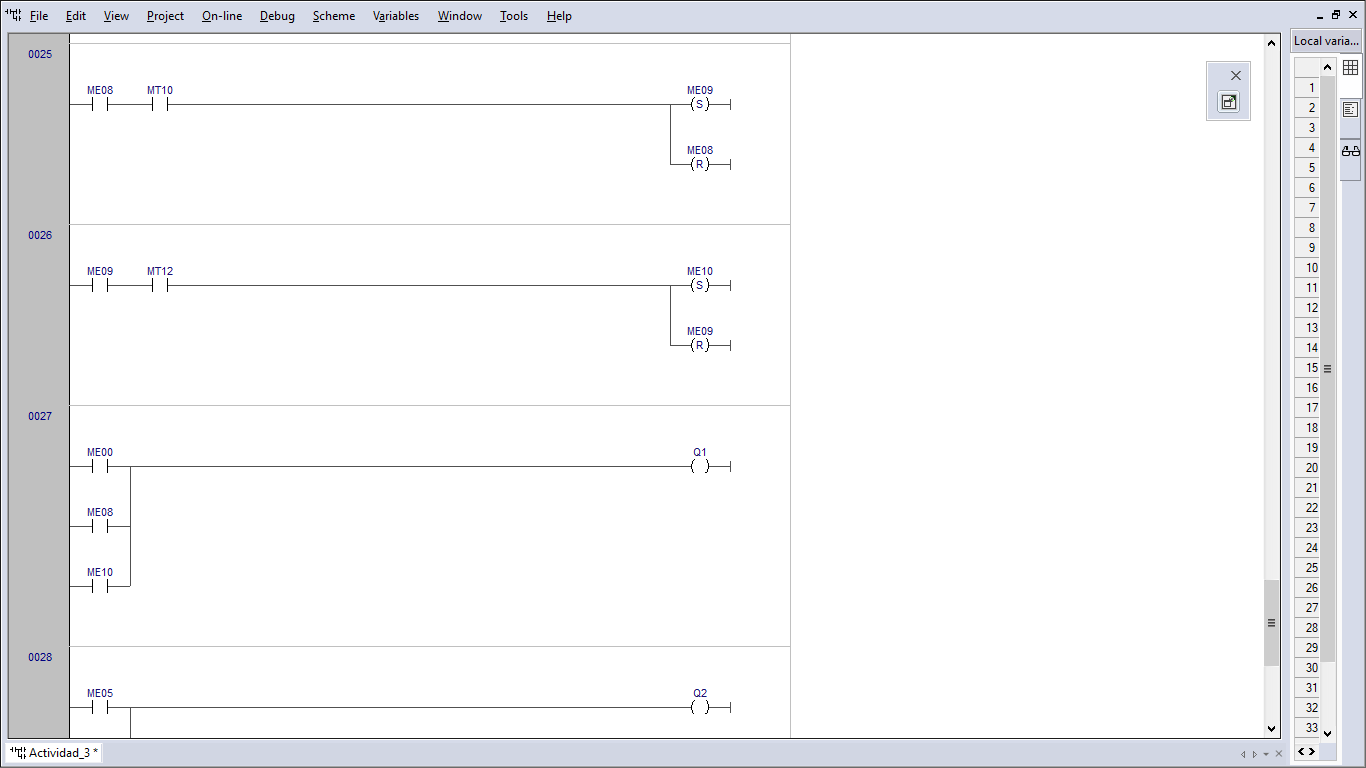
|  |  |
| --- | --- |
| Sensores | Actuadores |
| I1 | Q1 |
| I2 | Q2 |
| I3 | Q3 |
| I4 | Q4 |
| I5 |  |
| I6 |  |

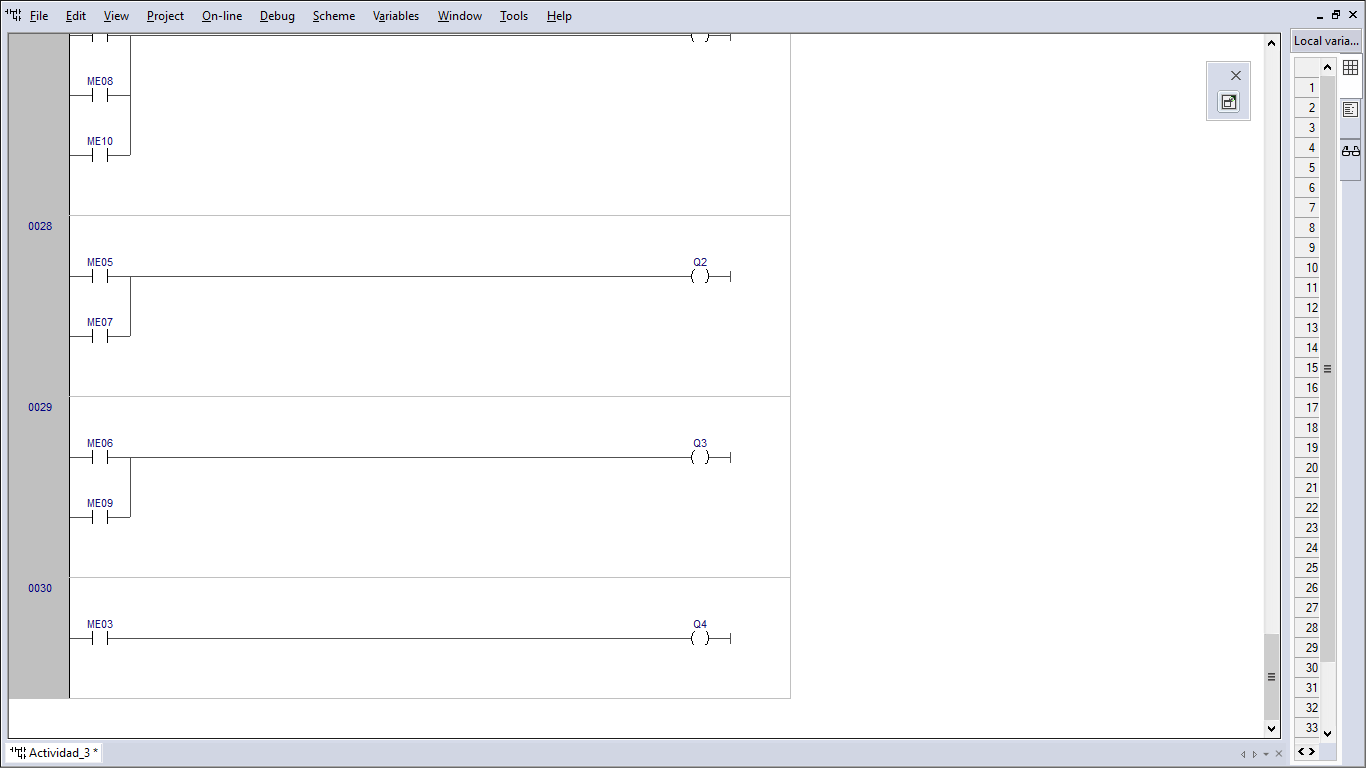
Programación en escalera



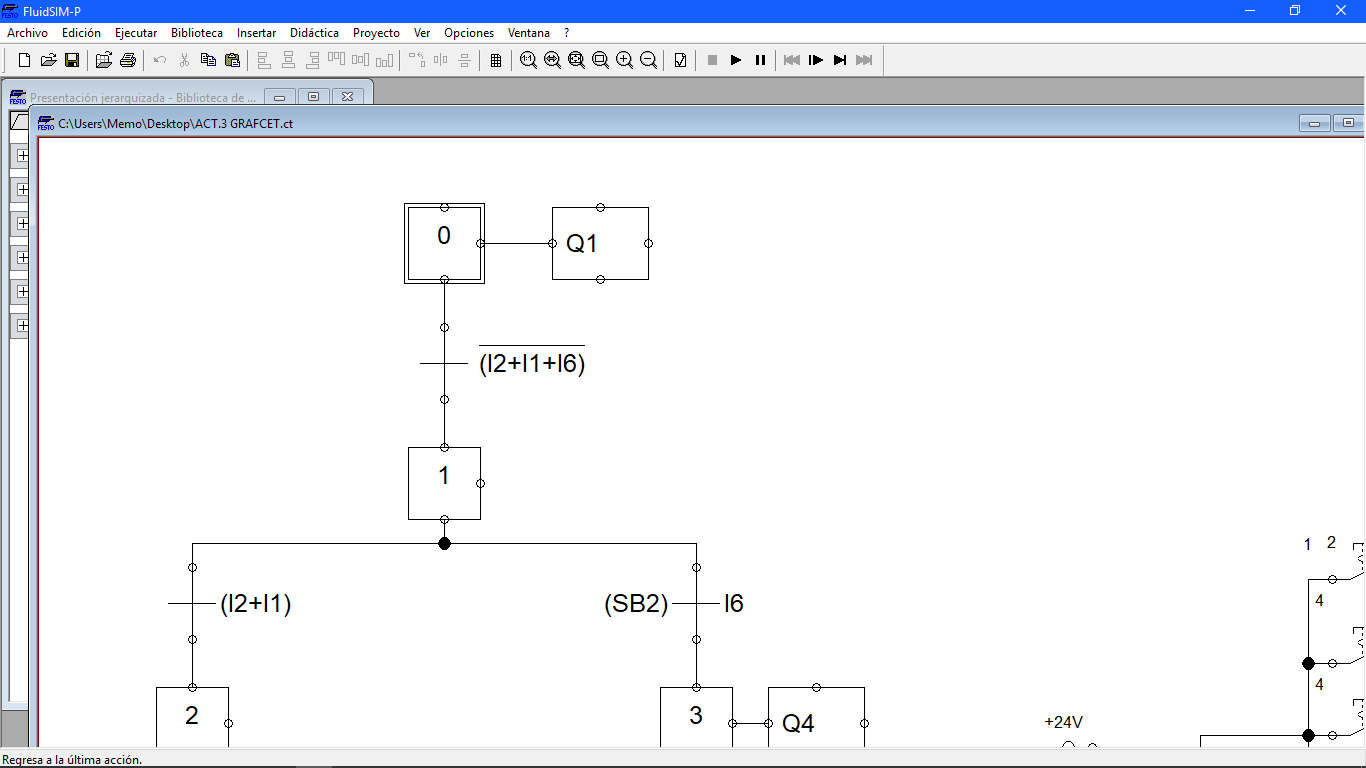




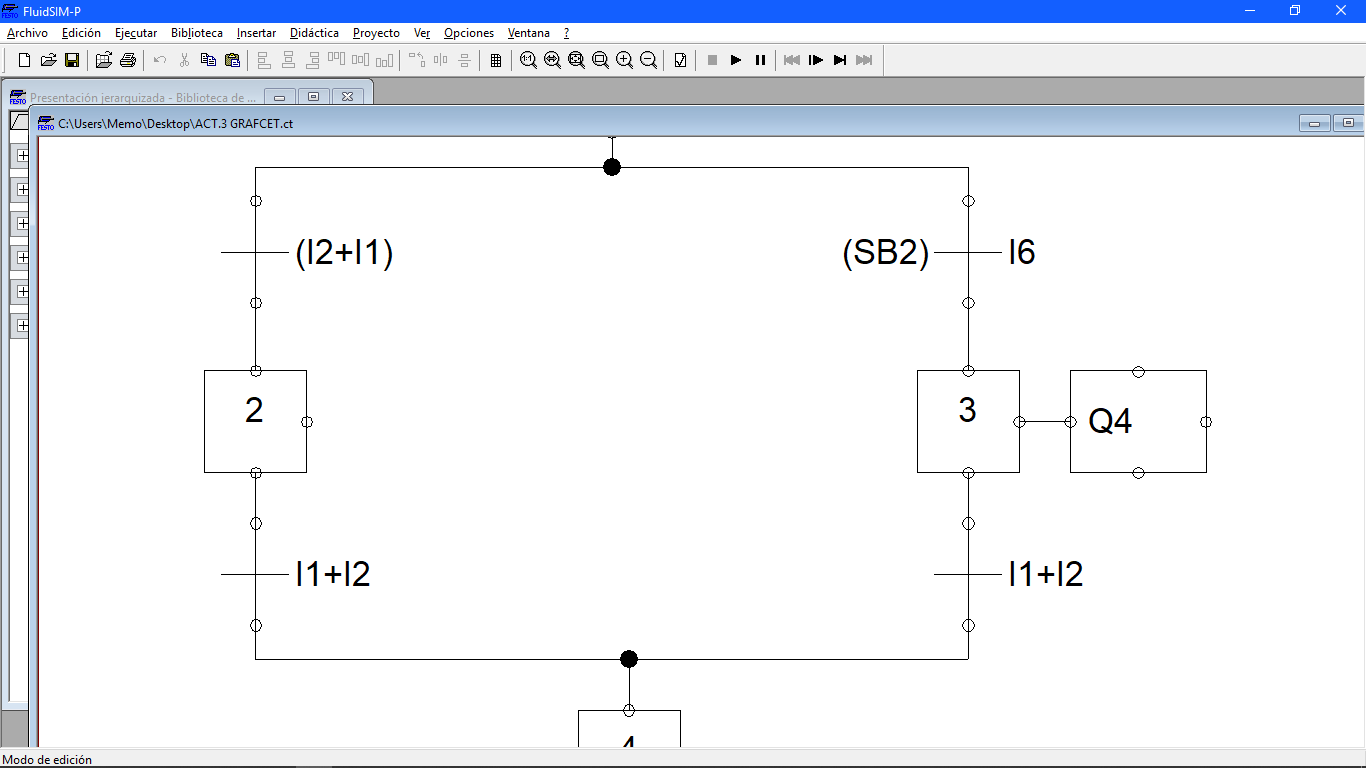


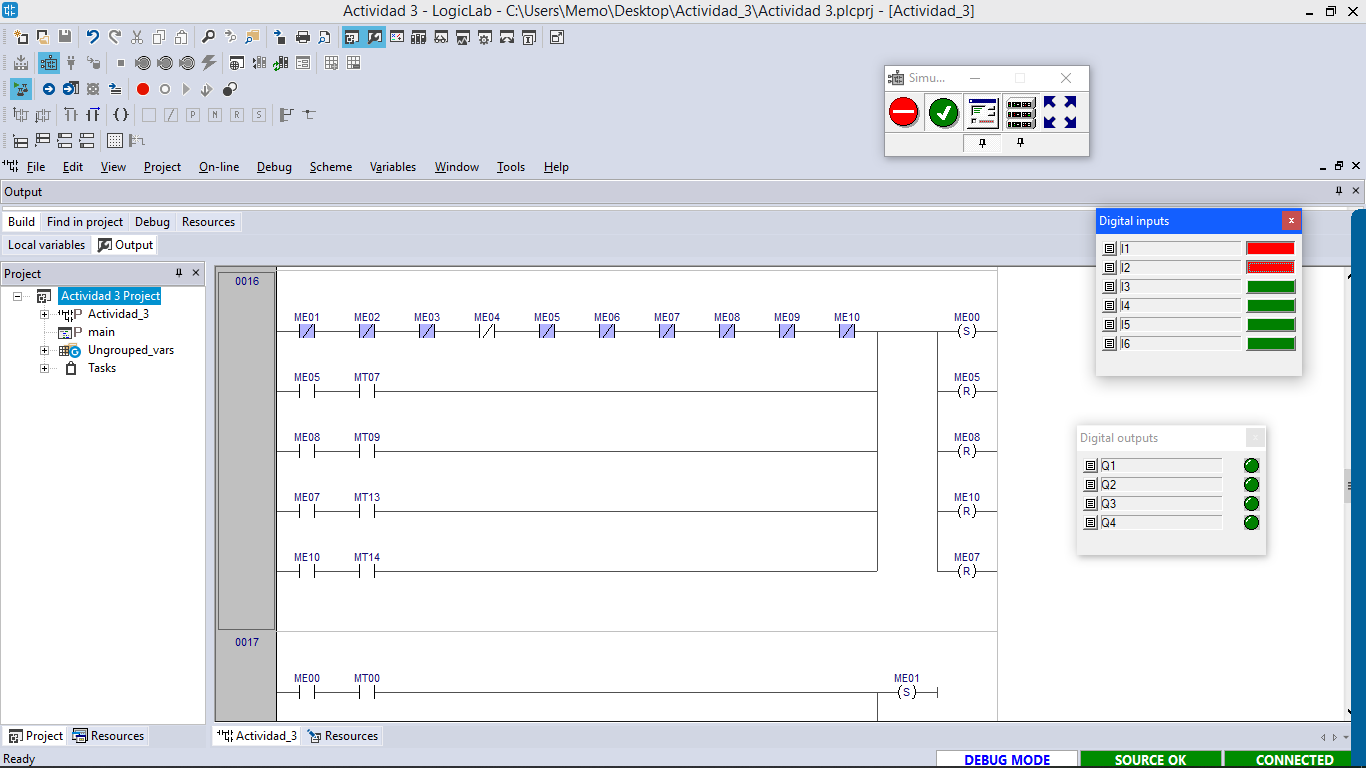


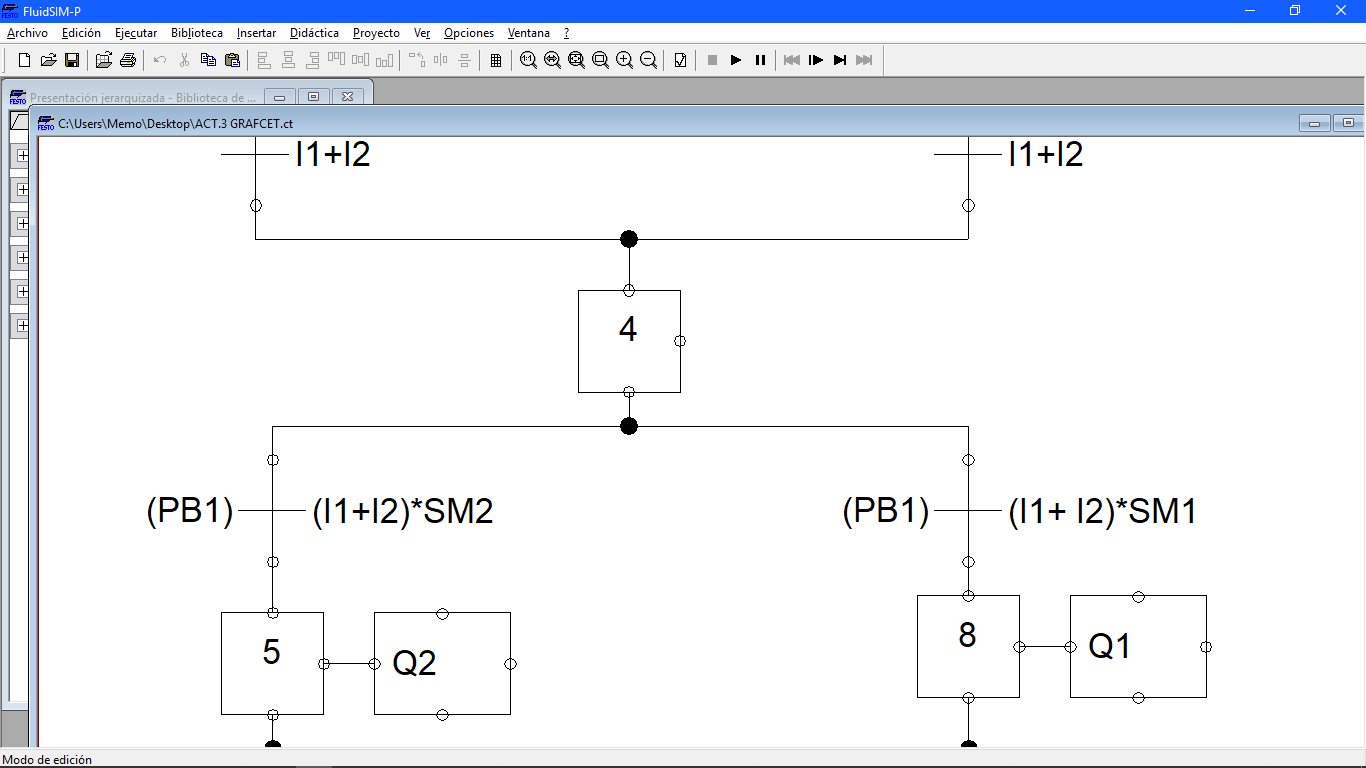
Simulación

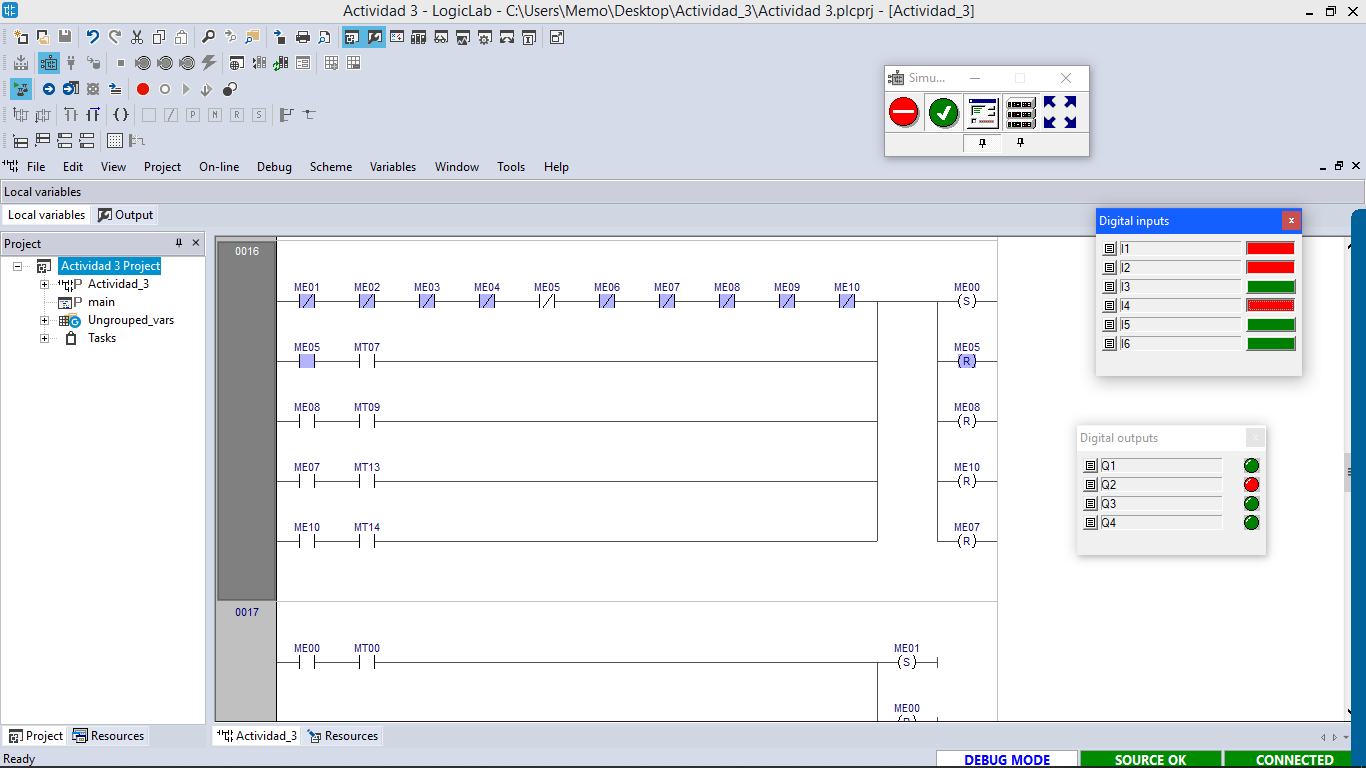


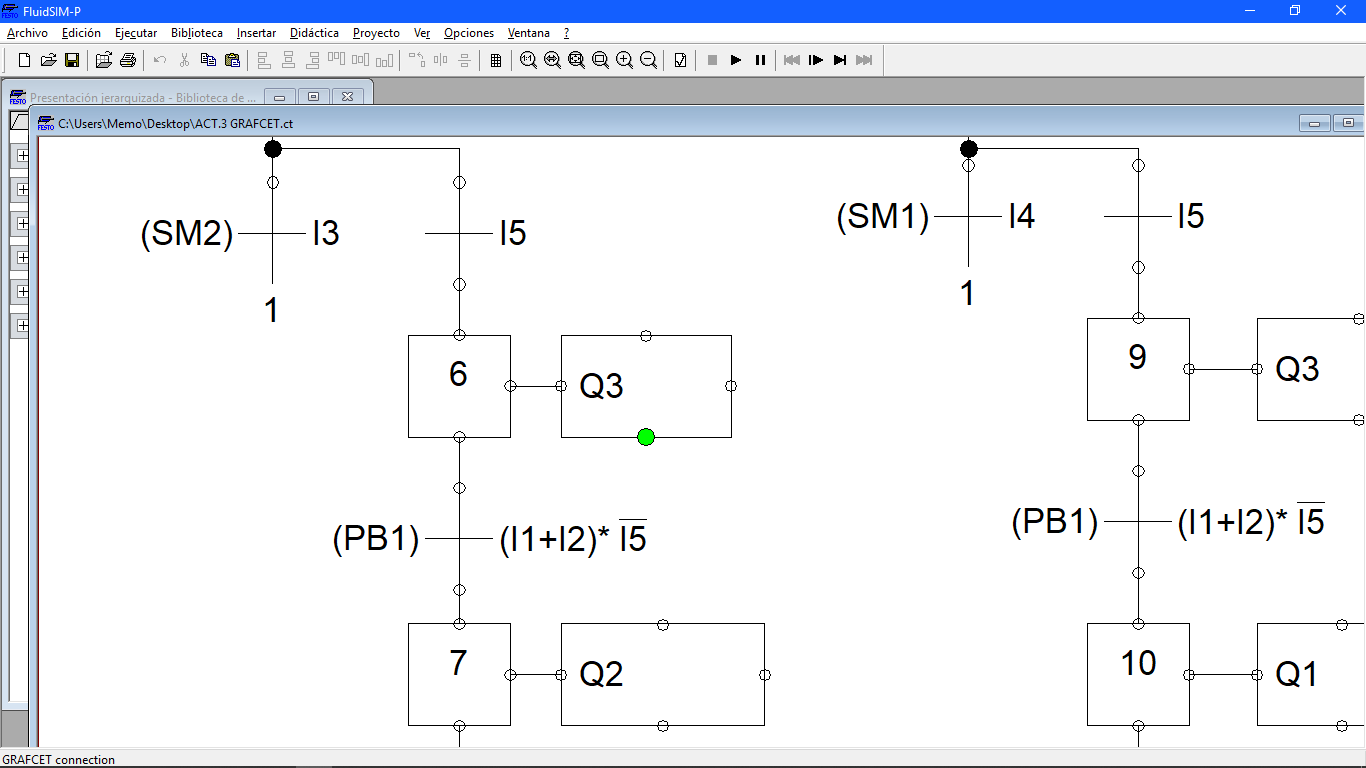


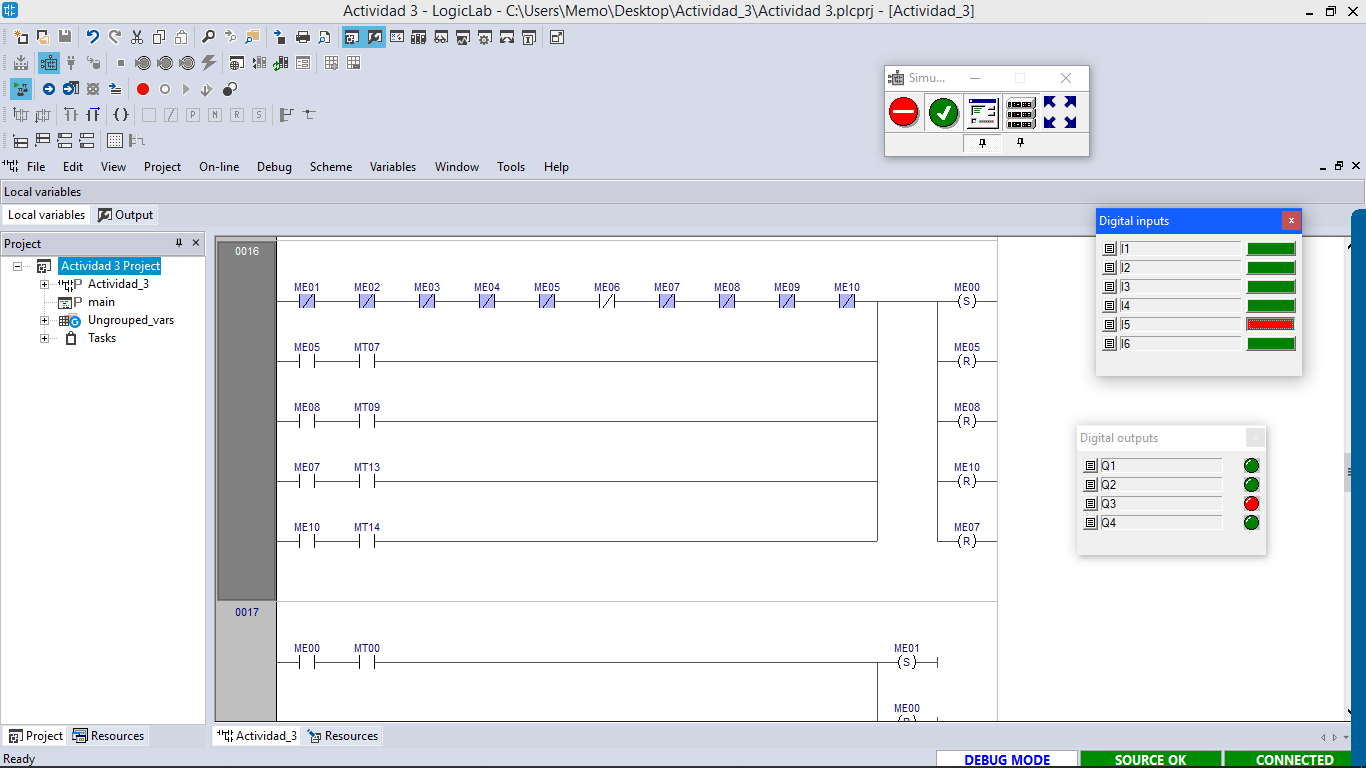


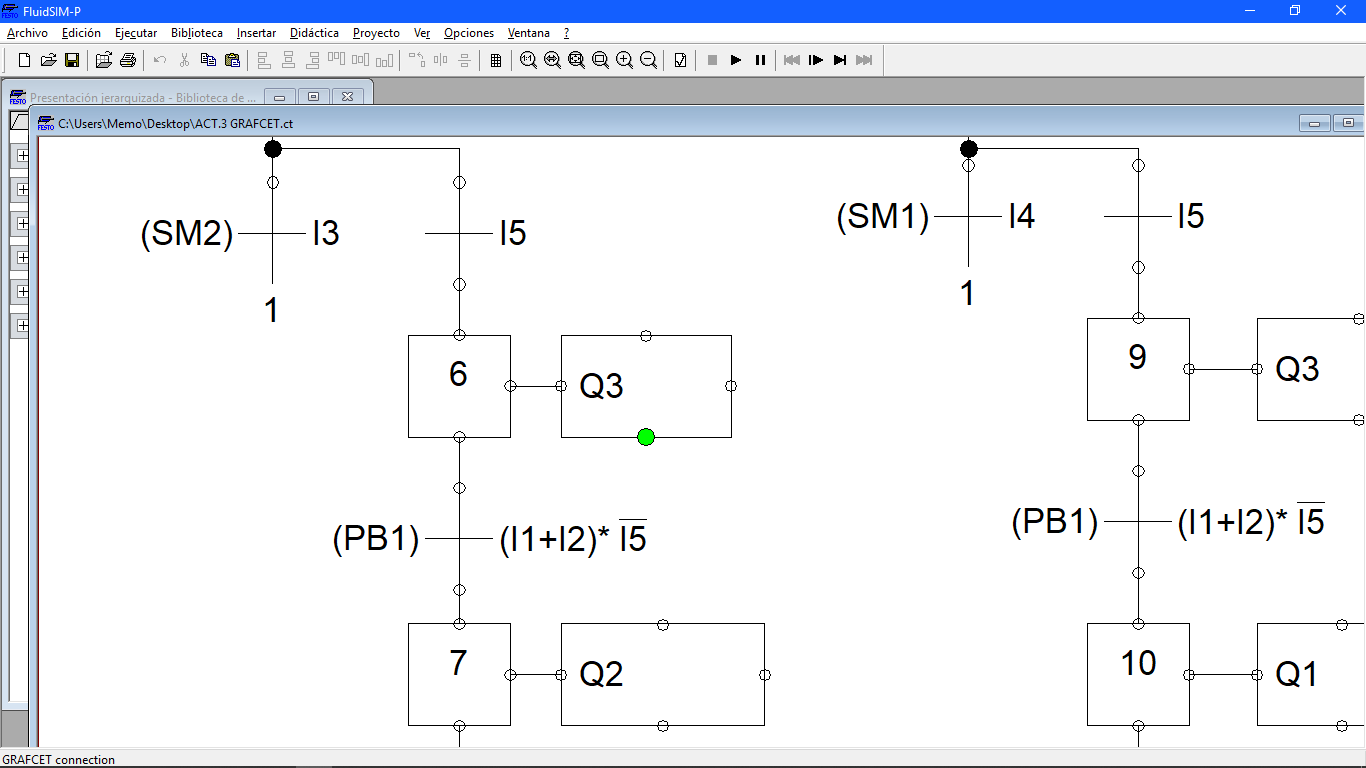


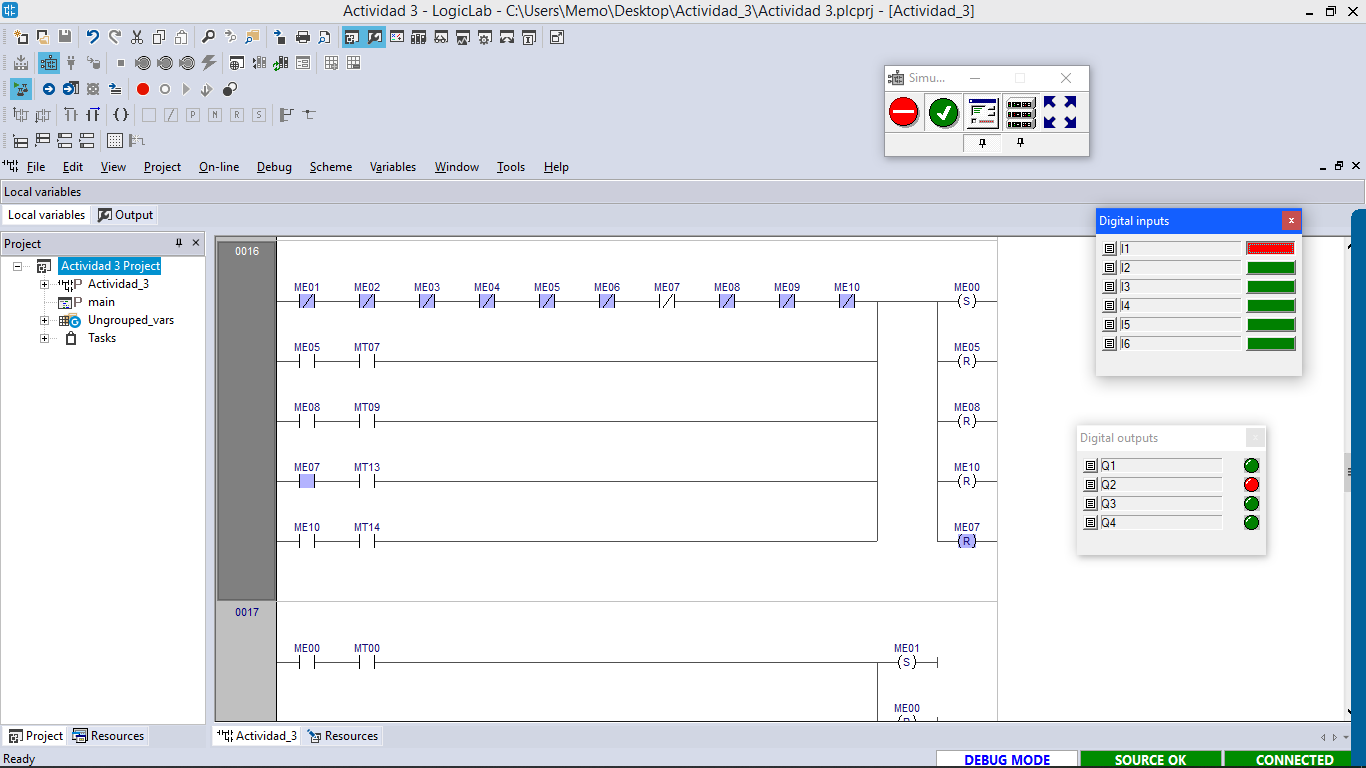


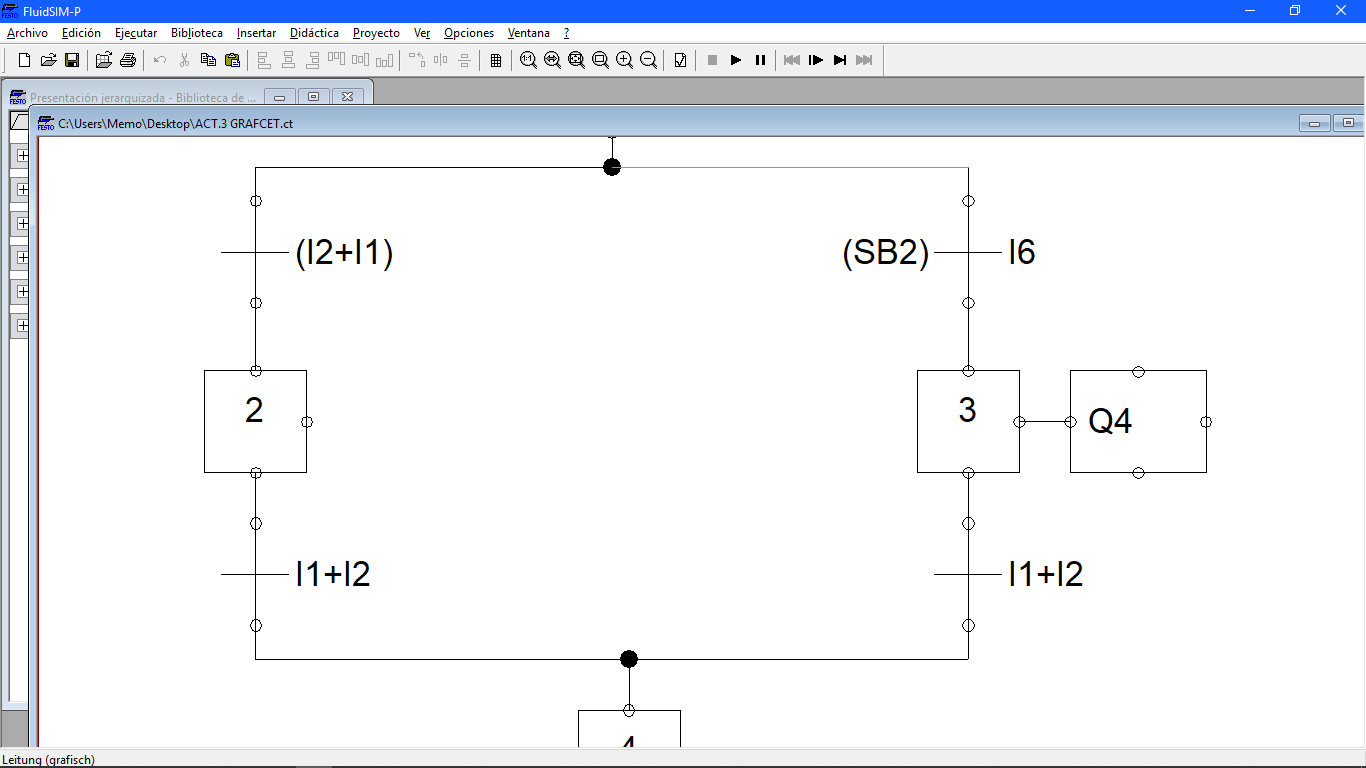


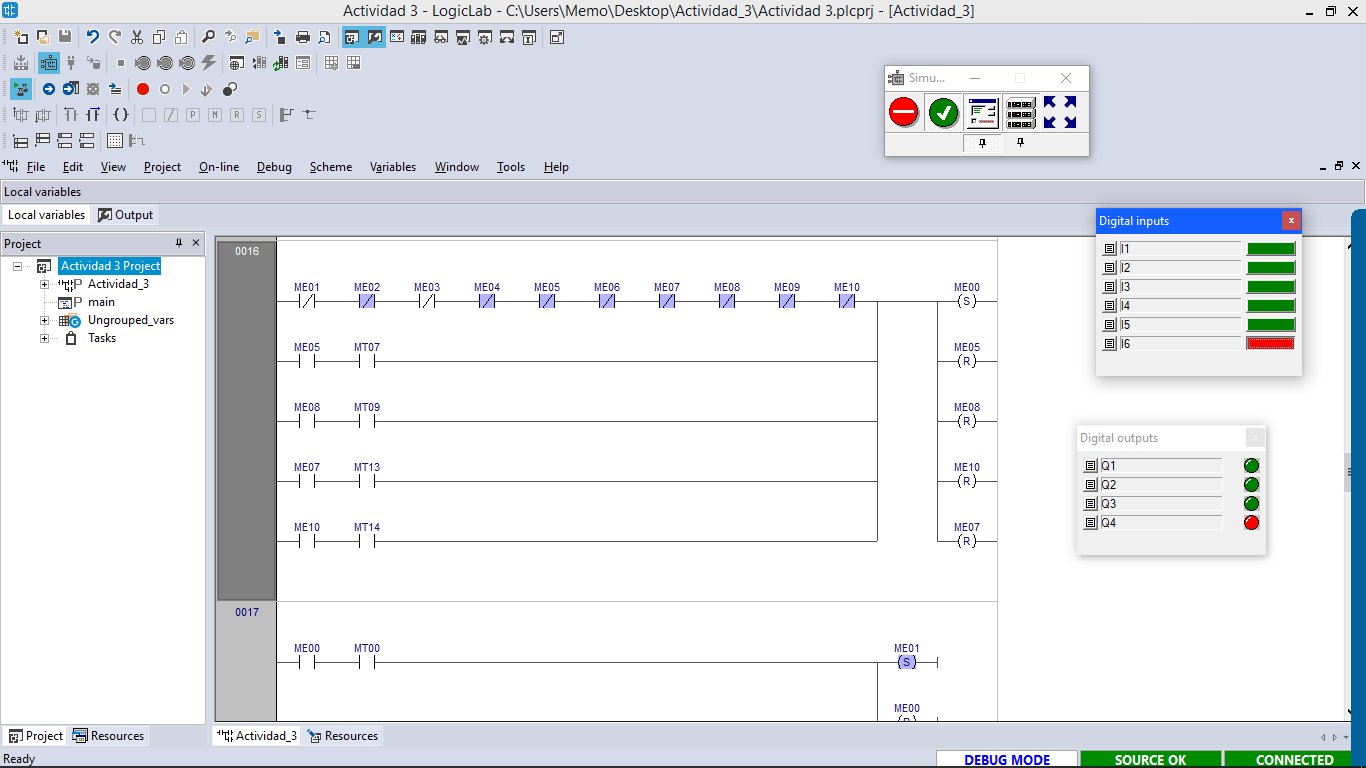


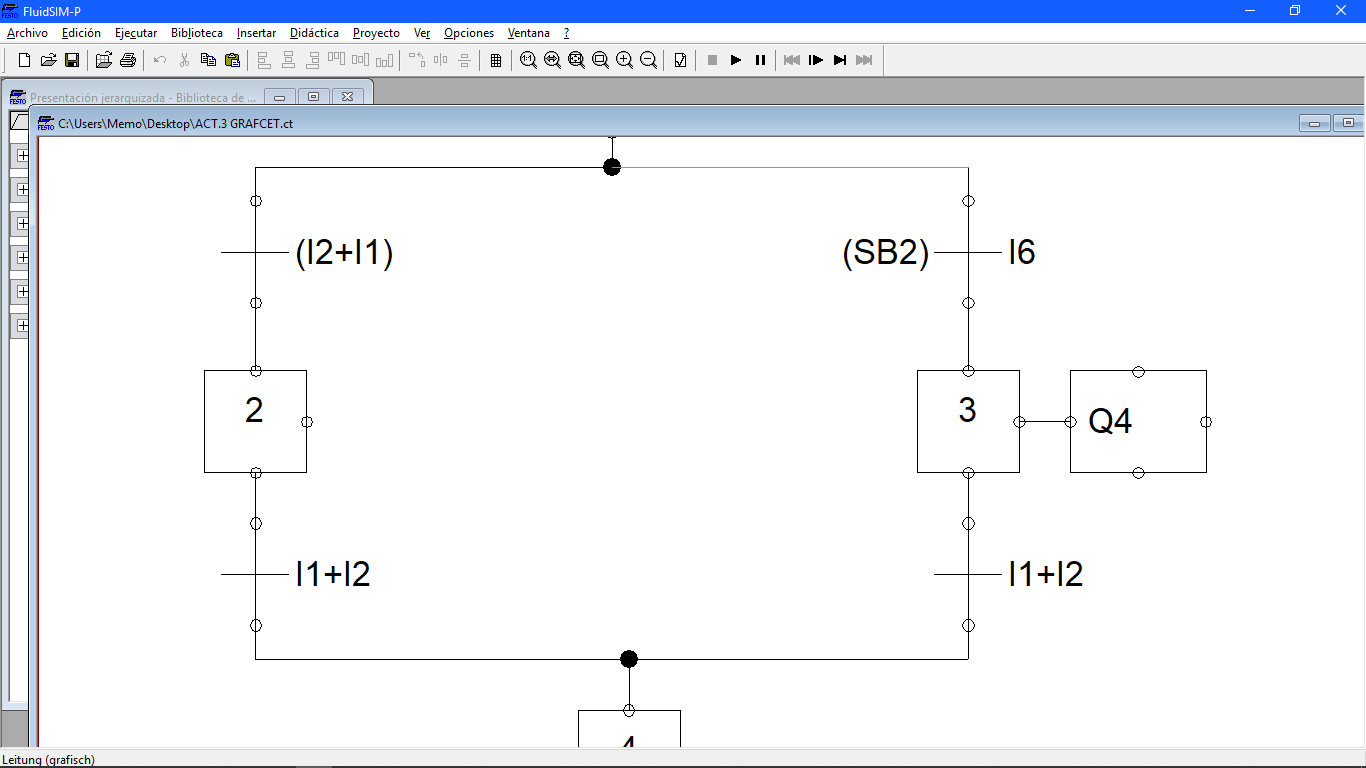


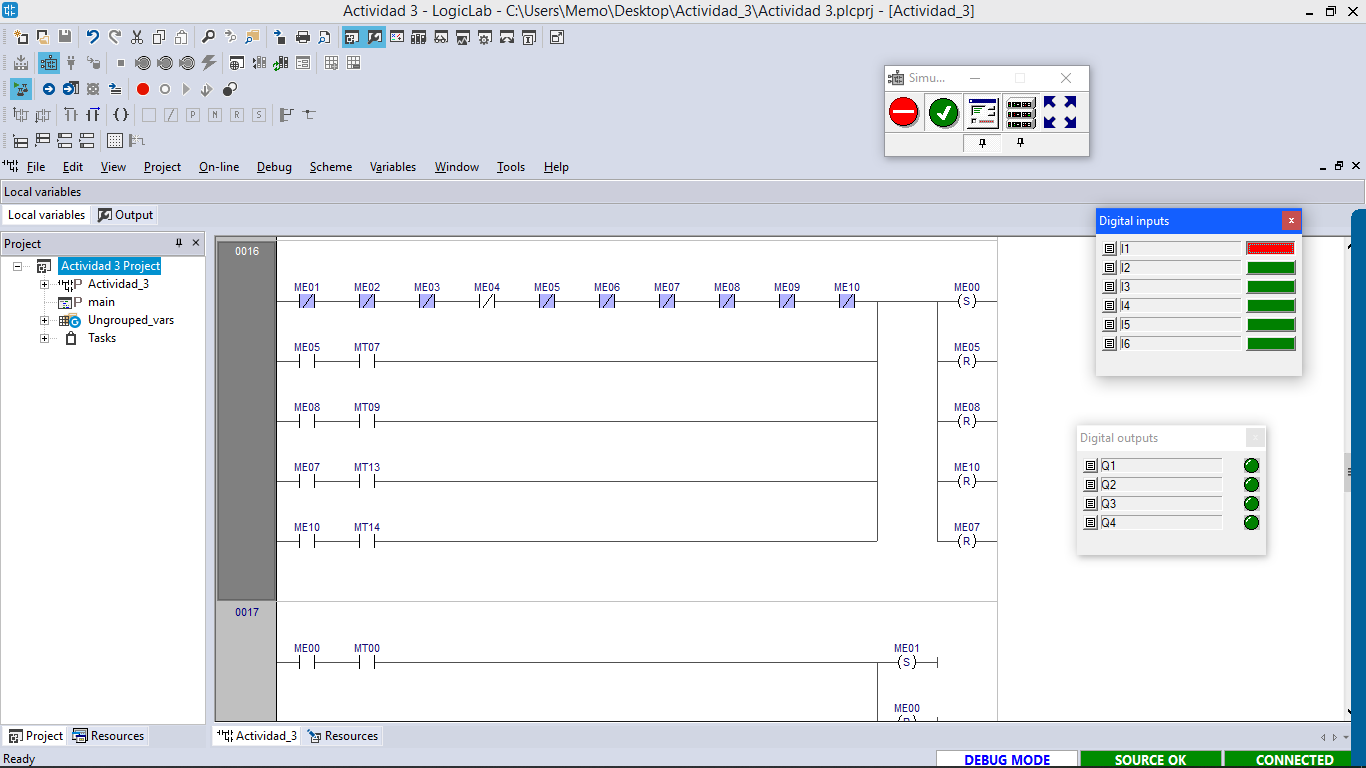


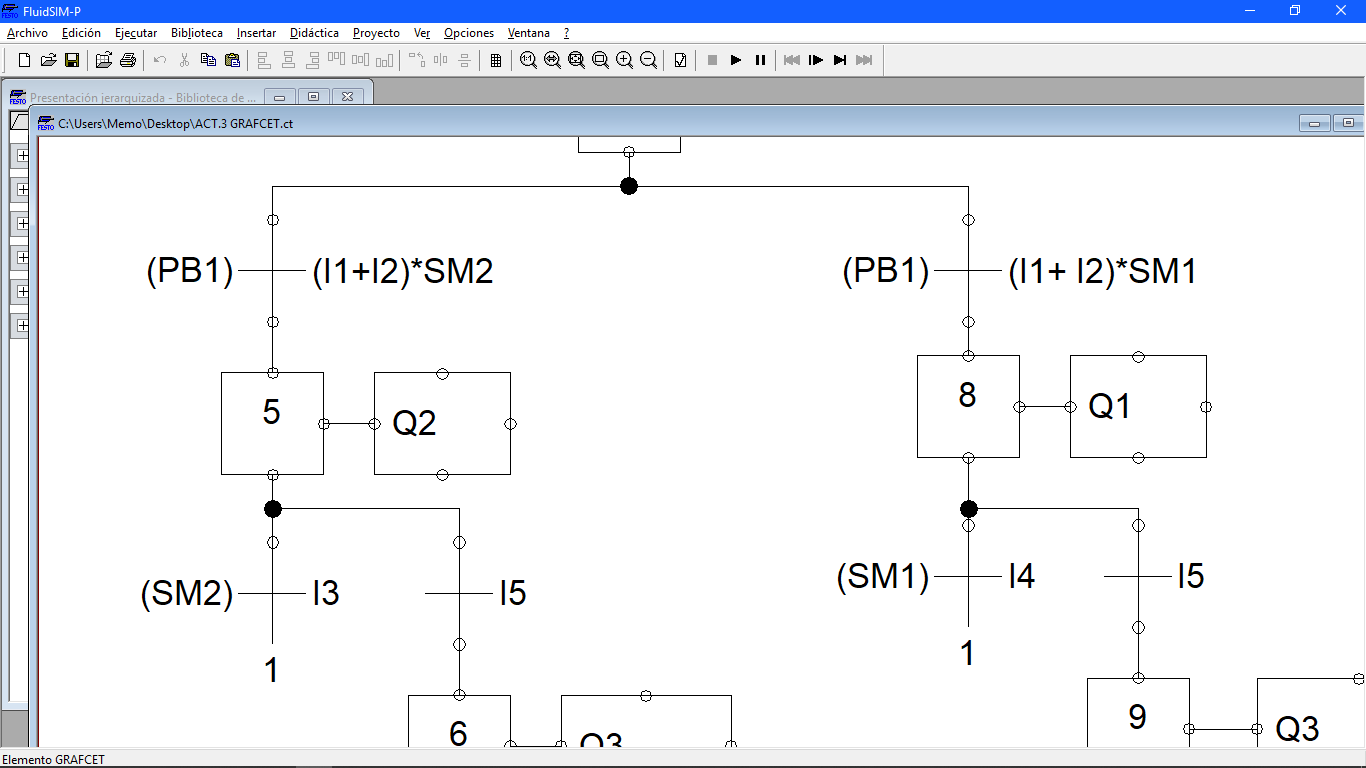


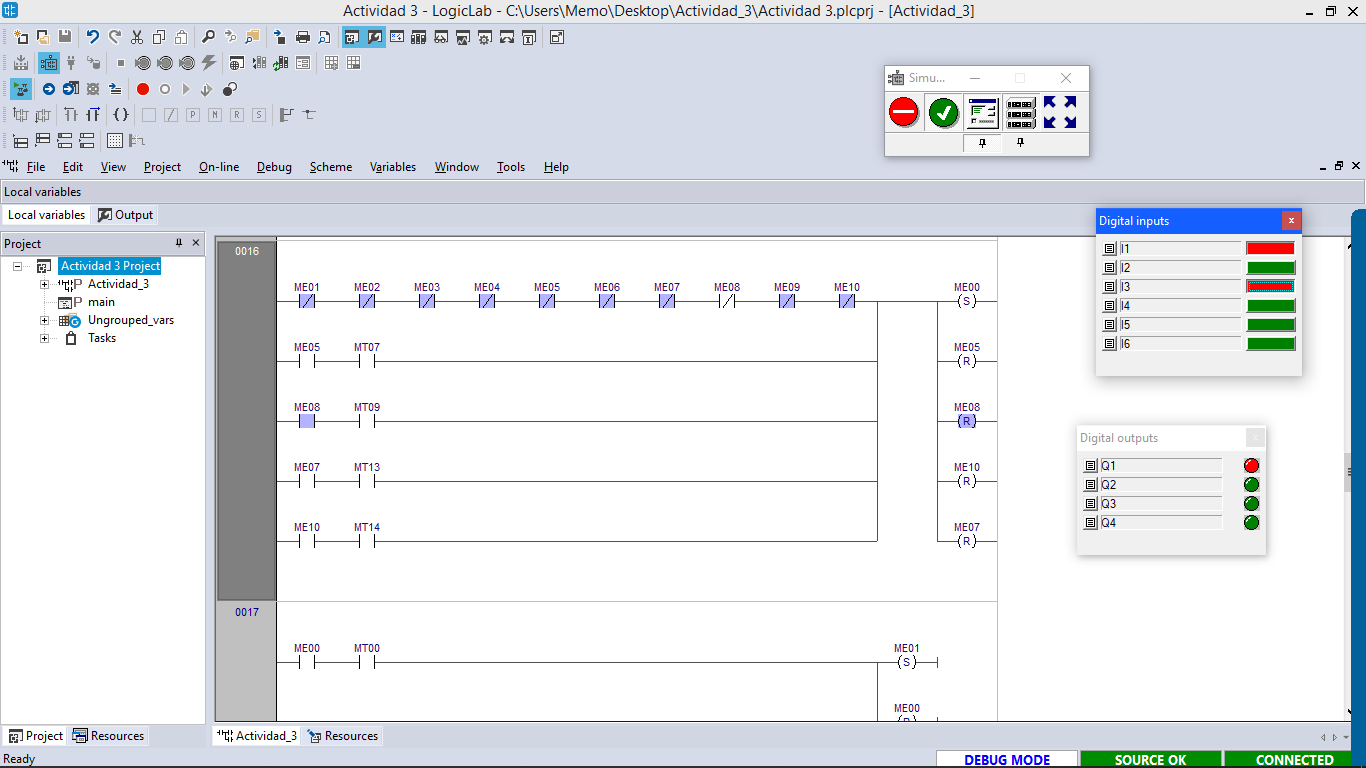


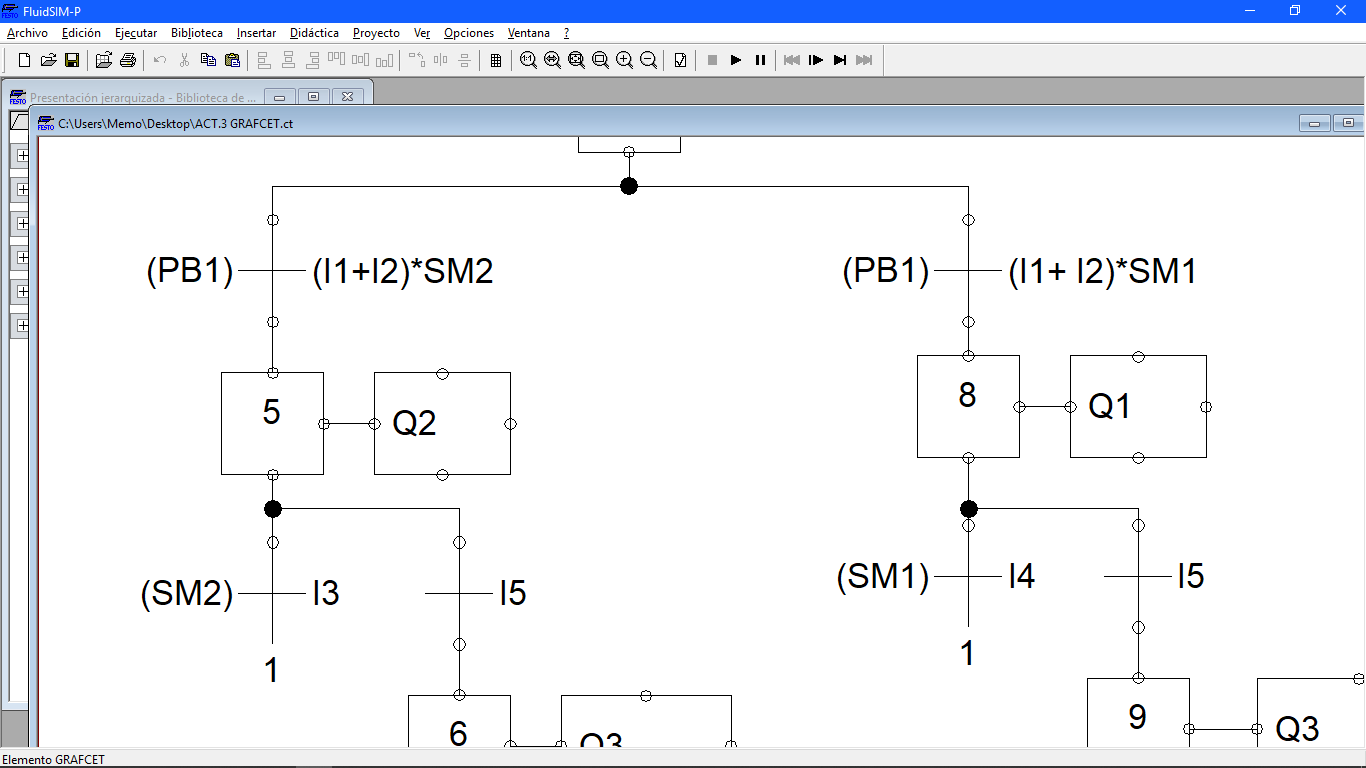


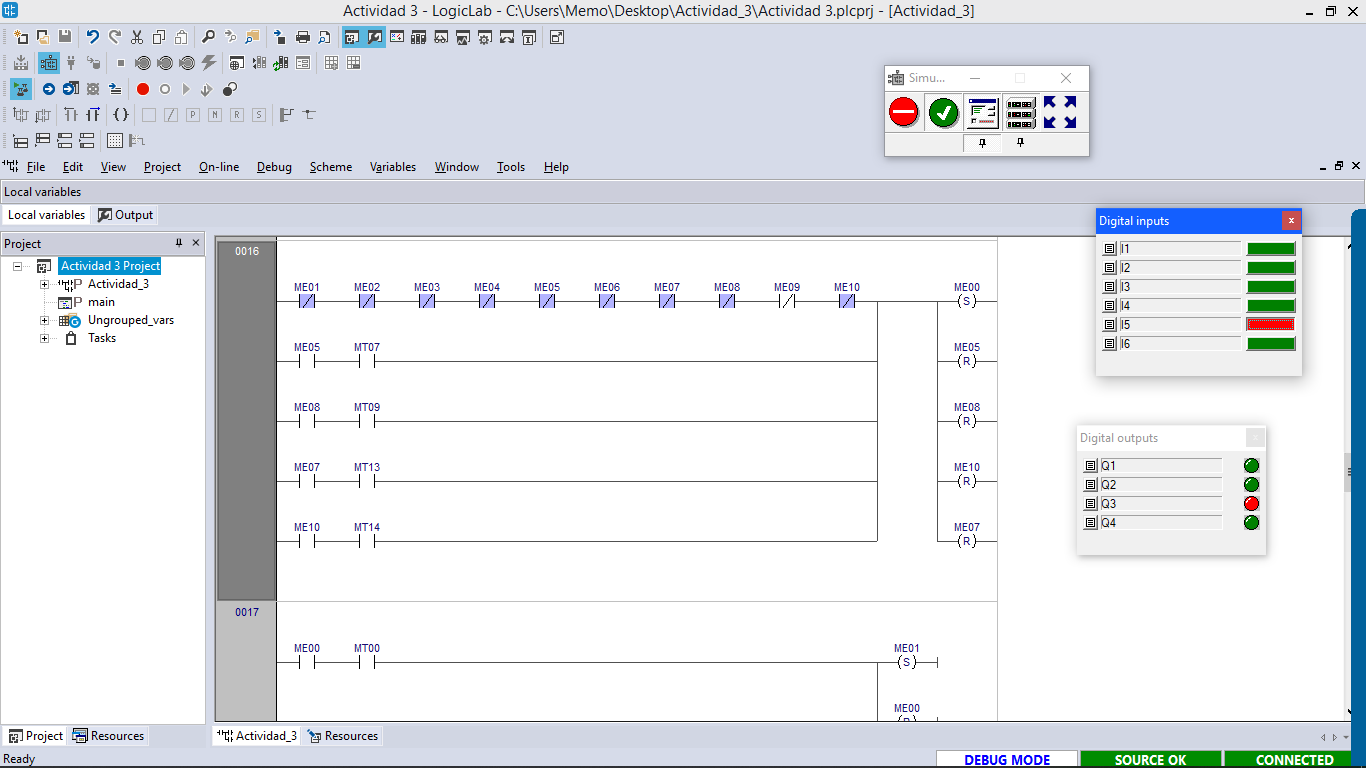


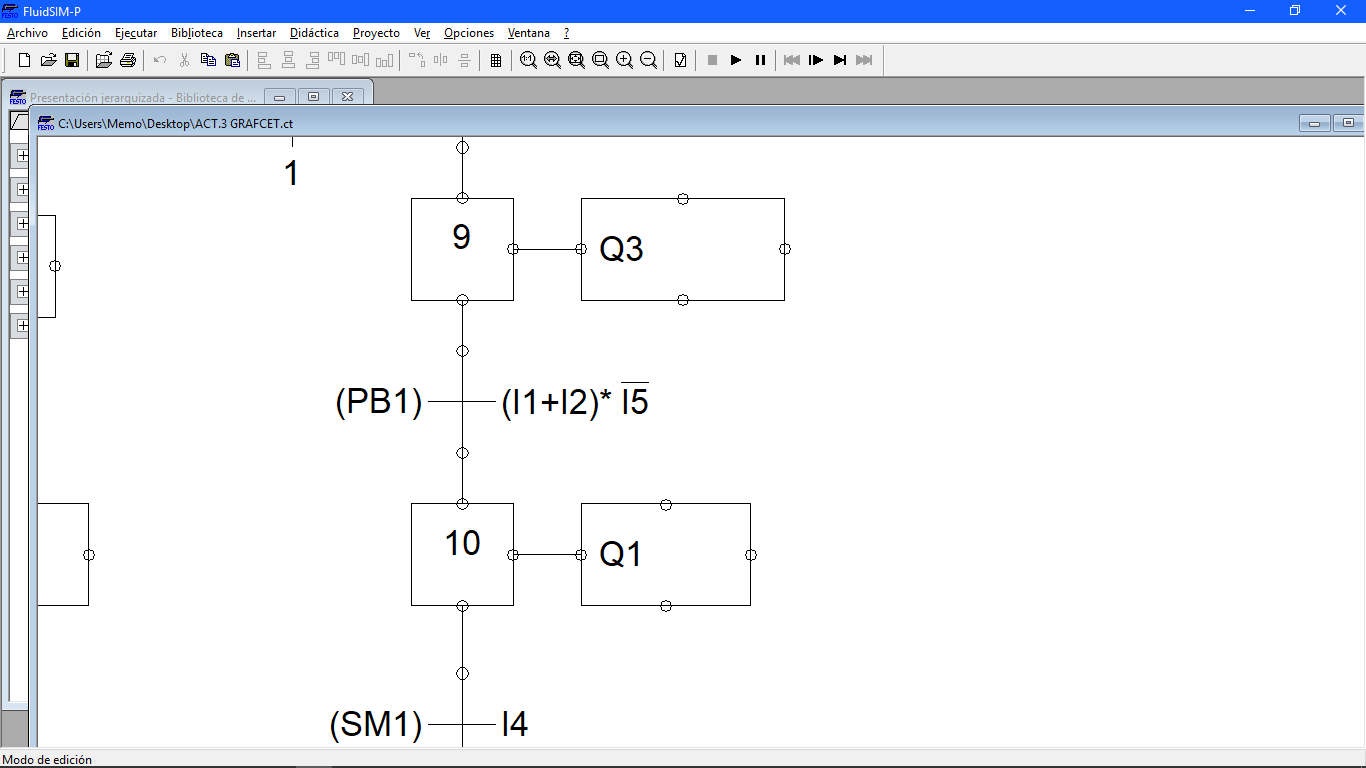


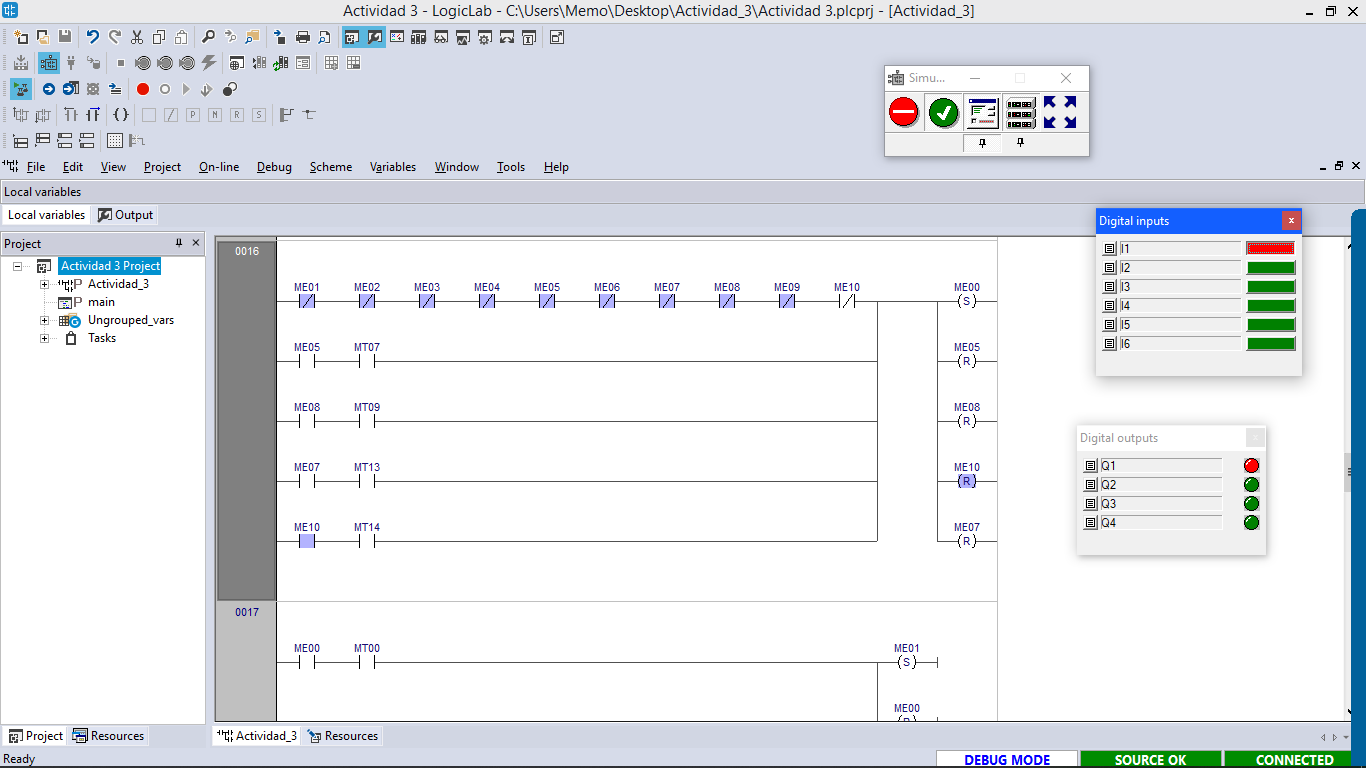




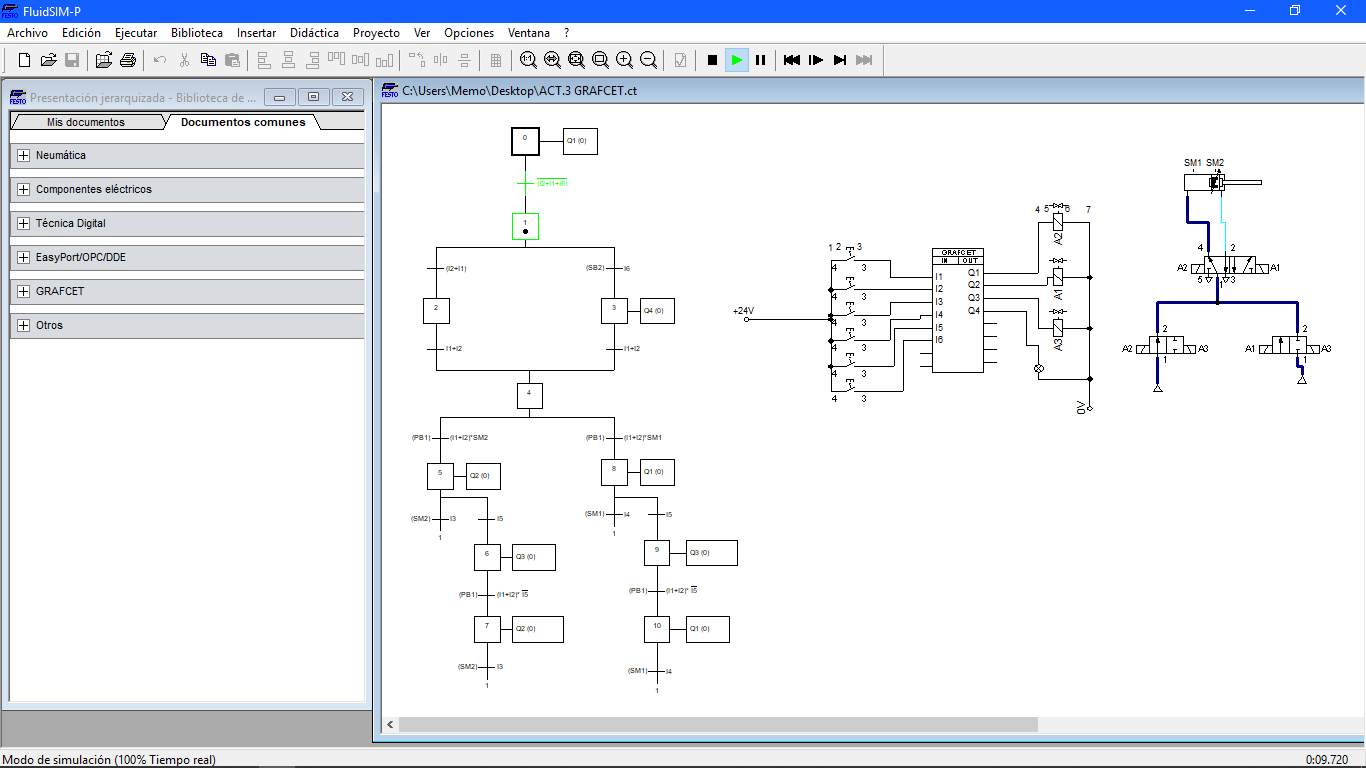


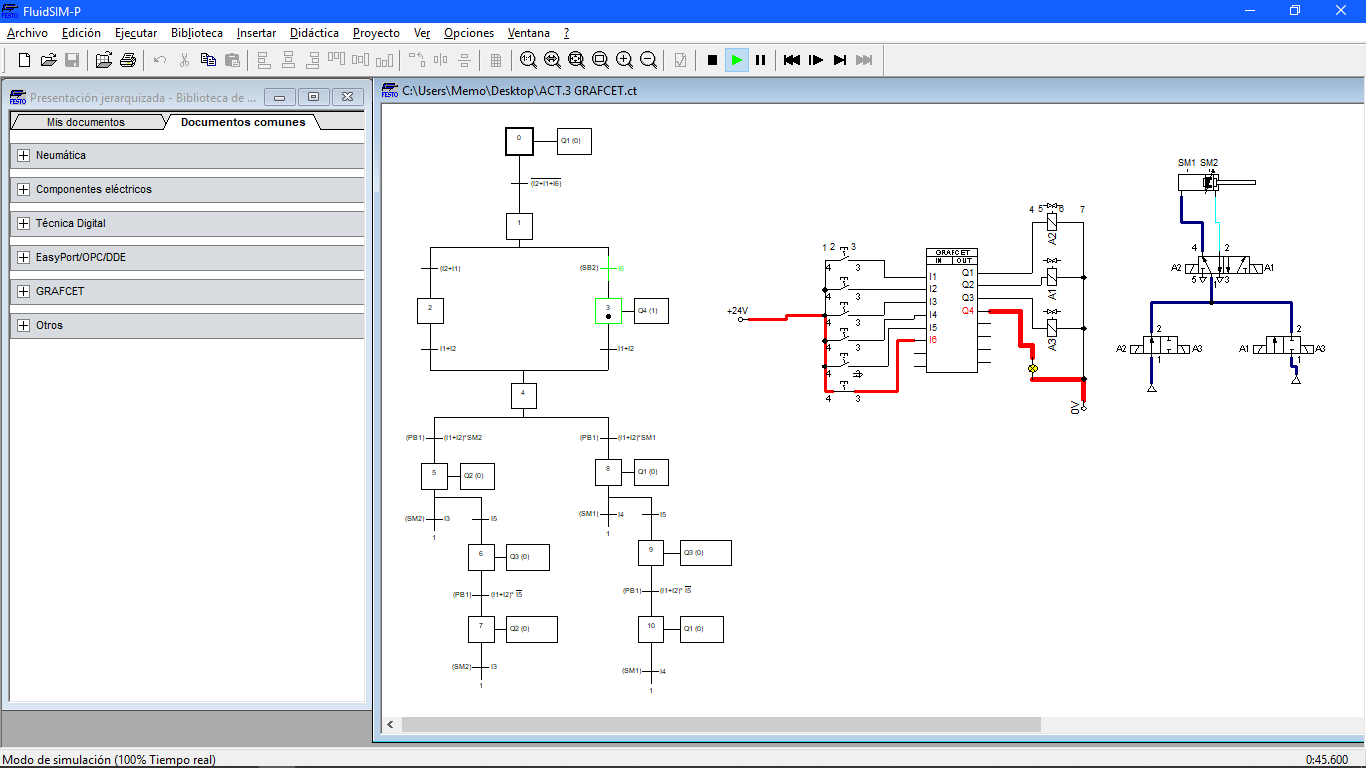


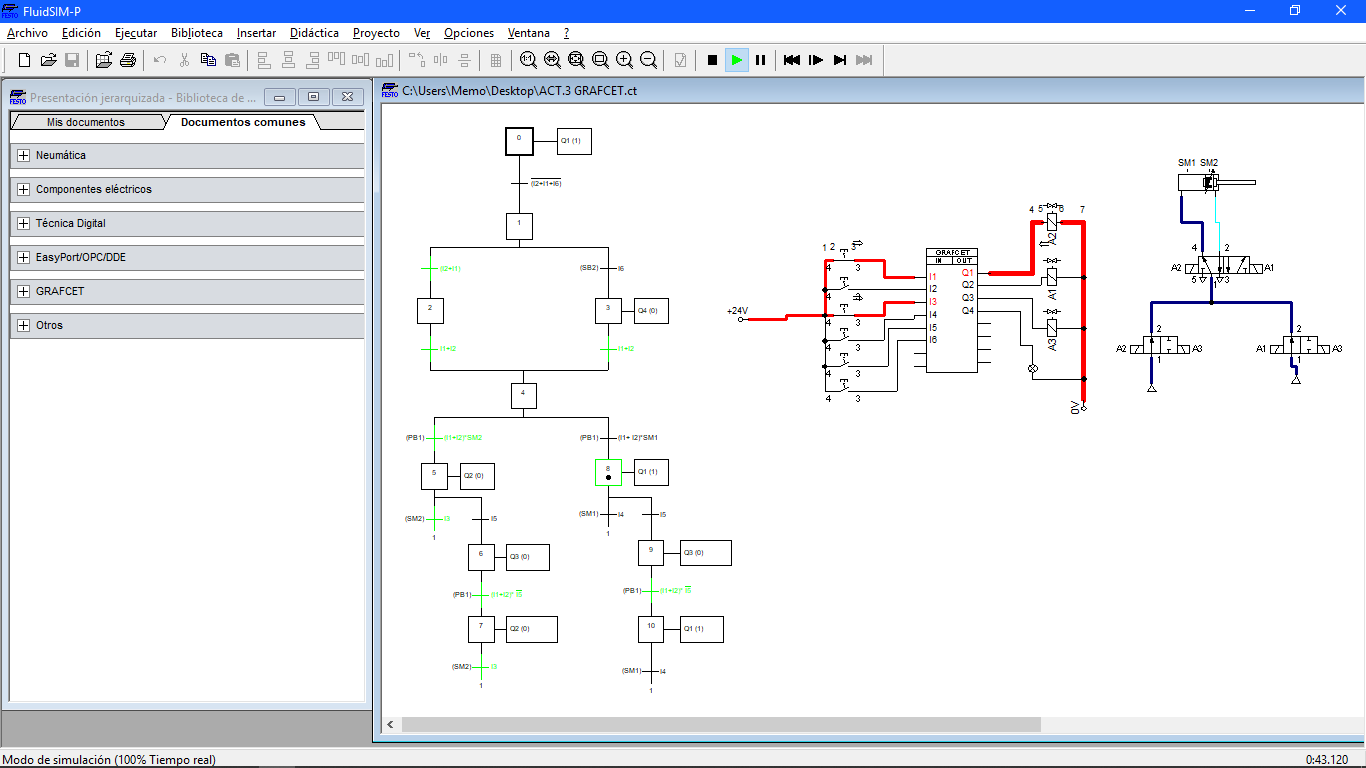


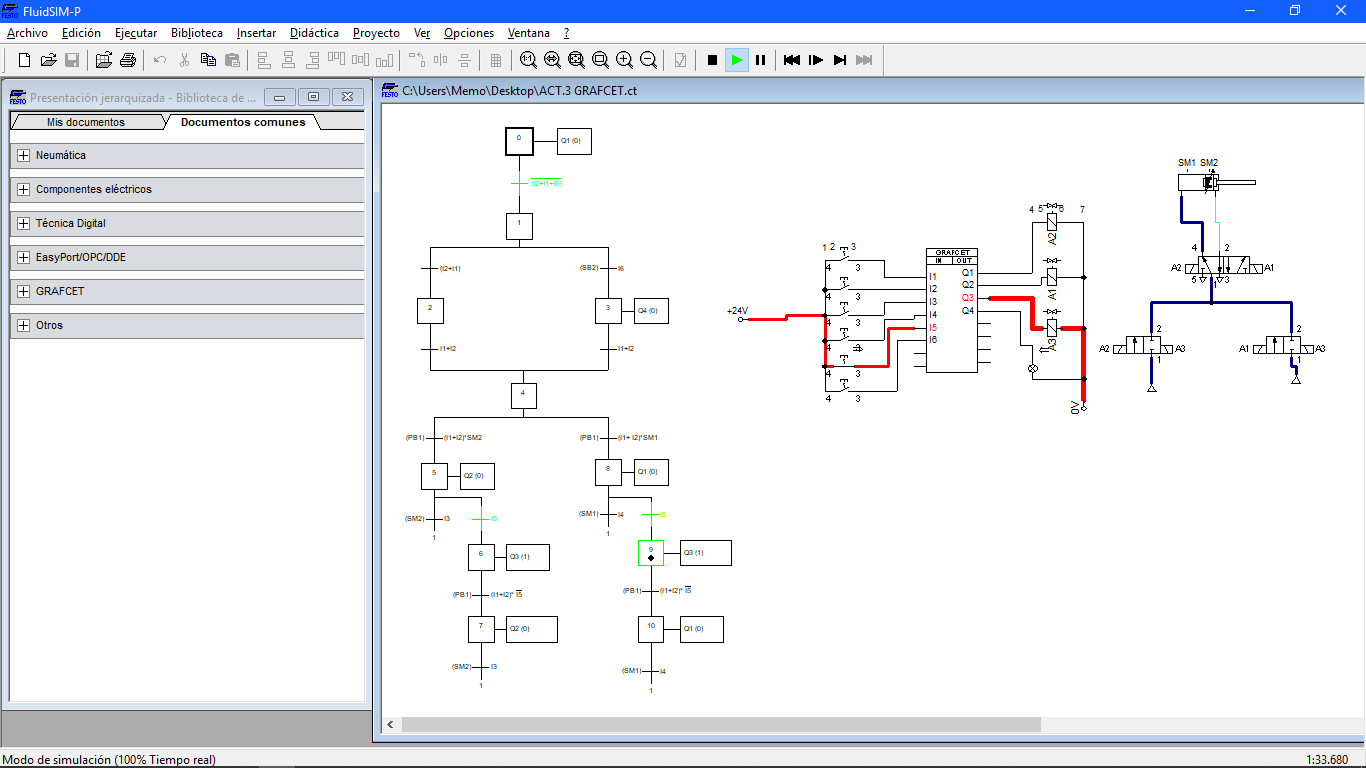


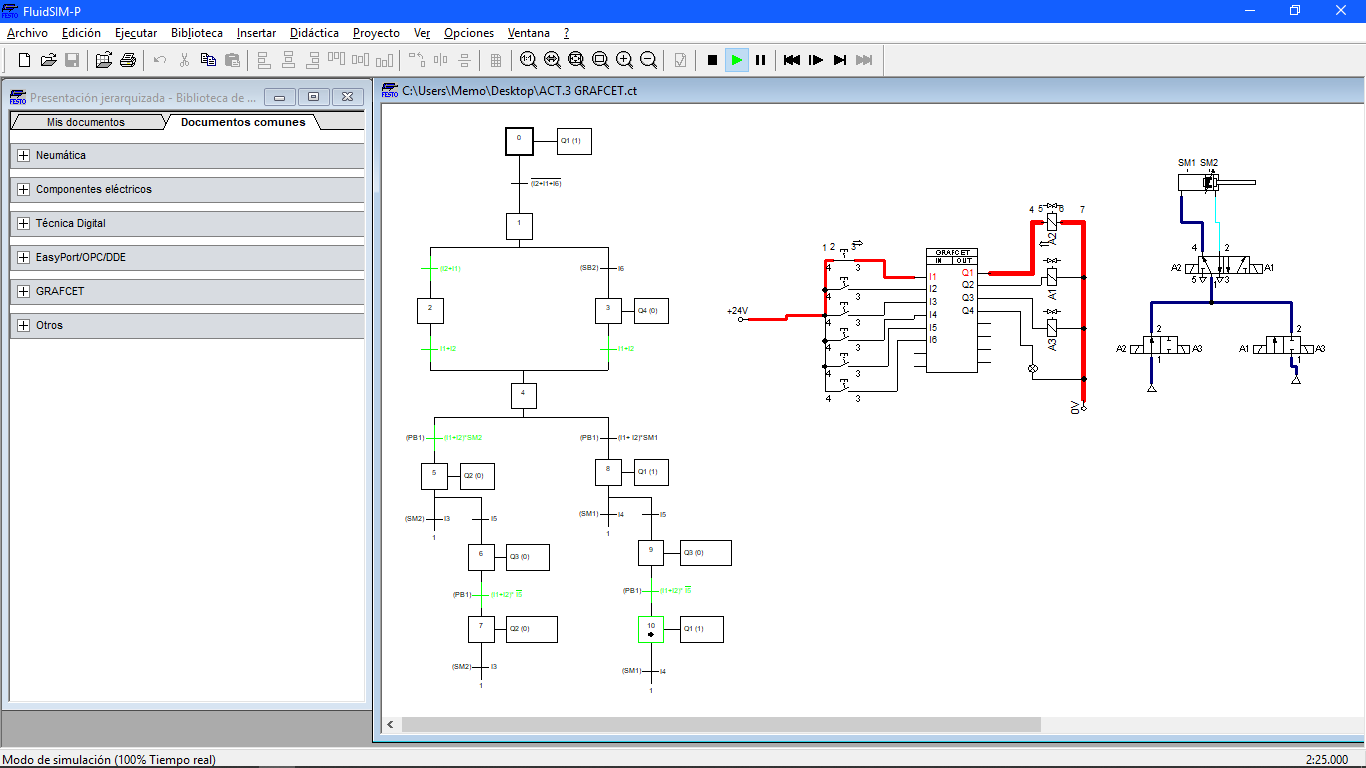
Grafcet en fluidsim

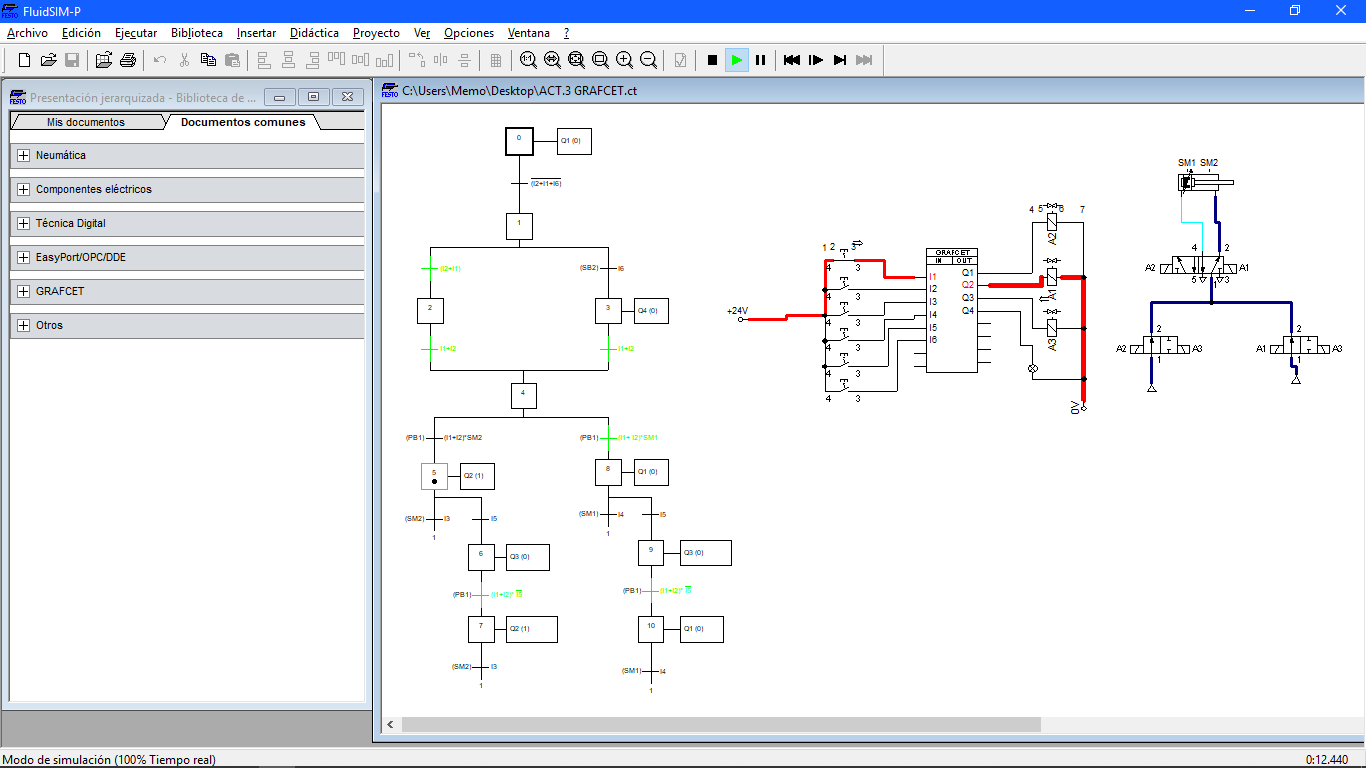
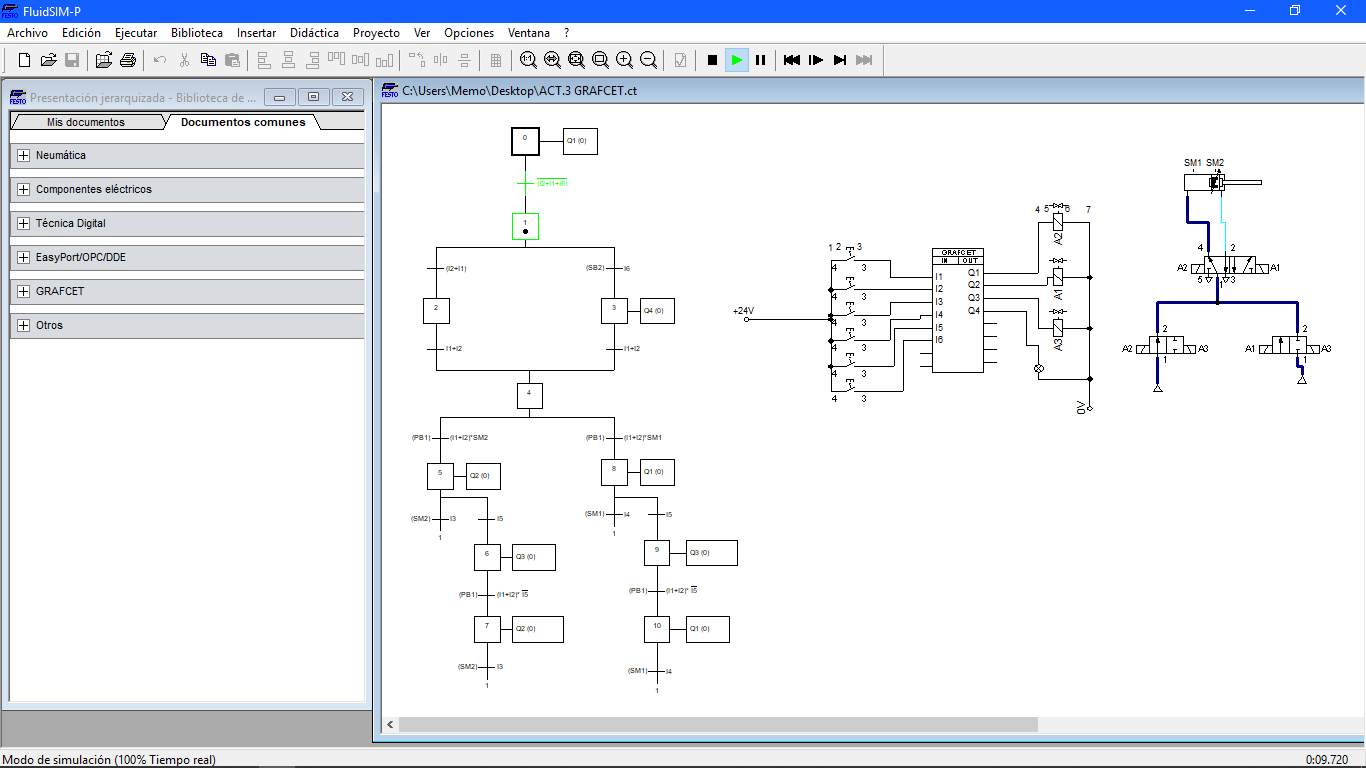


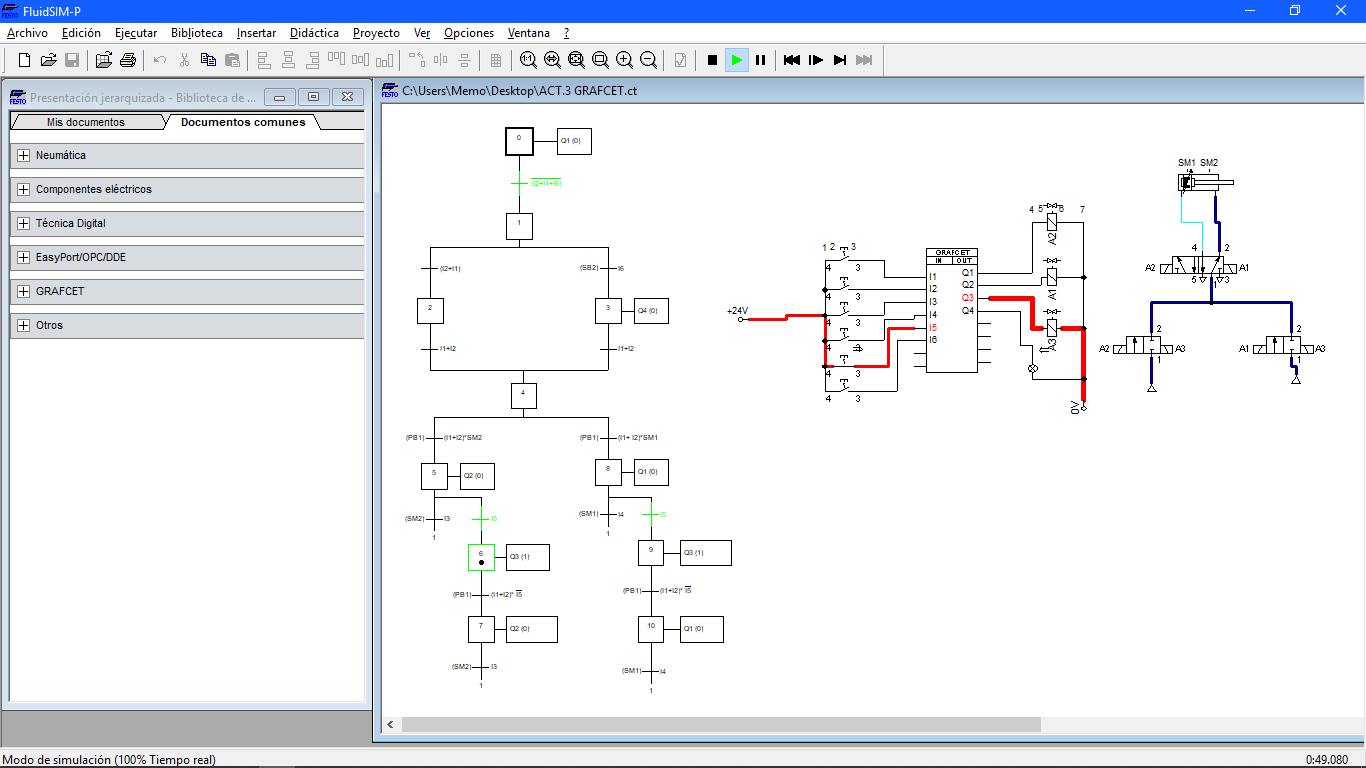


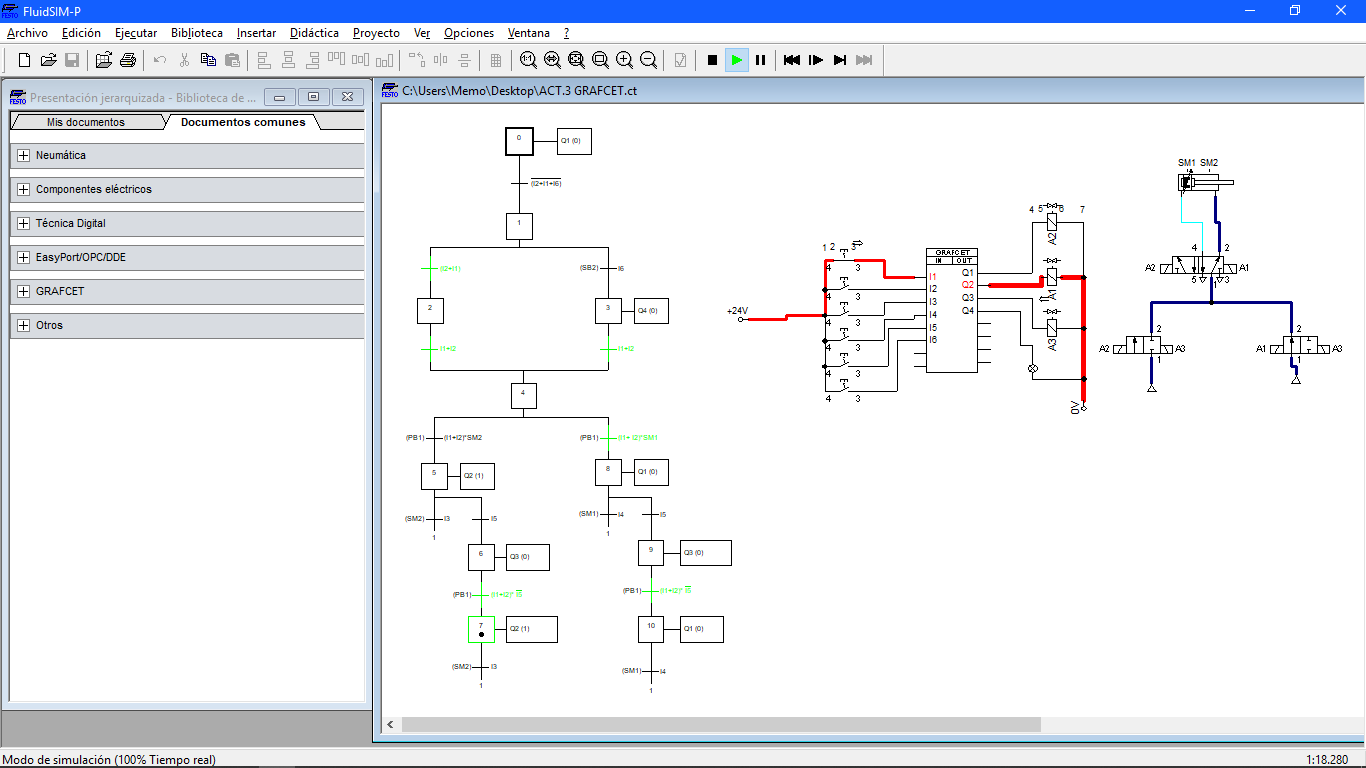












Evidencia

