Project of Distributed Systems: Paradigms and Models Parallel Iterative Jacobi Method

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1 Analysis of the problem

Notation premise: with $x_i^{(k)}$ we indicate the i-th entry of vector x at iteration k. The pseudo-code to execute the Jacobi method sequentially, for a fixed amount of iterations, is shown in 1. Note that we are forced to use an auxiliary vector x' to compute the updated elements of x for the current iteration, and then, before starting the following iteration, we copy the elements of x' in x. This is necessary because if at iteration k, we compute $x_1^{(k)}$ modifying x in-place, then when we'll compute $x_2^{(k+1)}$, instead of using $x_1^{(k)}$, we'll use $x_1^{(k+1)}$.

The algorithm is composed by three nested loops, where the first iterates $limit$ times, and the other two iterate n times, so the time complexity is $O(limit \cdot n^2)$. The $swap$ function used to copy the elements of x' in x, can be implemented in $O(1)$ time complexity, by swapping the pointers of the two arrays.

Algorithm 1 Sequential code for Jacobi method

Require: A strictly diagonal dominant matrix, b vector, $limit$ positive integer
1: **for** $k \leftarrow 1$ to $limit$ **do**
2:      **for** $i \leftarrow 1$ to n **do**
3:          $val \leftarrow 0$
4:          **for** $j \leftarrow 1$ to n **do**
5:              **if** $j \neq i$ **then**
6:                  $val \leftarrow val + A[i,j] * x[j]$
7:              **end if**
8:          **end for**
9:          $x'[i] \leftarrow \frac{1}{A[i,i]}(b[i] - val)$
10:      **end for**
11:      swap(x, x')
12: **end for**

Let's now analyze how we can exploit the properties of the algorithm in order to parallelize it. The first important thing we notice is that in order to compute the updated values for the vector x at iteration k, we need the full updated vector x at iteration $k-1$, therefore the iterations of the algorithm need to be strictly sequential.

A relevant property of the algorithm that we can notice from the pseudo-code, is that each element $x_i^{(k)}$ is computed independently from the others elements of the vector $x^{(k)}$, since to compute $x_i^{(k)}$ we just need the old vector $x^{(k-1)}$ and the matrix A. Therefore the iterations of the loop at line 2 of the pseudo-code, can be executed in parallel, which is the classical application of a map pattern. So, if we have nw workers available, we can assign to each worker a partition of indices of x to compute.

The innermost loop at line 4 is used to compute a sum of $n-1$ elements, which is the classical application of a reduce pattern. This is done inside the loop that we already said we can parallelize using a map pattern, so, if we want to implement also the reduce, for each one of the nw workers of the map, we have to arrange other nw' additional workers that can be used to compute in parallel the summation.

The introduction of a reduce inside each map would increase the overhead, since each worker assigned to the map would have to coordinate its computations with the workers that it uses for the reduce. Moreover, if the additional $nw \cdot nw'$ workers arranged for the reduce, are just used to increase the parallel degree of the map, we would obtain the same theoretical speedup (this is shown in the time analysis section) but which much less overhead. Therefore there is no advantage in implementing the reduce.

Even if we only implement the map, there some synchronization problems that we need to care

about:

1. Start iteration $k + 1$ only after all the workers finished their computations for iteration k, since the iterations must be strictly sequential

2. Executing the swap of the main and auxiliary vectors only after all the workers finished their computations for the current iteration, otherwise it might happen that the vectors are swapped while some workers are still computing their portion of $x^{(k)}$

3. Keep track of the number of iterations so that all the workers will stop after *limit* iterations

The first problem can be solved simply by putting a barrier after the execution of the map, so that all the worker will be forced to wait the others before starting the next iteration. In this way we can also solve the third problem by selecting one of the worker to count the number of iterations of the algorithm executed so far. The designated worker can simply use a shared variable among the workers, that it will increase by one before the barrier. Since the variable is modified by just one worker it doesn't need synchronization mechanism to be accessed even if it is shared. This solution guarantees that before starting iteration k the shared variable will have value k for all the workers, so that every worker can individually check if $k = limit$ and stop its execution if it is the case. To solve the second problem we need to introduce a second barrier, which has to be placed between the end of the map an the previously defined next iteration barrier. Between this new defined barrier and the next iteration barrier, one of the workers will be selected to execute the swap (it can be the same one that counts the iterations). In this way the swap will happen only after every worker finished its computations for the current iteration and before starting the next iteration.

The pseudo-code of the operations executed by a worker is showed in 2.

Algorithm 2 Worker pseudo-code

Require: A strictly diagonal dominant matrix, b vector, *limit* positive integer
 1: **while** $k < limit$ **do**
 2:    **for** $i \leftarrow start$ to end **do**
 3:        $val \leftarrow 0$
 4:        **for** $j \leftarrow 1$ to n **do**
 5:            **if** $j \neq i$ **then**
 6:                $val \leftarrow val + A[i,j] * x[j]$
 7:            **end if**
 8:        **end for**
 9:        $x'[i] \leftarrow \frac{1}{A[i,i]}(b[i] - val)$
10:    **end for**
11:    **Vectors swap barrier**
12:    **if** $start = 0$ **then**
13:        swap(x, x')
14:        $k \leftarrow k + 1$
15:    **end if**
16:    **Next iteration barrier**
17: **end while**

The variables *start* and *end* represent respectively the first and the last index of x assigned to the worker. The worker that will update the iterations counter and execute the swap of the vector is the one assigned to the leftmost chunk (the one with $start = 0$). Note that both of

the barriers are needed because if we only use the copy barrier, it could happen that one of the workers might start the next iteration before the designated worker could swap the vectors and update the iteration counter, so both barriers are needed.

2 Time analysis

In the sequential case, the time required to execute the algorithm is:

$$T_{seq} = T_{init} + k\left(n^2 T_\oplus + T_{swap}\right)$$

where k is the number of iterations, n is the number of row and columns of the matrix, T_{init} is the time required to initialize the computations, $T_\oplus$ is the time required to compute the expression at line 6 of 1 and T_{swap} is the time required to perform the swap of the vectors. T_{init} includes the declaration of some variable and the creation of the auxiliary array, an operation whose cost depends by n. If we consider negligible everything that it outside the main loop and the swap time, then the execution time is more or less:

$$T_{seq} \approx kn^2 T_\oplus$$

In the parallel case, the loop at line 2 of 1 is parallelized, so, with nw workers available, the program will execute at the same time nw parallel loops, each of which will iterate more or less $\frac{n}{nw}$ times. The overhead introduced is given by the forking and joining of the threads, by the time required to compute and assign tasks to the workers and by the time required to manage the barrier.

$$T_{par}(nw) = T_{init} + T_{split} + T_{fork} + k\left(\frac{n}{nw}nT_\oplus + T_{synch} + T_{swap} + T_{synch}\right) + T_{join}$$

where T_{fork} and T_{join} are the times for the fork-join mechanism, T_{split} is the time required to compute and split the tasks among the workers and T_{synch} is the time required to handle the barrier. Again, if we consider negligible everything that is not :

$$T_{par}(n) \approx k\frac{n}{nw}nT_\oplus$$

If we had decided to implement the reduce too, assigning nw_1 workers to the map and nw_2 workers to each worker of the map for the reduce, then then the parallel time would have changed as follows:

$$T_{par}(nw) \approx k\left[\frac{n}{nw_1}\left(\frac{n}{nw_2}T_\oplus + nw_2 T_\oplus\right)\right] = k\left(\frac{n}{nw_1}\frac{n}{nw_2}T_\oplus\right) + k\left(nw_2 T_\oplus\right) \approx k\left(\frac{n}{nw_1}\frac{n}{nw_2}T_\oplus\right)$$

since we could have computed in parallel nw_2 partial sums of $\frac{n}{nw_2}$ elements each, and then we would have computed the final sum summing up the nw_2 partial sums. As you can see if in the formula with only the map, if we set $nw = nw_1 * nw_2$ we would have obtained the same speedup.
So the speedup is:

$$Speedup(nw) = \frac{T_{init} + k\left(n^2 T_\oplus + T_{swap}\right)}{T_{init} + T_{split} + T_{fork} + k\left(\frac{n}{nw}nT_\oplus + T_{synch} + T_{swap} + T_{synch}\right) + T_{join}}$$

parallel If we consider negligible everything that happen outside the main loop of the program, then the speedup is:

$$Speedup(nw) \approx \frac{kn^2 t_\oplus}{k\frac{n}{nw}nt_\oplus} = nw$$

which is the ideal speedup which we can theoretically obtain with a map pattern. Of course to have a better estimate of the speedup we should consider the serial fraction of the program. We can make a rough estimation by considering that the total number of operations done by the algorithm is more or less in the order of kn^2, since we have to do some fixed amount of operations inside the three loops. Ideally, with n workers, every worker could compute its own $x_i^{(k)}$ in more or less n operations, reducing the number of operations to kn, which are the op so a rough estimation of the serial fraction f is:

$$f = \frac{kn}{kn^2} = \frac{1}{n}$$

if we now consider the speedup given by the Amdahl's law, we have that in the best case the maximum speedup is:

$$Speedup < \frac{1}{f} = \frac{1}{\frac{1}{n}} = n$$

So the speedup is upper-bounded by the number of rows/columns of the matrix. This makes sense, since in an ideal scenario, we would have a worker for each component of the vector x, reducing the cost of a single iteration of the algorithm from $O(n^2)$ to $O(n)$. So we should expect a better speedup as n increases.

3 Implementation details

The parallel program has been implemented in three versions:

 – The first using C++ threads

 – The second using FastFlow

 – The third using OpenMP

The program has been implemented defining a class called `Jacobi` which has as attributes the coefficients matrix A, the vector of known terms b and an integer n which is the size of A and b. For each implementation of the algorithm (the sequential and the three parallel ones) there's a corresponding method to solve the linear system $Ax = b$, using that specific implementation, by providing as parameters the number of iterations of the algorithm and the number of workers (for the parallel versions).

3.1 C++ threads

In the implementation with C++ threads we use the fork-join mechanism to spawn nw threads, each of them is assign to a contiguous chunk of x of size $\lfloor \frac{n}{nw} \rfloor$, and it executes code which is basically identical to the one in 2. For the barrier I used the `barrier` class introduced in C++20.

3.2 FastFlow

For the FastFlow implementation I uses the `ParalleFor` class to parallelize the loop at line 2 of 1. We create an object of class `ParalleFor` before the loops start, in order to prepare the nw workers, and then we call the `parallel_for` method, inside the main loop of the main loop of the algorithm. After the call of the parallel for method, the counter of the loop is increased and the vectors are swapped, and this is done sequentially by the main thread of the program. Note that we do not need to explicitly define the barrier since the call of the `parallel_for` method is an implicit barrier for the thread.

3.3 OpenMP

For the OpenMP implementation I simply parallelized the second innermost loop at line 2 of algorithm 1, using the `#pragama omp paralle for` directive, with the default static scheduling, which divides the loop in nw chunks of equal size. Like in the case of FastFlow, the end of the parallelized for is an implicit barrier for the threads used in the parallel for, so we do not need to define the barrier explicitly.

4 Manual

The source code, the scripts written for the experiments and a detailed manual of the program are available at this repository.
To compile the code is sufficient to execute the command `make` in the `src` directory of the project. The compilation will produce the executable `main` which can be launched to execute the program, providing the following arguments:

- `n`: the number of row and columns of the matrix

- `nw`: the number of workers that will be used in the parallel versions of the algorithm

- `iters`: the number of iterations of the algorithm

- `mode`: an integer number which specifies which implementations of the algorithm will be executed

The possible values of `mode`, with the corresponding effects, can be showed by launching the program with no argument. Once executed the program will run the versions of the algorithm required (according to the value of `mode`), and it will print their execution times.

5 Experiments

The main goals of the experiments that we will execute are the following:

- Comparing the performances obtained by the three implementations

- Check how the performances change with respect to the size of the matrix

- Comparing the actual performances to the ideal ones

To compare the performances of the three implementations we'll measure the times obtained when executing the algorithm with nw workers, varying nw from 1 to 128. Then from the times we'll derive the speedup and the efficiency of the three implementations. To see if the size of the matrix impact on the performances, we'll repeat this procedure twice, first on 1000×1000 matrix and then on a 1000×1000 matrix.
After that we'll select the number of workers for which we obtain the best speedup and we'll execute the algorithms with that number of workers for matrices of different sizes (500, 1000, 5000, 10000, 15000, 20000) to better understand the impact that the size of the matrix has on the performances. The results of the experiments are shown in the following plots.
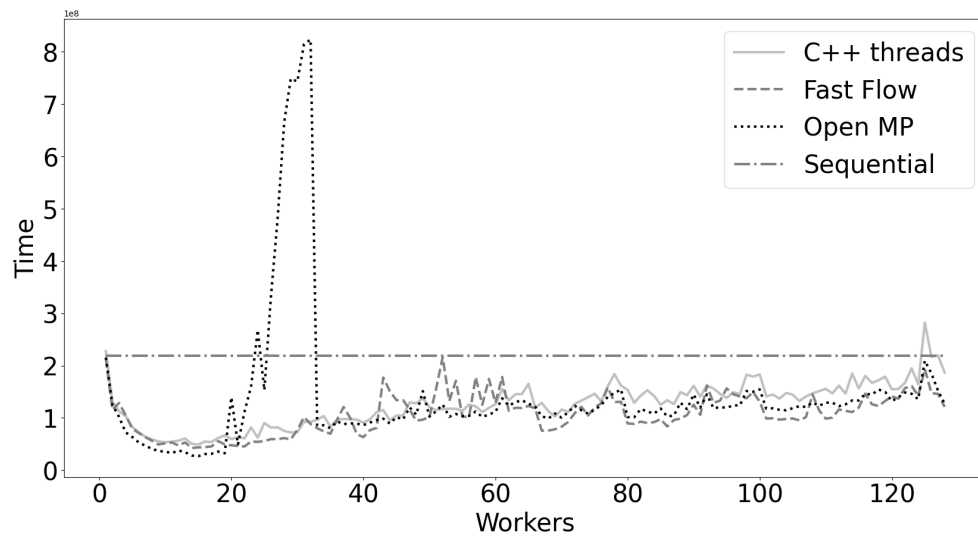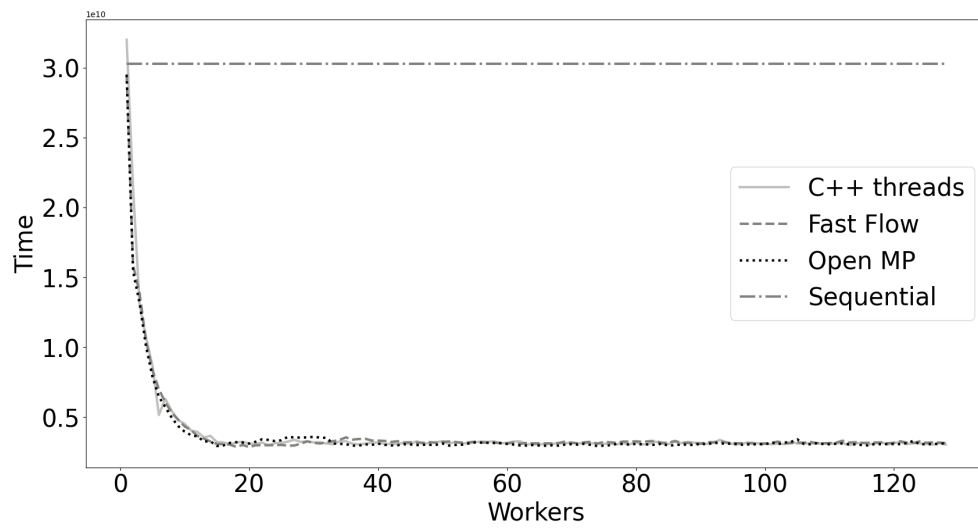
Figure 1: Time vs workers for matrix size 1000

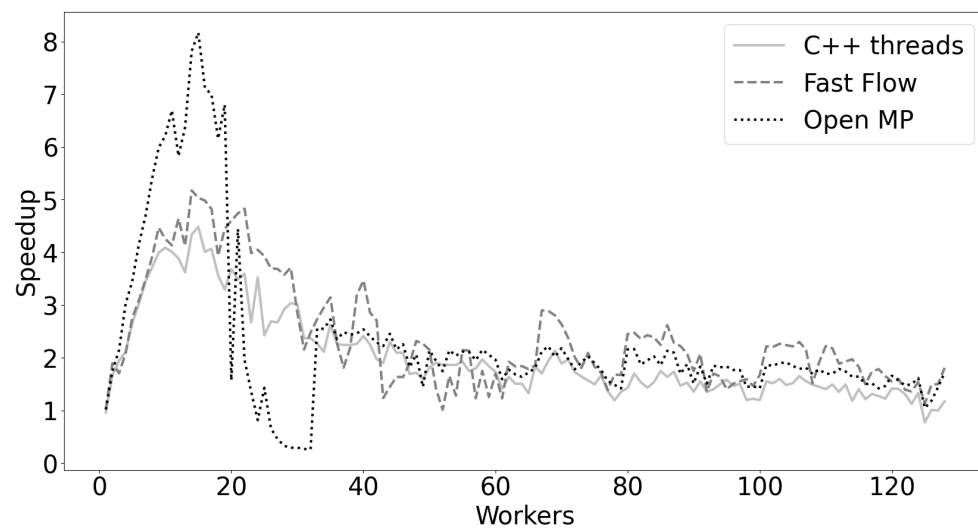Figure 2: Time vs workers for matrix size 10000

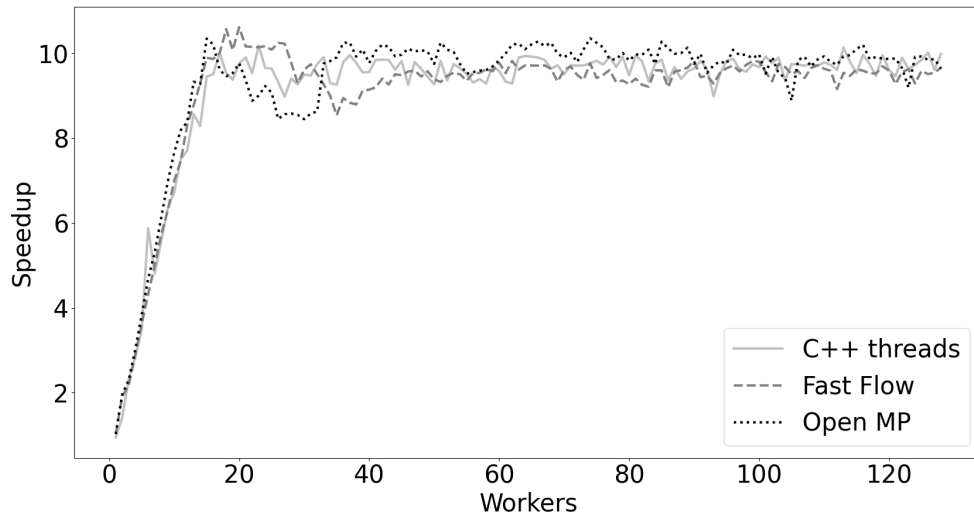Figure 3: Speedup vs workers for matrix size 1000

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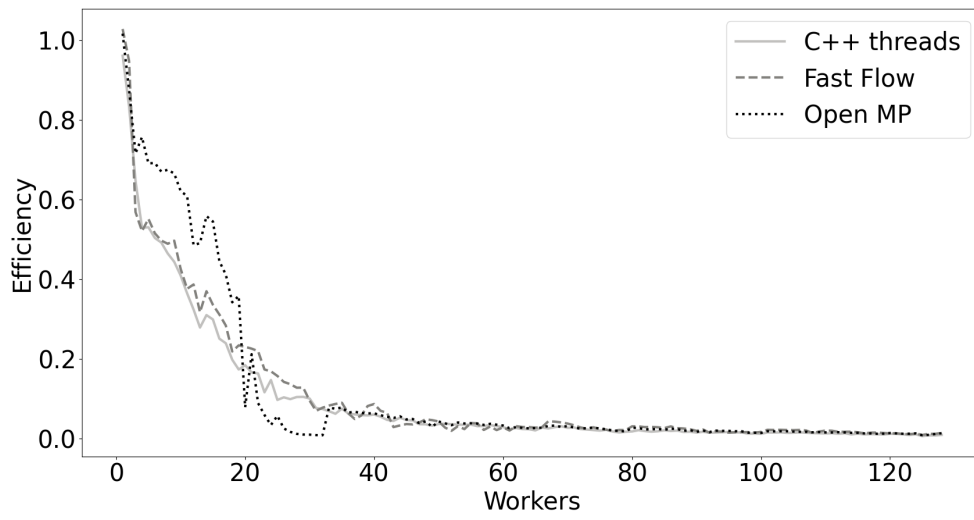Figure 4: Speedup vs workers for matrix size 10000

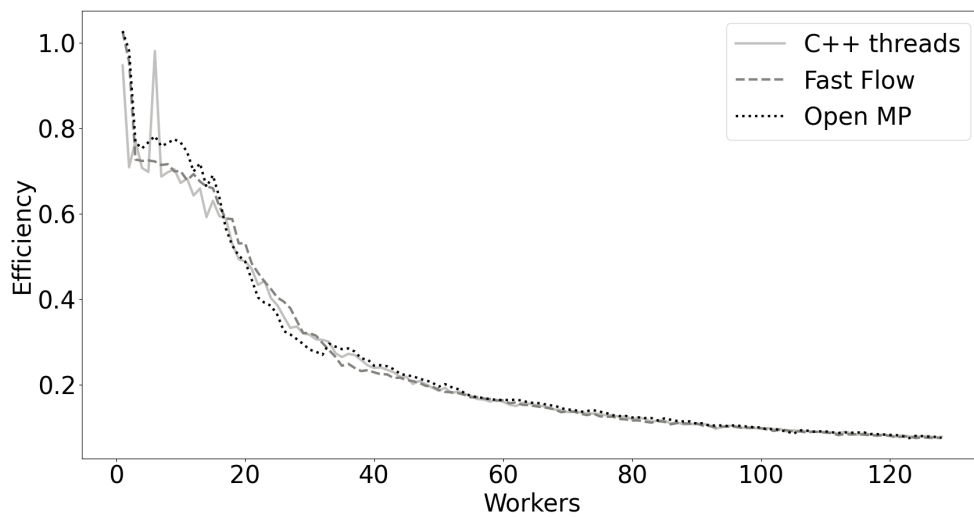Figure 5: Efficiency vs workers for matrix size 1000

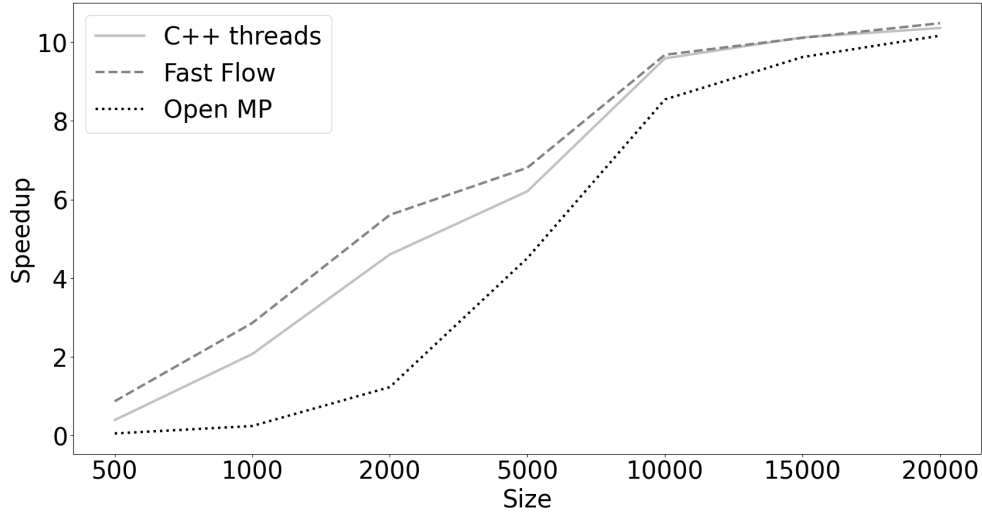Figure 6: Efficiency vs workers for matrix size 10000

Figure 7: Speedup vs matrix size with 32 workers

6 Conclusions

The first thing that we notice from the experiments is that the performances are much better in the experiment where the matrix size is larger. When $n = 1000$ the maximum speedup (which is less then 8) around 10-20 workers, and then it starts to decrease. While in the case where $n = 10000$ we reach the maximum speedup again with more or less 10-20 workers, but then it remains stable around 8-10 even with an higher number of workers. Consequentially also the efficiency decrease slower when n is large. This is also confirmed by figure **??**, that shows how the speedup increases while the matrix size increases. Probably executing test on larger matrices would bring a even better speedup, but the remote machine stopped the execution of the program when I tried with larger matrix sizes. Probably the better performances with larger sizes of the matrices are due to the fact, that increasing the size cause the grain of the computation of each worker to be coarser, and so the overhead has a reduced impact on the performances.

In the case of $n = 1000$ we can se that with less than 20 workers, the OpenMP implementation is by far the better one, then as the number of threads increases the performances of the three versions are more or less the same, with FastFlow which tends to do slightly better. For $n = 10000$ we can se that the performances of the implementations are almost identical, even in this case at the beginning OpenMP tends to do slightly better, and FastFlow does a little bit better when the number of threads gets high.