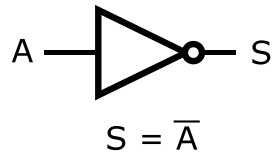


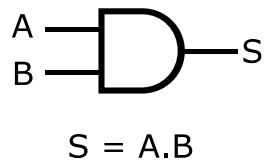
ELETRÔNICA DIGITAL - PORTAS LÓGICAS

PORTA NOT:



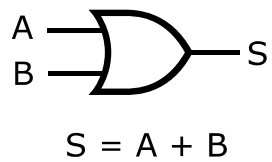
A	S
0	1
1	0

PORTA AND:



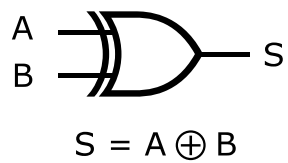
A	B	S
0	0	0
0	1	0
1	0	0
1	1	1

PORTA OR:



A	B	S
0	0	0
0	1	1
1	0	1
1	1	1

PORTA XOR (Ou exclusivo):



A	B	S
0	0	0
0	1	1
1	0	1
1	1	0

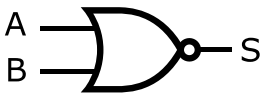
PORTA NAND (Negação da AND):



$$S = \overline{A \cdot B}$$

A	B	S
0	0	1
0	1	1
1	0	1
1	1	0

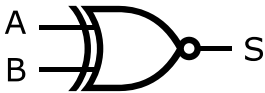
PORTA NOR (Negação da OR):



$$S = \overline{A + B}$$

A	B	S
0	0	1
0	1	0
1	0	0
1	1	0

PORTA XNOR (Negação da XOR):



$$S = \overline{A \oplus B}$$

A	B	S
0	0	1
0	1	0
1	0	0
1	1	1