



Code Description: Hardware Testing

The first order of business is to test the DAC. Because the dither voltage is so small with respect to the ADC LSB we cannot make a direct measurement. Temporarily replace the 50 ohms of R_{dith} with 2.2K. This raises the peak DAC voltage to around 385 mV, or 75 code values, which enables meaningful ADC measurements. Connect the output of the DAC on R_{dith} to the A1 input of the ADC.