# Lab2

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**Due** Oct 7, 2022 by 11:59pm          **Points** 100          **Submitting** a file upload

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Link to Lab 2: **Lab3B** ⤴ **(https://www.engr.scu.edu/~dlewis/book3/labs/Lab3B.pdf)**

Download **Lab3B-Empty.s (https://camino.instructure.com/courses/83789/files/6236074?wrap=1)** ↓ **(https://camino.instructure.com/courses/83789/files/6236074/download?download_frd=1)** and implement the functions in it.

When you push {LR}, make sure you push {R4, LR}

The reason that we need to push both R4 and LR is that we can't do just LR
The ARM Procedure Call Standard requires the stack pointer be **double word** aligned when functions are called. Failure to observe this requirement can cause a program to behave incorrectly or crash. So you need to save two registers.