

Lab 4 Results

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a)

```
diegoros@diegoros-VMware-Virtual-Platform: ~/CS6233/assignments/lab5/dr3432_lab5
diegoros@diegoros-VMware-Virtual-Platform:~/CS6233/assignments/lab5/dr3432_lab5$ make lab5_a
gcc -o lab5_a lab5_a.c -lrt
diegoros@diegoros-VMware-Virtual-Platform:~/CS6233/assignments/lab5/dr3432_lab5$ ./lab5_a 10
Parent printing fibonnacci sequence of 10:
0
1
1
2
3
5
8
13
21
34
55
diegoros@diegoros-VMware-Virtual-Platform:~/CS6233/assignments/lab5/dr3432_lab5$
```

b)

```
diegoros@diegoros-VMware-Virtual-Platform: ~/CS6233/assignments/lab5/dr3432_lab5
diegoros@diegoros-VMware-Virtual-Platform:~/CS6233/assignments/lab5/dr3432_lab5$ make lab5_b
gcc -o lab5_b lab5_b.c -lrt
diegoros@diegoros-VMware-Virtual-Platform:~/CS6233/assignments/lab5/dr3432_lab5$ ./lab5_b 12
Parent printing fibonnacci sequence of 12:
0
1
1
2
3
5
8
13
21
34
55
89
144
diegoros@diegoros-VMware-Virtual-Platform:~/CS6233/assignments/lab5/dr3432_lab5$
```