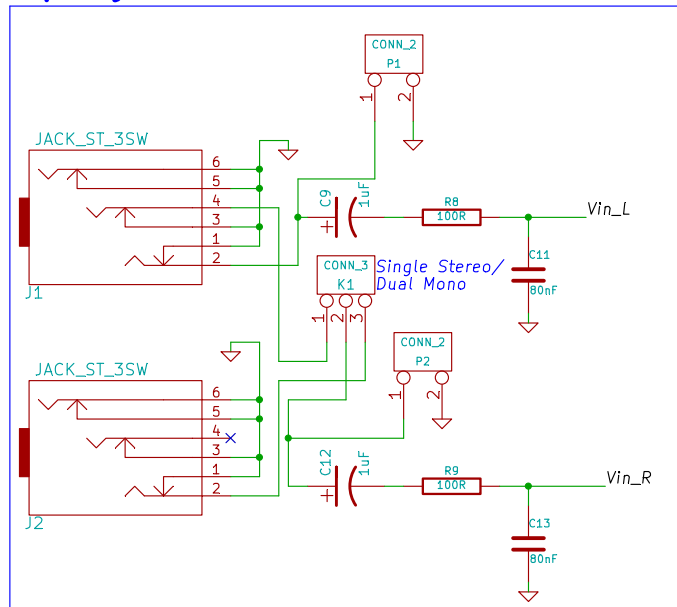
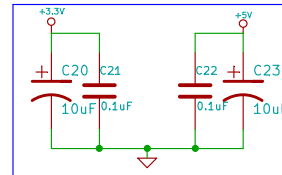


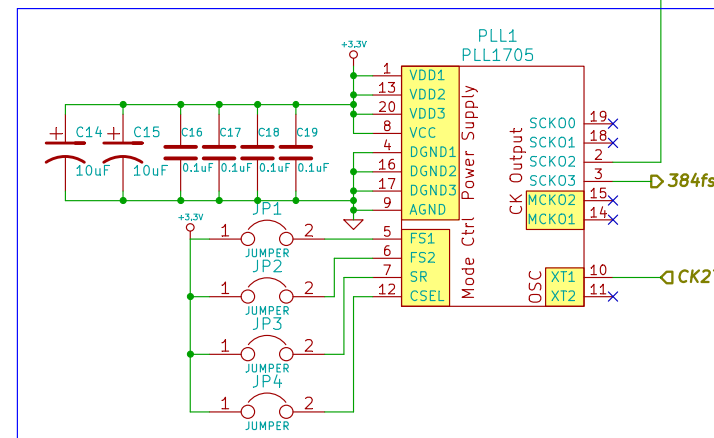
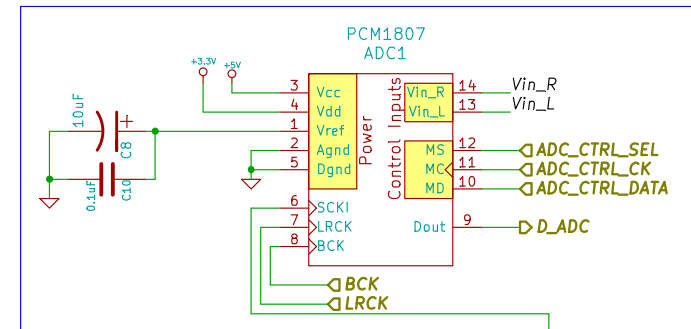
## Input jacks



## ADC Power Decoupling



## ADC



## Sample Clock Generator PLL

By Diego Rosales  
ADC Page  
**Cholula Audio**  
File: ADC.sch  
Sheet: /ADC/

**Title: Quetzalcoatl Digital Snake**

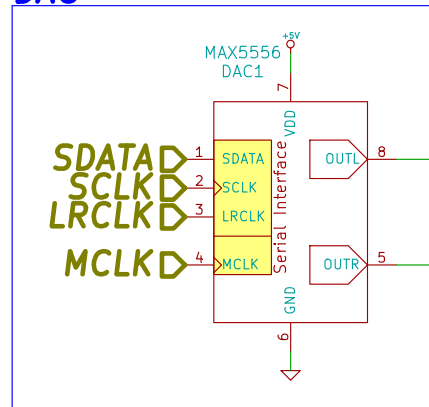
Size: A4 Date: 3 jul 2014

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

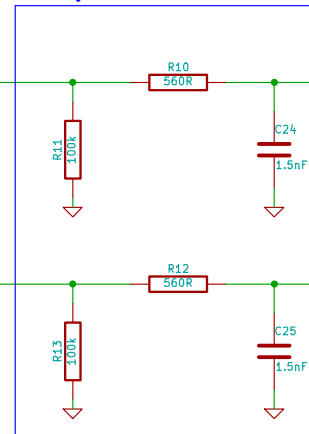
**Rev: 1.0**

Id: 2/5

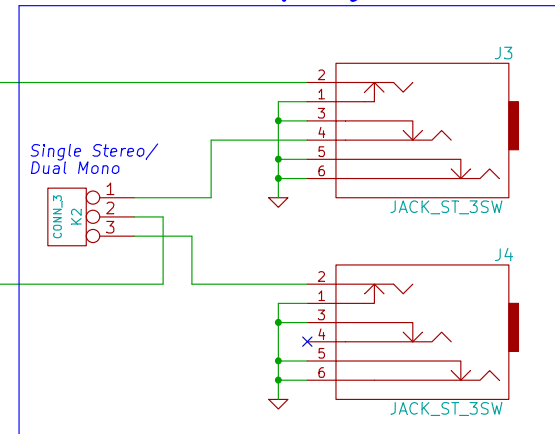
## DAC



## Output filter

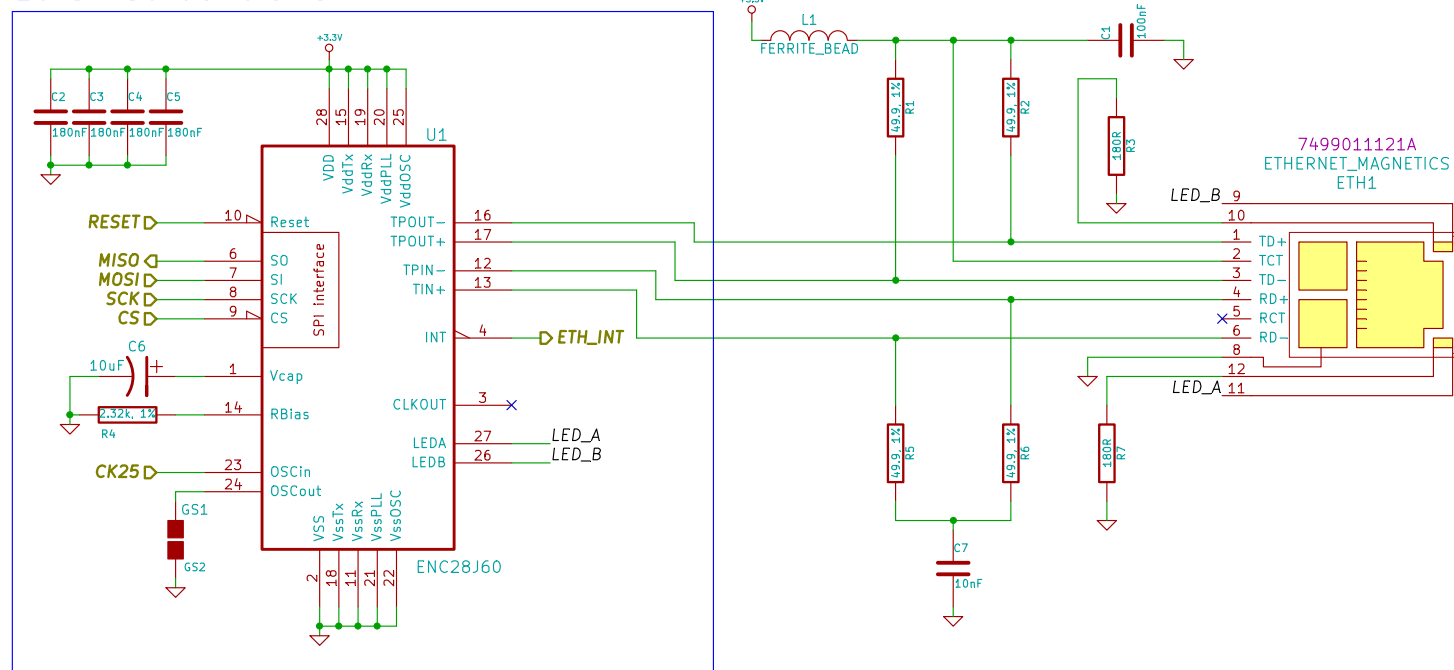


## Output jacks



File: DAC.sch		
Sheet: /DAC/		
<b>Title:</b>		
Size: A4	Date: 3 jul 2014	Rev:
KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable		Id: 3/5

## Ethernet Controller



By Diego Rosales

Ethernet Page

**Cholula Audio**

File: Ethernet.sch

Sheet: /Ethernet/

**Title: Quetzalcoatl Digital Snake**

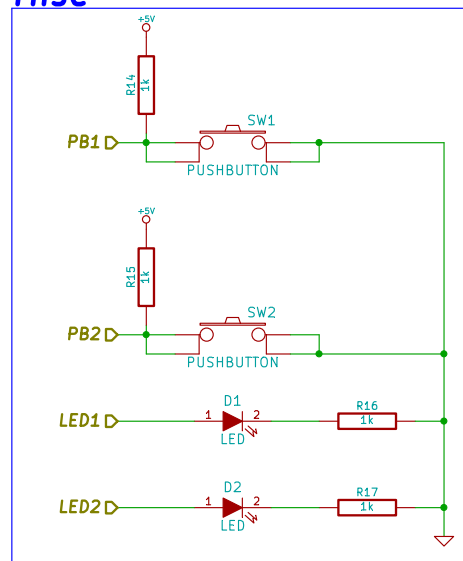
Size: A4 Date: 3 jul 2014

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

**Rev: 1.0**

Id: 4/5

## Misc



File: Misc.sch

Sheet: /Misc/

**Title:**

Size: A4

Date: 3 jul 2014

**Rev:**

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

Id: 5/5