Rommel Saquicela

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Ecuador, Guayas, Guayaquil

Languages

Programming

- TypeScript
- JavaScript
- Go
- GraphQL

Cloud Computing

- Lambda Function
- AWS S3
- Step Functions
- ECS
- Docker
- Firebase

Databases

- MySQL
- PostgreSQL
- MongoDB
- DynamoDB
- Elasticsearch

Education

Telematics Engineering from ESPOL

Graduate Feb 2021

Summary







I consider myself an Internet citizen, in the workplace and professional I am responsible and detailed with the requirements of the objective to be met. Lover of software development and always learning new technologies.

Career history

Software Engineer at Jelou Latam

Create new features and improve core systems.

Key responsibilities

- Backend developer
- Cloud infrastructure developer
- · Integration of technologies
- Real time connections.

Software Developer at Carvajal Consultants Inc

Independent consultant for create new features, resolve problems and technology migration.

Key responsibilities

- Design and Implemed the core web3 marketplace.
- FullStack developer.

Maintenance technician at Netlife

Support to end user with their Internet Services problems.

Key responsibilities

- · Meet technical requirements.
- Router Configuration

Some of my projects

DApp

Role/Title: Backend Developer

Responsibilities: Design and implement the microservice for transactions to the Ethereum network.

Employee Skill: Golang - REST - BlockChain - Microservices - Smart Contracts.

RecetasNET -- Flutter Mobile APP

https://play.google.com/store/apps/details?id=com.rommeldev.recetasnet_flutter_app

Role/Title: Backend Developer

Responsibilities: Design and implement the backend of the application.

Employee Skill: Golang - MySQL - API Rest - Firebase.

SOAP consuming microservices

SOAP Service: https://www.ecb.europa.eu/stats/eurofxref/eurofxref-hist-90d.xml

Role/Title: Backend Developer

Responsibilities: Implement a microservice that consumes SOAP information and send it through REST.

Employee Skill: Golang - SAP - REST - Microservices - MySQL

Paper/Publications

Monitoring of system memory usage embedded in FPGA

https://ieeexplore.ieee.org/abstract/document/9232863

Published in: 2020 International Conference on Applied Electronics (AE)

About: Design for monitoring the memory of an embedded system, in the logical part, making use of the communication between the FPGA and the HPS.