

Milestone 2

Michael Kelly | Keith Johnson | Eric Wells

Faculty Sponsor: Dr. William H. Allen

Milestone 2

	% Complete	Michael K.	Keith J.	Eric W.
Partial CPU Emulation	100%	33%	33%	34%
Basic Applet Frontend	100%	33%	33%	34%
Read ROM from Client	100%	33%	33%	34%
Memory implementation	100%	33%	33%	34%



Partial CPU Implementation

Code Coverage Report





Memory Implementation

- LoROM layout implemented
- Shadow RAM, System RAM, ROM access
- Hardware Registers NOT included







Milestone 3

	Michael K.	Keith J.	Eric W.
Improved CPU Emulation	33%	34%	33%
Basic Video Functionality	33%	34%	33%
GUI Configuration Options	33%	34%	33%





