

I2C Junior Computer Implementation

Ports

SDTA \$F682 PB6

SCLK \$F680 PA0

BIOS Interface

BIOS Command in BDOS \$1A

Input Y Bios I2C-CMD

 A Byte or page \$00 pointer for 2 Bytes to be transfered

Output C 1 = Error

 A Result byte

Commands

\$00	I2C-CLR	Clear I2C bus	-	C=1 Error
\$01	I2C-Stop	Send stop condition	-	C=1 Error
\$02	I2C-Poll	Poll device	Dev adr	C=1 not active
\$03	I2C-ADEV	Send device adr 7 Bit for write	Dev adr	C=1 Error
\$05	I2C-R16	Send register word	(Reg L/H)	C=1 NAK
\$06	I2C-RDEV	Send device adr 7 Bit for read	Dev adr	C=1 Error
\$07	I2C-WByte	Write byte to I2C bus	Byte	C=1 Error
\$08	I2C-RBYTE	Read byte from I2C bus	-	A=byte C=1 Error
\$09	I2C-RLB	Read last byte & I2C-NAK & I2C-Stop	-	A=byte C=1 Error
\$0A	I2C-LOAD_ID	Load ID from dev to addr	(dev,ID,ptr)	C=1 Error
\$0B	I2C-G_TIME	Get time from RTC (SS,MM,HH)	(ptr)	C=1 Error
\$0C	I2C-G_DATE	Get date From RTC (dy,DD,MM,YY)	(ptr)	C=1 Error