

# SOURADIP DAS

📍 **Home** : Sangrur, Punjab, PIN-148106, India

✉ **Email**: [souradipdas806@gmail.com](mailto:souradipdas806@gmail.com) ☎ **Phone**: (+91) 8640905340

🌐 **LinkedIn**: [www.linkedin.com/in/souradip-das-077a72256](https://www.linkedin.com/in/souradip-das-077a72256)

## ABOUT ME

I am Souradip Das, a 3rd year B.E. (Electronics and Communication Engineering) student with a strong interest in analog and digital circuit design. I have completed research internships at NIT Silchar and gaining hands-on experience in analog circuit design. Proficient in tools like Cadence Virtuoso & Xilinx Vivado. As a coordinator of a electronics society 'Nano-Nerds Club' at SLIET, I actively engage in technical collaboration and project development. I aim to contribute meaningfully to your organization with all my skills.

## SKILLS

VerilogHDL | Digital Circuit Design | CMOS | analog circuit | Cadence-Virtuoso | Xilinx VIVADO | Python (Basic) | linux OS | Cocotb | RTL Design | Tcl/Tk Scripting

## PROJECTS

[ 01/05/2024 – 09/06/2024 ]

### ML Assisted Analog Circuit Sizing , National institute of Technology Silchar

In this project i was designed OpAmp & Current mirror in Candence Virtuoso. Analyze the Bodes plot for gain and frequency. Support Vector regression algorithm to train a model. This model will evaluate the design parametric value for required design specification. Collected around 2k data from a design current and voltage equation using MATLAB.

**Link**: <https://drive.google.com/file/d/1p5uRBVpchMlcRpQRb-mZj7kjEAW0jQNg/view>

### Digital Circuit Design using Verilog-HDL and FPGA architechture

RTL code in Verilog using Xilinx Vivado with synthesis and implementation. Architecture of FPGA (basic level) , System on chip design basic from RTL to GDSII on Linux OS .

Verification using python based library 'Cocotb'.

**Link**: <https://github.com/Digi-soura22>

## EDUCATION AND TRAIN- ING

[ 2022 – 2026 ]

### Bachelor of Engineering in Electronics and Communication Engineering

*Sant Longowal Institute of Engineering and Technology*

**City**: Sangrur | **Country**: India |

[ 01/06/2024 – 09/07/2024 ]

### Research Internship , 2nd year

*Nation Institute of Technology Silchar*

**City**: Silchar | **Country**: India |

Designed 2-stage Differential amplifier (gain 40dB), CS amplifier , Gain and phase plot with analysis

[ 04/2025 – Current ]

### Dyumnin Semiconductors (OPC) Private Limited Internship Trainee · Remote

*Dyumnin Semiconductors (OPC) Private Limited* <https://dyumnin.com>

**City**: Bengaluru, Karnataka | **Country**: India |

ASIC Design, Makefile, Linux OS, Cocotb, python

[ 06/2025 ]

### High Frequency Circuit Design Internship

## HONOURS AND AWARDS

**South-east Asian Mathematical Olympiad,2017 Awarding institution: SEAMO**

I was ranked 411(indian rank) in Southeast Asian Mathematical Olympiad,2017

[ 2022 ] **JEE Main Exam Qualified**

## CONFERENCES AND SEMINARS

[ 28/11/2023 ] **Knowledge Sharing and Capacity Building Workshop for Precision Agriculture using UAV techniques in the South and Southeast Asian Region: Hands-on training workshop**

Sant Longowal Institute of Engineering and Technology

[ 11/05/2023 – 12/05/2023 ] **Organized a event on National Technological Day 11th may 2023. Dr. Dipanwita Halder, Scientist, Indian Institute of Remote sensing ,Dehradun ,deliver a speech on " Remote Sensing technology for Agriculture" and interacted with the participants**  
SLIET UNIVERSITY

**Attendent the two day hands-on workshop on VEGA Processor Ecosystem at Chandigarh University, Mohali, Punjab**

[ 29/09/2023 – 30/09/2023 ] **Two Days Training Program on "Design and Development of Terahertz Self-Multiplex Antennas Using Substrate Integrated Waveguide for 6G Wireless Communication System", Organized By Department of ECE, Sant Longowal Institute of Engineering & Technology**

## VOLUNTEERING

[ 09/02/2023 – Current ] **IEEE STUDENT BRANCH CHAIRMAN (IEEE SLIET STUDENT BRANCH)**

SLIET,LONGOWAL,PUNJAB,148106

- Organized a event on National Technological Day 11th may 2023. Dr. Dipanwita Halder, Scientist, Indian Institute of Remote sensing ,Dehradun ,deliver a speech on " Remote Sensing technology for Agriculture" and interacted with the participants.
- Two Days Training Program on "Design and Development of Terahertz Self-Multiplex Antennas Using Substrate Integrated Waveguide for 6G Wireless Communication System", (29-30, September, 2023)Organized By Department of Electronics and Communication Engineering Sant Longowal Institute of Engineering & Technology.
- I also attend in a Hands-on training workshop on Knowledge Sharing and Capacity Building Workshop for Precision Agriculture using UAV techniques in the South and Southeast Asian Region,HokkaidoUniversity,japan .

[ 09/2024 – Current ] **Nano-Nerds Society, a VLSI Club Department of ECE** Sant Longowal Institute of Engineering and Technology

A VLSI Club Stablished by me under Department of ECE .In this club we teach about VLSI domains, designing using of tools and project work etc. Discuss on analog and digital circuit design process . Organize Seminar and workshop from IITs , NITs and industry persons.

**Link:** <http://sliet.ac.in/nano-nerds-club/>

HOBBIES AND INTERESTS

---

Singing , playing football, Book reading