

Digital Compact Cassette Recorder 70DCC600

/00B/05B/06B

**Service
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Service Manual

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**PHILIPS**

PCS 60 385

TECHNICAL SPECIFICATIONS**GENERAL:**

Power supply : 115 / 230V ±15% switchable
 Power consumption : 30W max.
 5W max. in stand by
 Dimensions (w x h x d) : 360 x 102 x 300mm
 Weight : 2,5kg

DIGITAL I/O:

Format : AES/EBU according IEC958
 Sampling frequency : 32kHz / 44,1kHz / 48kHz ±0,1%
 Unbalanced chinch : 75Ω

ANALOG IN (digital recording from analog input)

Sampling frequency : 44,1kHz
 Input sensitivity : 150mV
 Max. input voltage : 2Vrms ±2dB
 Input impedance : 50kΩ

ANALOG OUT DCC PLAYBACK (DAC performance)

Output voltage : 2Vrms ±2dB (0dB digital level)
 Frequency range : 20Hz..14,5kHz at fs = 32kHz
 20Hz..20kHz at fs = 44,1kHz
 20Hz..22kHz at fs = 48kHz
 Amplitude linearity : ±0,5dB
 Phase non-linearity : 2° max. at 1kHz
 Channel unbalance : <0,5dB at 1kHz
 Output resistance : 200Ω
 Outband attenuation : 60dB above 30kHz
 Channel separation : 80dB at 1kHz
 70dB at 20Hz..20kHz
 Muting (search) : 100dB
 SNR : 90dB / 93dBA typ.
 THD + noise : 85dB at 1kHz
 82dB at 20Hz..20kHz

ANALOG OUT DCC RECORD & PLAYBACK**(ADC & DAC performance)**

Output voltage : 2Vrms ±2dB
 Frequency range : 20Hz..14,5kHz at fs = 32kHz
 20Hz..20kHz at fs = 44,1kHz
 20Hz..22kHz at fs = 48kHz
 Amplitude linearity : ±0,5dB
 Phase non-linearity : 2° max. at 1kHz
 Channel unbalance : <2dB
 Output resistance : 200Ω
 Outband attenuation : 60dB above 30kHz
 Channel separation : 80dB at 1kHz
 70dB at 20Hz..20kHz
 Muting (search) : 100dB
 SNR : 85dB / 88dBA
 THD + noise : 82dB at 1kHz
 80dB at 20Hz..20kHz

ANALOG OUT ACC

Output voltage : 0,84Vrms ±2dB (200nWb/m)
 Amplitude linearity : 40Hz..14kHz within 5dB
 250Hz..10kHz within 3dB
 Phase non-linearity : 2° max. at 1kHz
 Channel unbalance : <3dB
 Output resistance : 200Ω
 Channel separation : 26dB at 1kHz
 46dB at 20Hz..20kHz
 Muting (search) : 100dB
 SNR

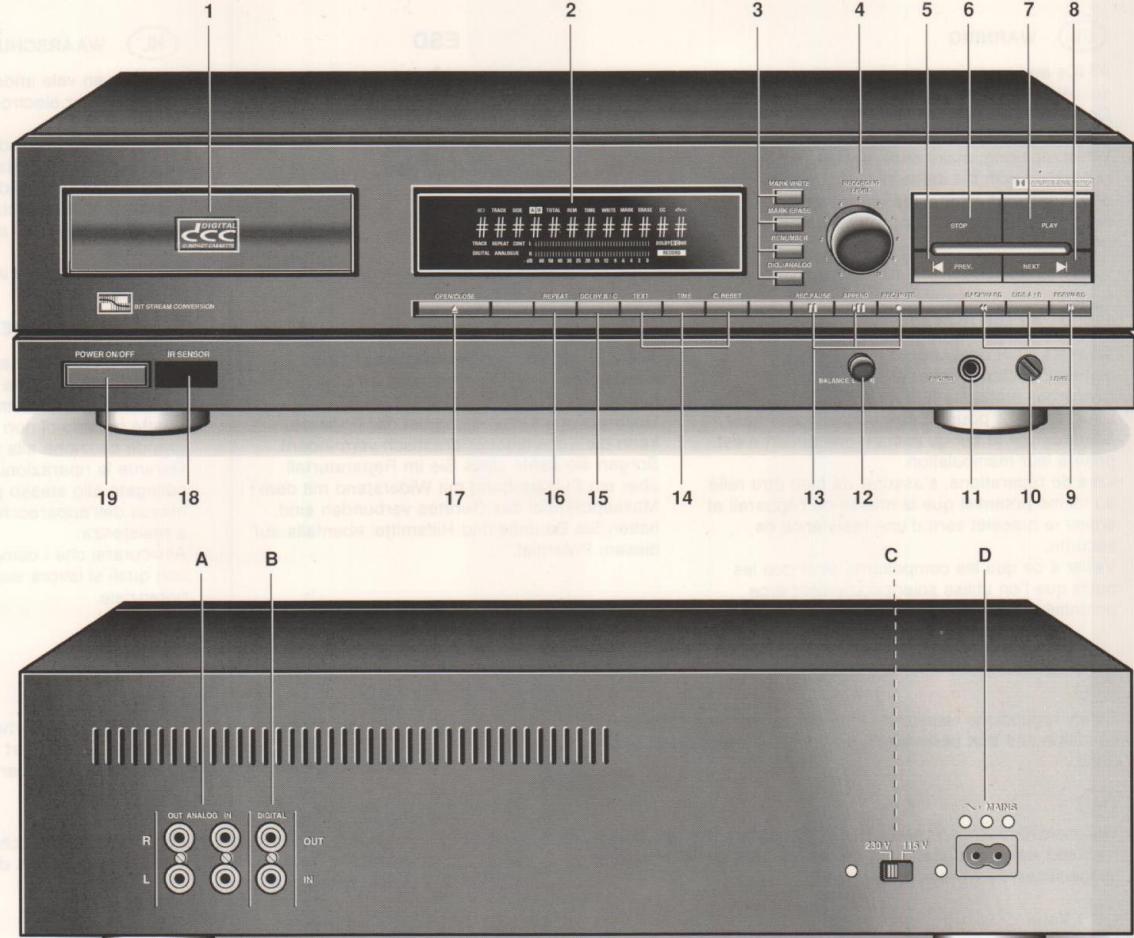
Dolby-mode	Fe (type I)	Cr (type II)
Dolby off	47dB	50dB
Dolby B	56dB	59dB
Dolby C	64dB	67dB

THD : <3%

HEADPHONE OUTPUT DCC PLAYBACK

Output voltage : 5Vrms max.
 Channel unbalance : <4dB
 Output impedance : 140Ω
 Load impedance : 32 / 120 / 600Ω
 Output power : 27 / 44 / 27mW
 Channel separation : 75dB typ. at 1kHz
 Muting (search) : 100dB
 SNR : 88dB
 THD + noise : 82dB at 1kHz/-3dB

CONTROLS AND CONNECTIONS



CONTROLS

CONNECTIONS

Indication on Recorder	Indication in Diagram	Indication on Recorder	Indication in Diagram	Indication on Recorder	Indication in Diagram
1. Cassette Holder		10. LEVEL	3365	A. ANALOG IN	1301
2. Display	1400	11. PHONES	1311	A. ANALOG OUT	1306
3. MARK WRITE	1413	12. BALANCE	3311	B. DIGITAL IN	1315
MARK ERASE	1417	13. REC. PAUSE	1403	B. DIGITAL OUT	1315
RENUMBER	1412	APPEND	1406	C. VOLTAGE SELECTOR	1203
DIG./ANALOG	1409	REC/MUTE	1407	D. MAINS	1202
4. RECORDING LEVEL	3312	14. TEXT	1410		
5. PREVIOUS	1415	TIME	1418		
6. STOP	1404	COUNTER RESET	1411		
7. PLAY	1402	15. DOLBY B/C NR	1405		
8. NEXT	1414	16. REPEAT	1420		
9. BACKWARD	1421	17. OPEN/CLOSE	1401		
SIDE A-B	1419	18. IR SENSOR	7410		
FORWARD	1416	19. POWER ON/OFF	1204		

WARNINGS**GB WARNING**

All ICs and many other semi-conductors are susceptible to electrostatic discharges (ESD). Careless handling during repair can reduce life drastically. When repairing, make sure that you are connected with the same potential as the mass of the set via a wrist wrap with resistance. Keep components and tools also at this potential.

ESD**NL WAARSCHUWING**

Alle IC's en vele andere halfgeleiders zijn gevoelig voor electrostatische ontladingen (ESD).

Onzorgvuldig behandelen tijdens reparatie kan de levensduur drastisch doen verminderen. Zorg ervoor dat u tijdens reparatie via een polsband met weerstand verbonden bent met hetzelfde potentiaal als de massa van het apparaat.

Houd componenten en hulpmiddelen ook op ditzelfde potentiaal.

I AVVERTIMENTO

Tutti IC e parecchi semi-conduttori sono sensibili alle scariche statiche (ESD).

La loro longevità potrebbe essere fortemente ridotta in caso di non osservazione della più grande cautela alla loro manipolazione. Durante le riparazioni occorre quindi essere collegato allo stesso potenziale che quello della massa dell'apparecchio tramite un braccialetto a resistenza.

Assicurarsi che i componenti e anche gli utensili con quali si lavora siano anche a questo potenziale.

F ATTENTION

Tous les IC et beaucoup d'autres semi-conducteurs sont sensibles aux décharges statiques (ESD). Leur longévité pourrait être considérablement écourtée par le fait qu'aucune précaution n'est prise à leur manipulation. Lors de réparations, s'assurer de bien être relié au même potentiel que la masse de l'appareil et enfiler le bracelet serti d'une résistance de sécurité. Veiller à ce que les composants ainsi que les outils que l'on utilise soient également à ce potentiel.

D WARNUNG

Alle ICs und viele andere Halbleiter sind empfindlich gegen elektrostatische Entladungen (ESD).

Unsorgfältige Behandlung bei der Reparatur kann die Lebensdauer drastisch vermindern. Sorgen Sie dafür, dass Sie im Reparaturfall über ein Pulsarmband mit Widerstand mit dem Massepotential des Gerätes verbunden sind. halten Sie Bauteile und Hilfsmittel ebenfalls auf diesem Potential.

D

Bei jeder Reparatur sind die geltenden Sicherheitsvorschriften zu beachten. Der Originalzustand des Geräts darf nicht verändert werden für Reparaturen sind Original-Ersatzteile zu verwenden.

I

Le norme di sicurezza esigono che l'apparecchio venga rimesso nelle condizioni originali e che siano utilizzati pezzi di ricambio identici a quelli specificati.

SP Varning!

Osynlig laserstrålning när denna del är öppnad och spärren är urkopplad. Betrakta ej strålen.

F

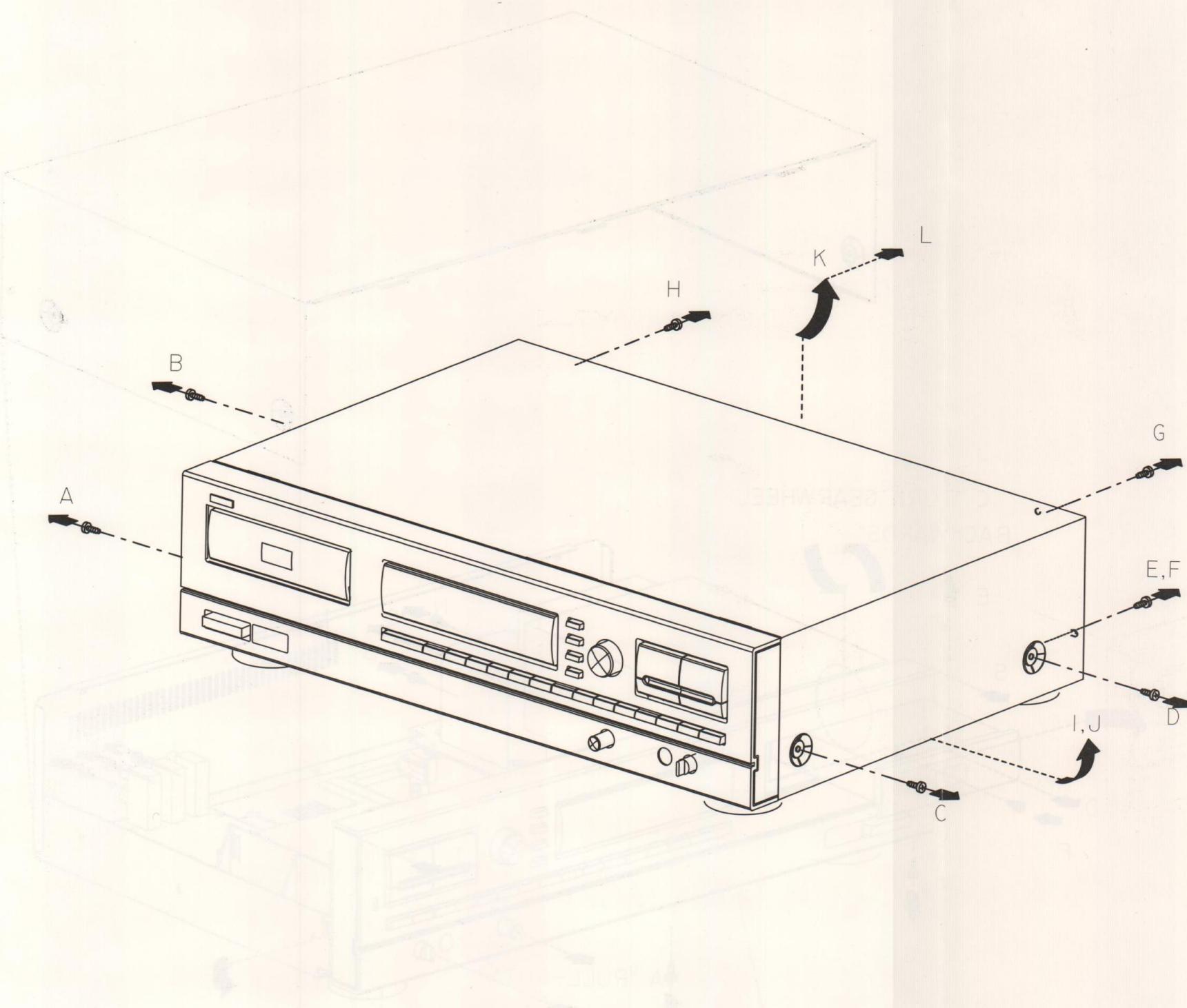
Les normes de sécurité exigent que l'appareil soit remis à l'état d'origine et que soient utilisées les pièces de rechange identiques à celles spécifiées.

"Pour votre sécurité, ces documents doivent être utilisés par des spécialistes agréés, seuls habilités à réparer votre appareil en panne".

**DISMANTLING INSTRUCTIONS
DEMOUNTING COVER**

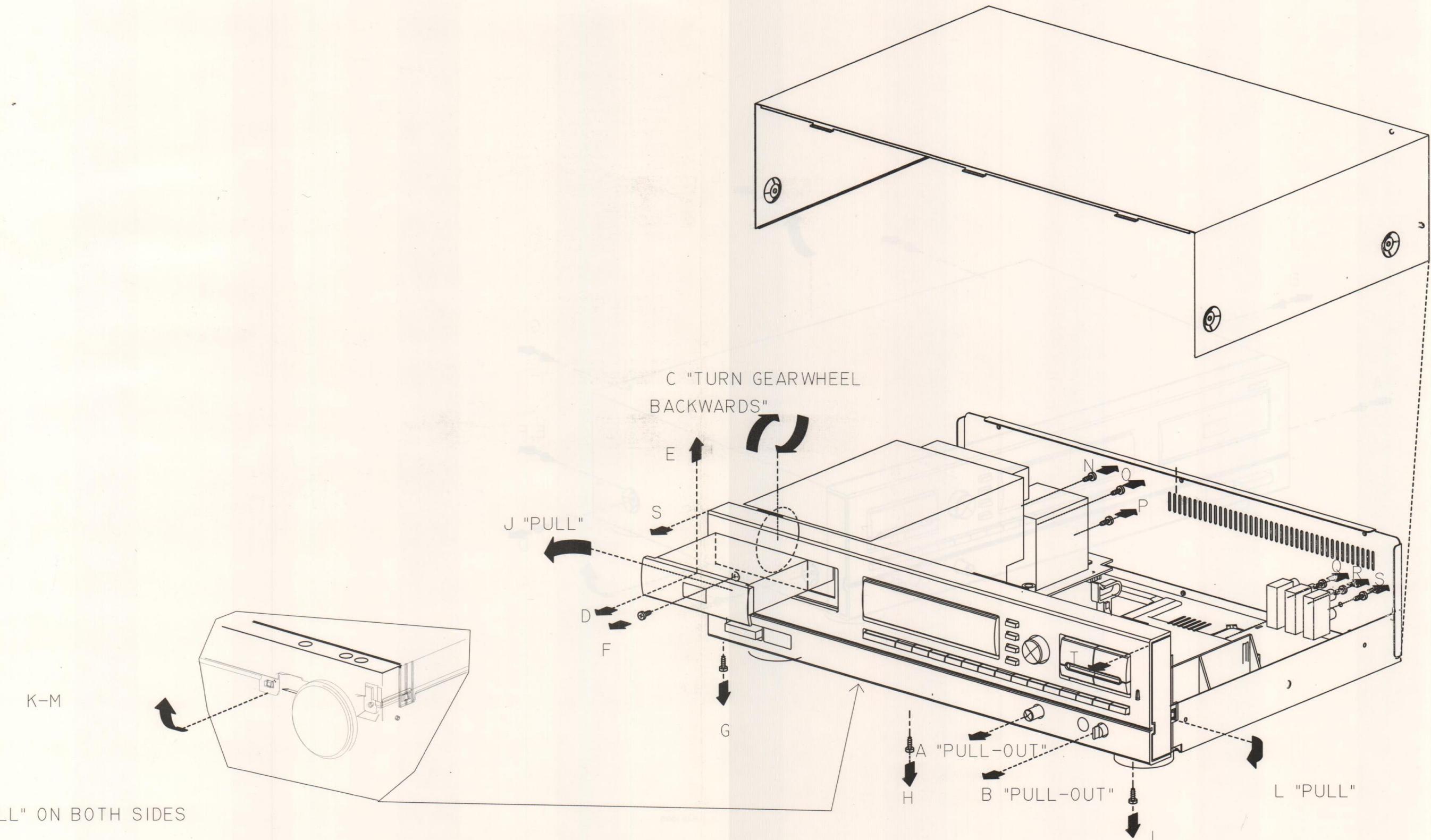
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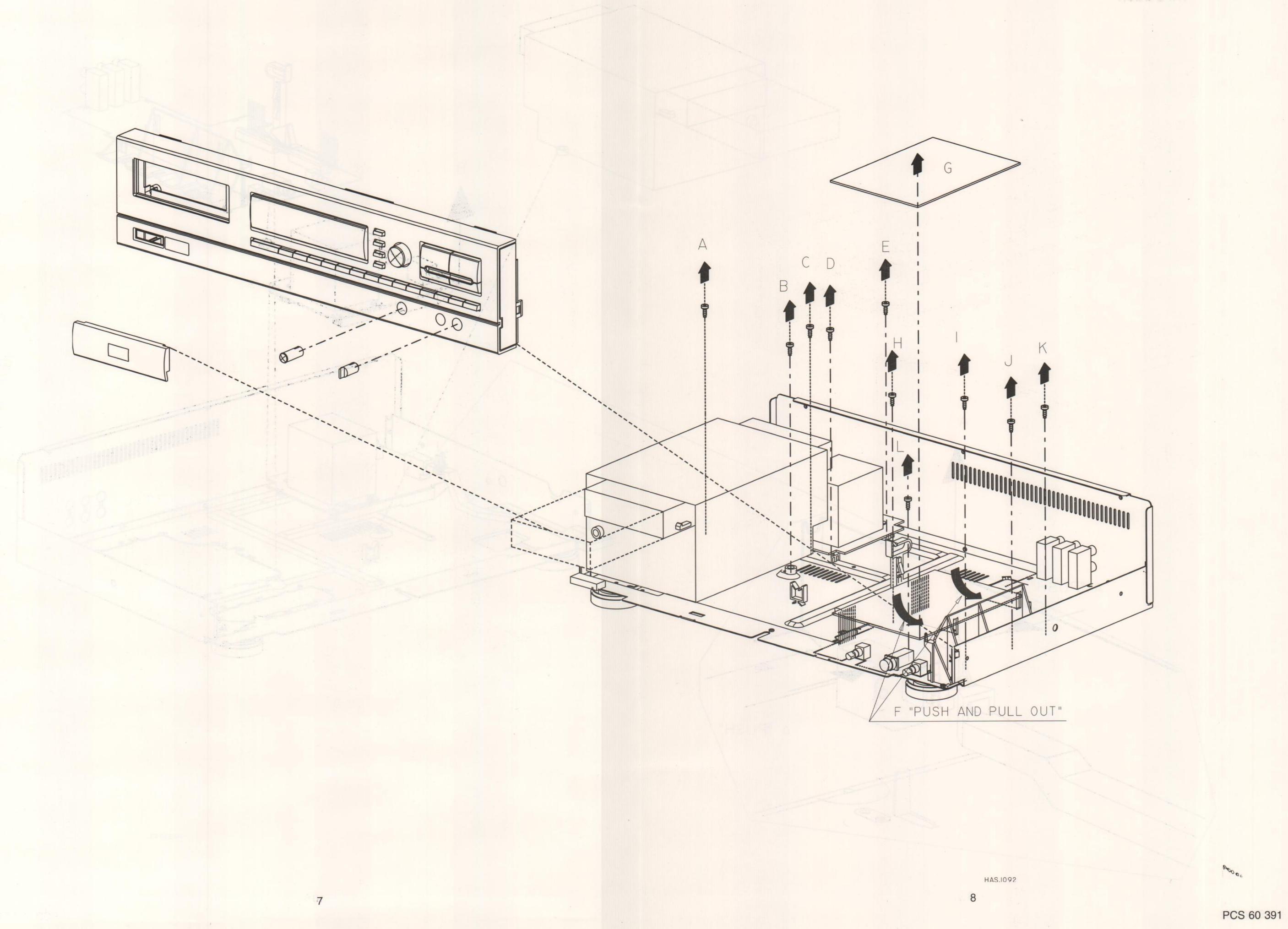
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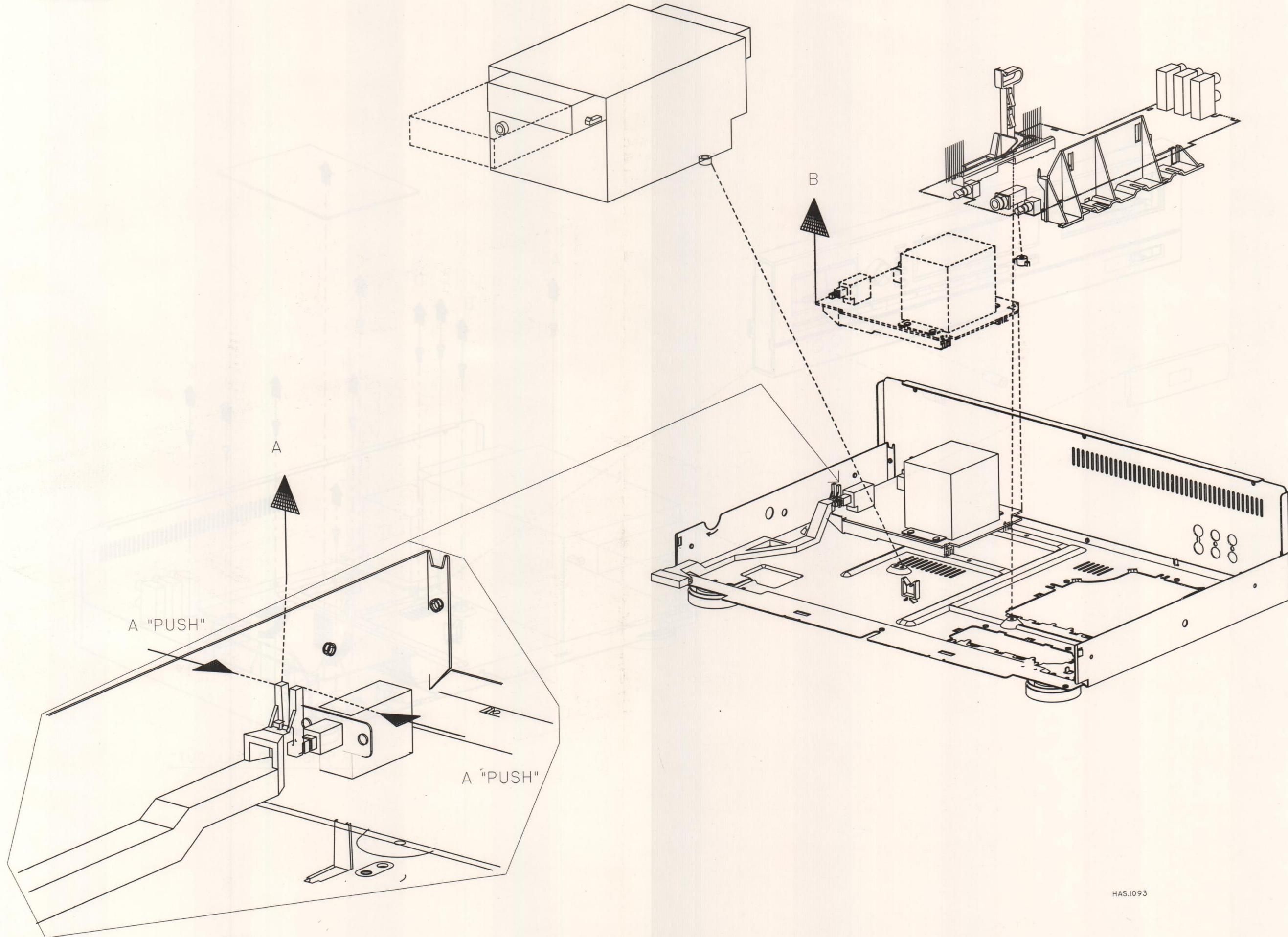


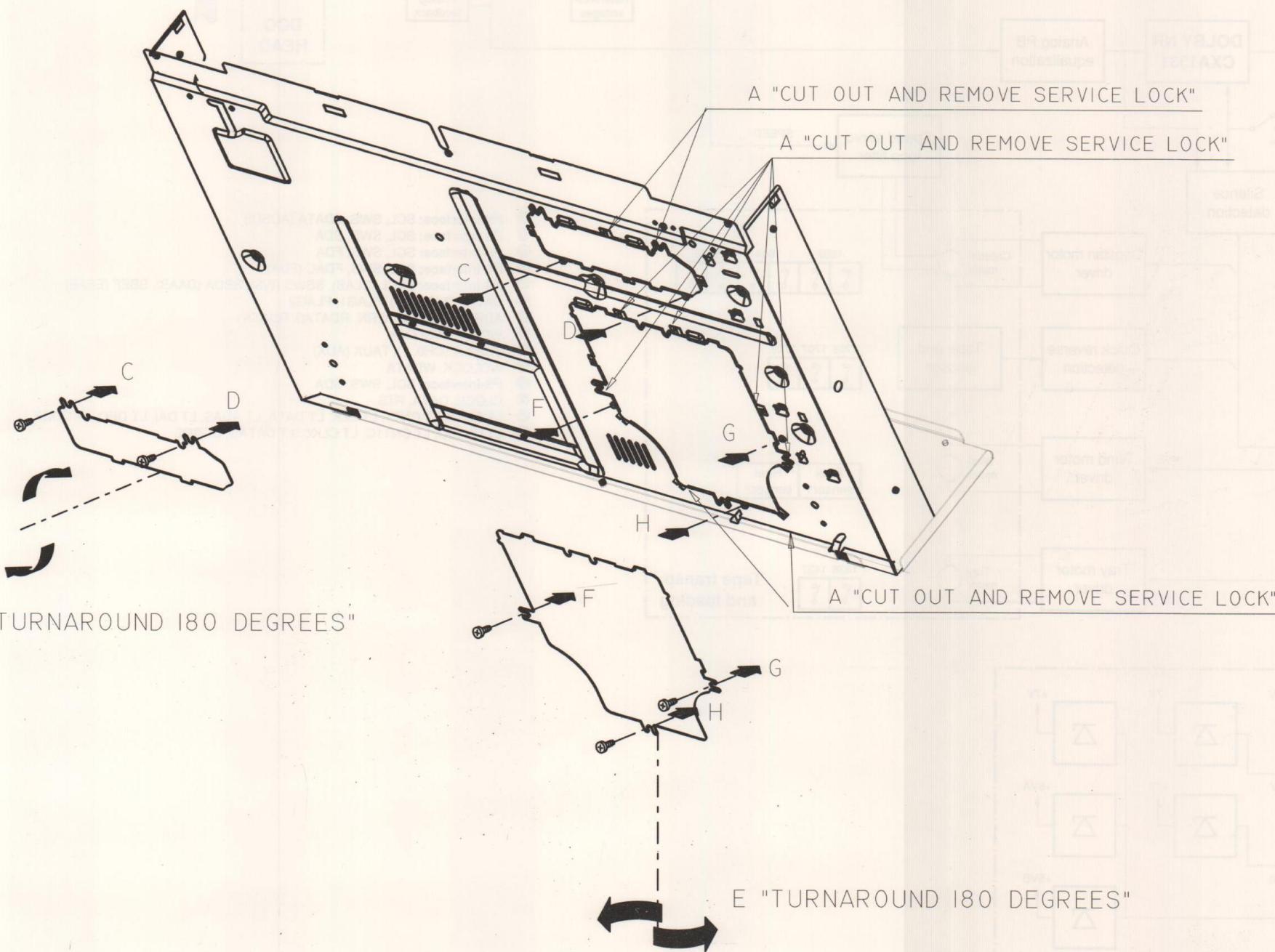
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- C. If the tray does not move properly
you can use a screw driver

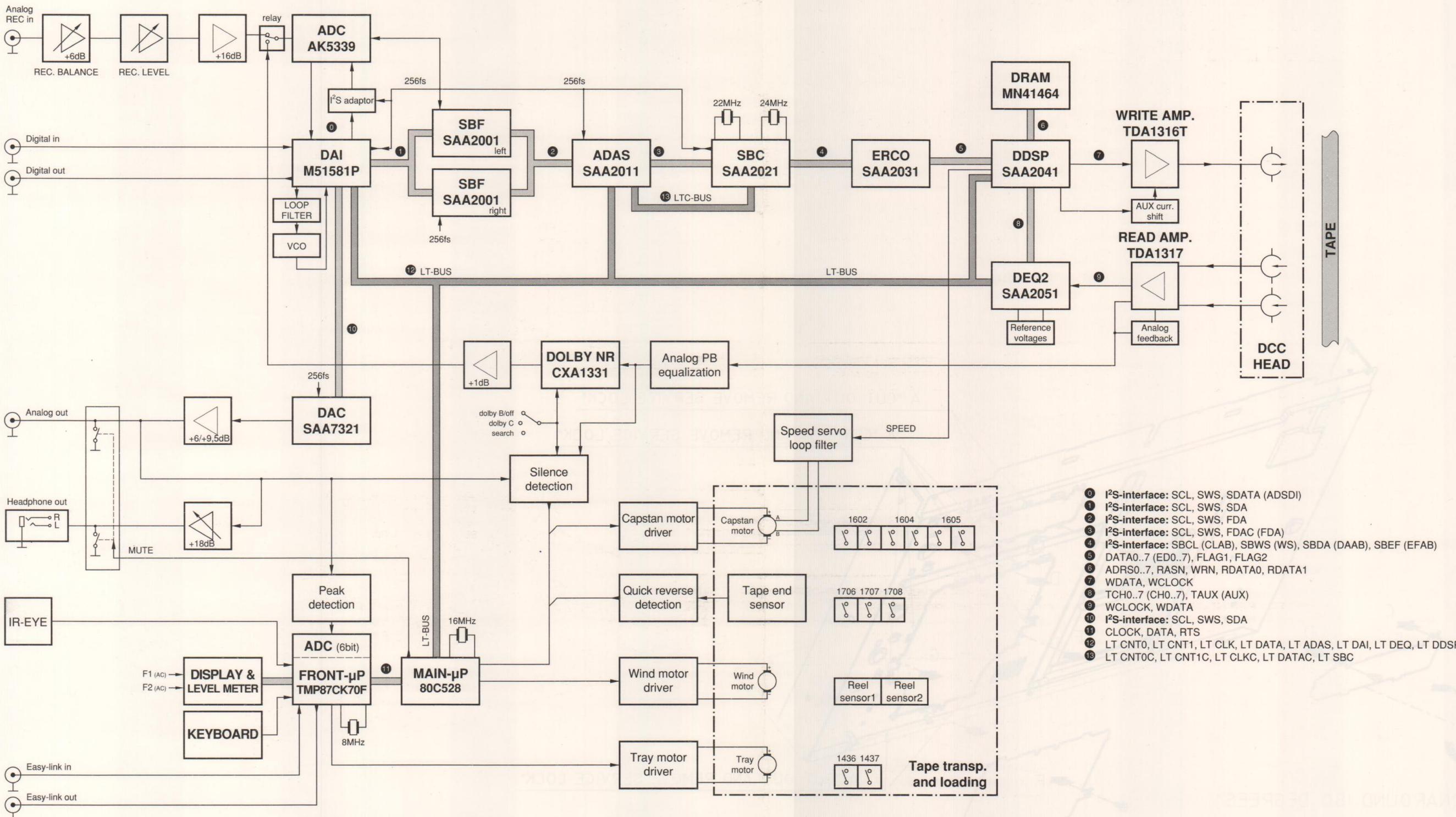








BLOCKDIAGRAM



Description of signal names

Signal name	Signal flow	Function	Explanation
128Fs	SBC → n.c.	clock	Clock output from SBC, 128 x sampling frequency.
256Fs	SBC ↔ DAI SBC → SBF SBC → ADC SBC → DAC SBC → ADAS	system clock	Master clock signal (256 x sampling frequency) for SBF, DAI, ADC, DAC and ADAS. Is generated by SBC with exception of the mode Digital Record. In that case the DAI is the MASTER and supplies 256Fs on MSTCK pin. See also MSTCK. Fs=32 kHz for DAB, DSR or BS (digital audio broadcast). Fs = 44.1 kHz for CD and DCC. Fs=48 kHz for professional recording and DAT.
ADRS0 ADRS1 ADRS2 ADRS3 ADRS4 ADRS5 ADRS6 ADRS7	DDSP → DRAM	address lines	8 address lines to DRAM to locate an address for writing data into or reading data from memory.
ADSDI	ADC → DAI	analog/digital serial data input	DAI input for serial data from AD convertor (see also S-DATA).
ADSEL	DAI → gnd	control line	Serial data output source selection
ANA L ANA R	read amp → analog Pb equalization	signal line	Analog signal left (right) channel playback analog compact cassette.
ASL	DAI → +5V	control line	Audio sample length selection
ATT	SBC → DAC	attenuation	Data input for DAC to set his attenuation register.
ATTDAC	SBC → DAC	attenuate DAC	Control line (output from SBC) connected to DAC attenuation input.
AUX	DEQ → DDSP	auxiliary channel output	Sliced output from DEQ of auxiliary channel data (bit rate 12 kb/s) routed to DDSP input TAUX.
AUXENV	DEQ → Main µP	auxiliary envelope	Digital representation of the AUX signal and monitors during DCC search mode the start of a track.
AZCHK	DDSP → test pin	azimuth check	Monitors the azimuth of channels 0 and 7 (output of DDSP).
BIASA BIASD	DEQ → high DEQ → low	control line control line	Bias current for internal A/D converter of DEQ2 Bias current for internal A/D converter of DEQ2
CAP A CAP B	Capstan motor → speed control	control line	Via connection points A and B of capstan motor the reference of the integrated speed control is controlled by the additional external speed control.
CAPSTAN	Main µP → 7359	control line	Low output level switches the capstan motor on.
CASN	DDSP → DRAM	control line	Column address strobe for DRAM
CH0 CH1 CH2 CH3 CH4 CH5 CH6 CH7	DEQ → DDSP	channel n	DEQ channel n output to DDSP inputs TCH0..TCH7.
CHROME	RE Deck electronic → Analog Pb equalization	control line	Indication if a chrome analog cassette is inserted. Chrome Cassette is high level.

Signal name	Signal flow	Function	Explanation
CKACO	DAI → n.c.	Testpin	Frequency accuracy check output. (1=frequency deviation > 0,14% = error condition)
CLAB	ERCO ↔ SBC	I ² S bit clock	Bit clock I/O from ERCO directly connected to SBC I/O SBCL pin (see also SBCL).
CLOCK	Main μP → Front μP	clock	Clock frequency for data transfer.
CLK22	SBC → n.c.	22.5792 MHz clock output	
CLK24	SBC → DDSP SBC → DEQ SBC → ADAS	24.576 MHz master clock	Master clock from SBC to DDSP, ADAS and DEQ to determine the length of tape frame and inter frame gap. In case of a digital recording this clock is not synchronous with the sampling frequency and its related frequencies, coming from the DAI (see also F24).
DAAB	ERCO ↔ SBC	serial data (I ² S)	Bidirectional I ² S serial data line between ERCO and SBC (see also SBDA).
DATA	Front μP ↔ Main μP	data line	Communication line Front μP - Main μP.
DATA0	ERCO ↔ DDSP	data line n	Parallel data lines for symbol transfer between ERCO and DDSP. DDSP is the master.
DATA1			See also ED0 ... ED7.
DATA2			
DATA3			
DATA4			
DATA5			
DATA6			
DATA7			
DCC RESET	Main μP → ADAS Main μP → DAI Main μP → DDSP	control line	Reset output for Main μP for digital board.
DEEMDAC	SBC ↔ DAC	deemphasize DAC	Control line for DAC
DET1	sensor tape counter → Main and Front μP	Indication of reel movement	Signals enable the μP: - to detect if both reels move - tape end indication - source for tape counter - to calculate speed while WIND and REWIND - position of tape
DIGEYE	DEQ → test pin	digital eye output	Serial data output signal to obtain digital eye pattern to test equalization performance of the channels. See also VAL.
DIG OFF	MUTE circuit → DAI	control line	Mutes the DAI during switch on/off the set additional to the analog outputs.
DMUTE	Main μP → read amp	control line	Mutes the digital part of the read amplifier when playing analog cassettes.
EASY LINK	Easy link interface ↔ Front μP	Easy link bus	Easy link bus enables to control the set via another set (e.g. amplifier). Easy link command to/from internal μP to external set.
EL switch	easy link indication → Front μP	control line	Indication for Front μP if set is equipped with Easy link input.
ED0	DDSP ↔ ERCO	Erco data line	Bidirectional parallel databus between DDSP and ERCO. See also DATA0 ... DATA7.
ED1			
ED2			
ED3			
ED4			
ED5			
ED6			
ED7			

Signal name	Signal flow	Function	Explanation
EFAB	ERCO → SBC	Error flag	I ² S error flag directly connected to SBC input SBEF to give the error status of bytes being transferred during data playback (see also SBEF).
F24	DDSP ← SBC DEQ ← SBC	24.576 MHz master clock	Master clock from SBC to DDSP and DEQ to determine the length of tape frame and inter frame gap. In case of a digital recording this clock is not synchron with the sampling frequency and its related frequencies, coming from the DAI (see also CLK24).
FAST	Main μP → wind motor driver	control line	High output level switches +12V supply to the motor bridge in order to obtain high speed.
FDA	SBF ↔ ADAS SBC ↔ ADAS	filtered data	Bidirectional serial data line between SBF and ADAS respectively SBC and ADAS.
FDAC	ADAS ↔ SBC	filtered data	Data transfer in I ² S format, carrying 32 sub-band channels digital audio data (see also FDAF and FDAC). Each SWS period 2x18 bits data are transferred.
FDAF	ADAS ↔ SBF	filtered data	Filtered data transfer between ADAS and SBF (see also FDA).
FDIR	SBC → SBF SBC → ADAS	direction control	Control line output from SBC to SBF and ADAS to indicate the mode of operation. FDIR=1; decoding mode (sub-band synthesis) FDIR=0; encoding mode (sub-band analysis).
FLAG1	ERCO ↔ DDSP	data bus flag	Data lines for symbol transfers between ERCO and DDSP. DDSP acts as the master (see also ED8 and ED9).
FLAG2	DAI → gnd	error flag	Error flag input
FLAGI	DAI → n.c.	error flag	Error flag output
FRESET	SBC → SBF SBC → ADAS	filter reset	Reset output from SBC to cause a general reset for SBF and ADAS.
FSYNC	SBC → SBF SBC → ADAS	filter synchronization	At filter sync, with a repetition rate of Fs/32, the transfer of the 2x32 sub-band samples is started. Fsync ensures each SBF is synchronized with the SBC to permit only transfer of sub-band 0 data during FSYNC.
HRESET	mute circuit → Front μP	control line	Switches Front μP on/off. Via mute circuit there is detected if the set is switched on or off. Via the HRESET line the front μP gets this info
ICLAMP	write amp → 1706	clamp circuit output	During the periods, when the head elements are not selected, the write current is directed through the external resistor connected to ICLAMP.
IFL	DDSP → ERCO	imposed flag	During the ERCO encoding mode the IFL line from DDSP is used to force the symbol currently transferred to the ERCO to become a parity symbol during ERCO encoding.
IIS	DAI → +5V	control line	I ² S Bus format selection (1=I ² S Bus, 0=non I ² S Bus format)
IMSTART	DAI → Main μP	information message start	Control line from DAI to main μP to indicate the start of a message transfer.

Signal name	Signal flow	Function	Explanation
IN0 IN1 IN2 IN3 IN4 IN5 IN6 IN7	Head → read amp	data lines	Head signals of main data channels 0-7
INAUX	Head → read amp	data line	Head signal of auxiliary data.
INHERCO	DDSP → ERCO	inhibit ERCO	Control line output of DDSP to inhibit the ERCO for settings transfer. These settings determine whether the ERCO should encode or decode (see also SETINH).
INL	Head → read amp	analog data line	Analog input signals from DCC head
INMFL	read amp →	feedback line	Magnetic feedback amplifier input left
INMFR	read amp →	feedback line	Magnetic feedback amplifier input right
INR	Head → read amp	analog data line	Analog input signals from DCC head
INTL	DAC → L-ch	integrator left	Analog output of the DAC (outputs from the left positive and negative switched-capacitor integrator) to the left channel amplifier stage.
INTR	DAC → R-ch	integrator right	Analog output of the DAC (outputs from the right positive and negative switched-capacitor integrator) to the right channel amplifier stage.
IOSC1	ERCO ← SBC	input oscillator	Oscillator input for ERCO coming from the sub-band coder SBMCLK output. The nominal frequency is 6.144 MHz. See also SBMCLK.
IRQU	DAI → μP	information request microprocessor	Control line to indicate the main microprocessor information can be read.
I ² S-bus		inter IC sound	3-line serial bus consisting of a line for two time-multiplexed audio data channels, a word select line for indication of the channel being transmitted (left or right) and a clock line. The lines are called SD, WS and SCK. The device which generates the SCK and WS is the master. See also SCK, SWS and SDA.
L-IN	Relay 1307 → ADC	signal line	Analog signal input left channel for ADC from recording amplifier or dolby IC selected by relay 1307.
L-OUT	DAC → line out amp	signal line	Analog signal output left channel of DAC.
LABEL	DEQ → μP	label	Search mode label detection output of DEQ signals that a label is found in the AUX-channel. When DCC player is in search mode, the tape speed increases. LABEL information is encoded throughout its length. To examine the length of a label, the tape speed must be known. In search mode DEQ assesses the speed of labelled tapes. The microprocessor obtains this information via the LT-interface.
LEVEL	Main μP → silence detection	control line	Control signal to adapt the silence detection circuit to the tape speed. (High speed during search, Normal speed in Rec mode.)
L/R	ADC ←	L/R clock input	Word clock input for the ADC
LRCKPOL	DAI → gnd	control line	polarity of LRCK selection

Signal name	Signal flow	Function	Explanation
LT-Bus	μP → DAI μP → ADAS μP → DEQ μP → DDSP		LT-interface is used for the system control of the digital panel. The LT-interface consists of clock-, data-, control- and enable lines.
LTC-Bus	ADAS → SBC		LTC-interface is mainly used for transfer of allocation information from ADAS to SBC. (Encoding mode) The LTC-interface consists of clock-, data-, control- and enable lines.
LTCLK	μP → DAI μP → ADAS μP → DEQ μP → DDSP	LT-clock	Bit clock line for the LT-interface. Main microprocessor supplies the bit clock and acts as master whilst the other devices perform as slaves.
LTCLKC		LTC-clock	Bit clock line for the LTC-interface. Main microprocessor supplies the bit clock and acts as master whilst the other devices perform as slaves.
LTCNT0 LTCNT1	μP → DAI μP → ADAS μP → DEQ μP → DDSP	LT control lines	Control lines of the LT-interface output from main microprocessor. LTCNTn determine the type of transfer to occur across the LTDATA serial data line to/from microprocessor.
LTDATA	μP → DAI μP → ADAS μP → DEQ μP → DDSP	LT data	Bidirectional serial data line of the LT-interface from/to microprocessor. Direction of data transfer is dependant on the information on LTCNT0 and LTCNT1.
LTEN LT-ADAS	μP → ADAS	LT enable ADAS	Activates the LT-interface of the ADAS in case LTENA =1.
LTEN LT-DAI	μP → DAI	LT enable DAI	Activates the LT-interface of the DAI in case LTEN (on DAI) =1.
LTEN LT-DDSP	μP → DDSP	LT enable DDSP	Activates the LT-interface of the DDSP in case LTEN (on DDSP) =1.
LTENDEQ LT-DEQ	μP → DEQ	LT enable DEQ	Activates the LT-interface of the DEQ in case LTENDEQ =1.
MAG	Main μP → solenoid control circuit	control line	Low output pulse switches the solenoid.
MCLK	DDSP → ERCO	master clock	MCLK line of the DDSP provides the 6.144 MHz master clock signal and is connected to the MCLK input of the ERCO. This clock (128 x Fs) is used for the symbols transfer between DDSP and ERCO.
MFL1 MFL2	read amp ← head		feedback amplifier output left
MFR1 MFR2	read amp ← head		feedback amplifier output right
MODE0 MODE1	DAI ← gnd	mode selection input	Control lines from to select the operation mode of the DAI. DAI operates in μP mode when both lines are at '0' level.
MPCL	DDSP → ERCO	clock phase reference	The MPCL output of the DDSP provides the 3.072 MHz (64 x Fs) clock phase reference signal which is connected to the MPCL input of the ERCO.
MSBF	DAI → +5V	control line	1 = Most Significant Bit First 0 = Least Significant Bit First
MSTCK	DAI ↔ 256Fs	master clock	Bidirectional master clock line. Dependant on CKSEL settings the master clock is at 128Fs or 256Fs. See also

Signal name	Signal flow	Function	Explanation
MUTE1	DAI ← μP	mute audio	256Fs.
MUTE	Main μP → mute circuit	control line	Control line from microprocessor to mute the digital audio interface. The audio output of the DAI is kept zero when the PLL is not locked in the reception mode (see also UNLOCK).
MUTEDAC	DAC ← SBC	mute	Input line. See also MUTEDAC.
MUXAUX	SBC → DAC	mute DAC	control output line of SBC for D/A convertor.
NER0	read amp →	data line	multiplexed auxiliary data
NER1	ERCO → test connector	number of erasures	The NERx outputs produce an indication of the number of erasures encountered in the code word currently being processed.
NER2	ERCO → test connector	number of erasures	
NFR	DAI → DAI	ext. resistor	Output of level converter
NODONE	ADAS → +5V	control line	
OERDCB	DDSP → ERCO	output enable for ERCO	Indication for the ERCO to output data on the data bus lines (DATA1..DATA7, FLAG1 and FLAG2).
OUT0	read amp → n.c.	output	parallel output of read IC.
OUT1	read amp → n.c.	output	
OUT2	read amp → n.c.	output	
OUT3	read amp → n.c.	output	
OUT4	read amp → n.c.	output	
OUT5	read amp → n.c.	output	
OUT6	read amp → n.c.	output	
OUT7	read amp → n.c.	output	
OUTAUX	read amp →	output line	Analog out of read IC.
OUTL	read amp →	output line	
OUTR	read amp →	output line	
PD1	DAI → VCO	phase detector	Phase detector output from DAI for the charge pump of the VCO. The VCO locks to incoming frequencies on digital input. When locked the DAI supplies the 256Fs master clock.
PD2	DAI → VCO	phase detector	
PLAY MAG	Solenoid control → RE Deck electronic	control line	Connection to the solenoid on the RE deck. Low pulse is solenoid on.
PRGSTAT	DDSP → n.c.	program status	DDSP program status output.
PWRDWN	DDSP → n.c.	program status	

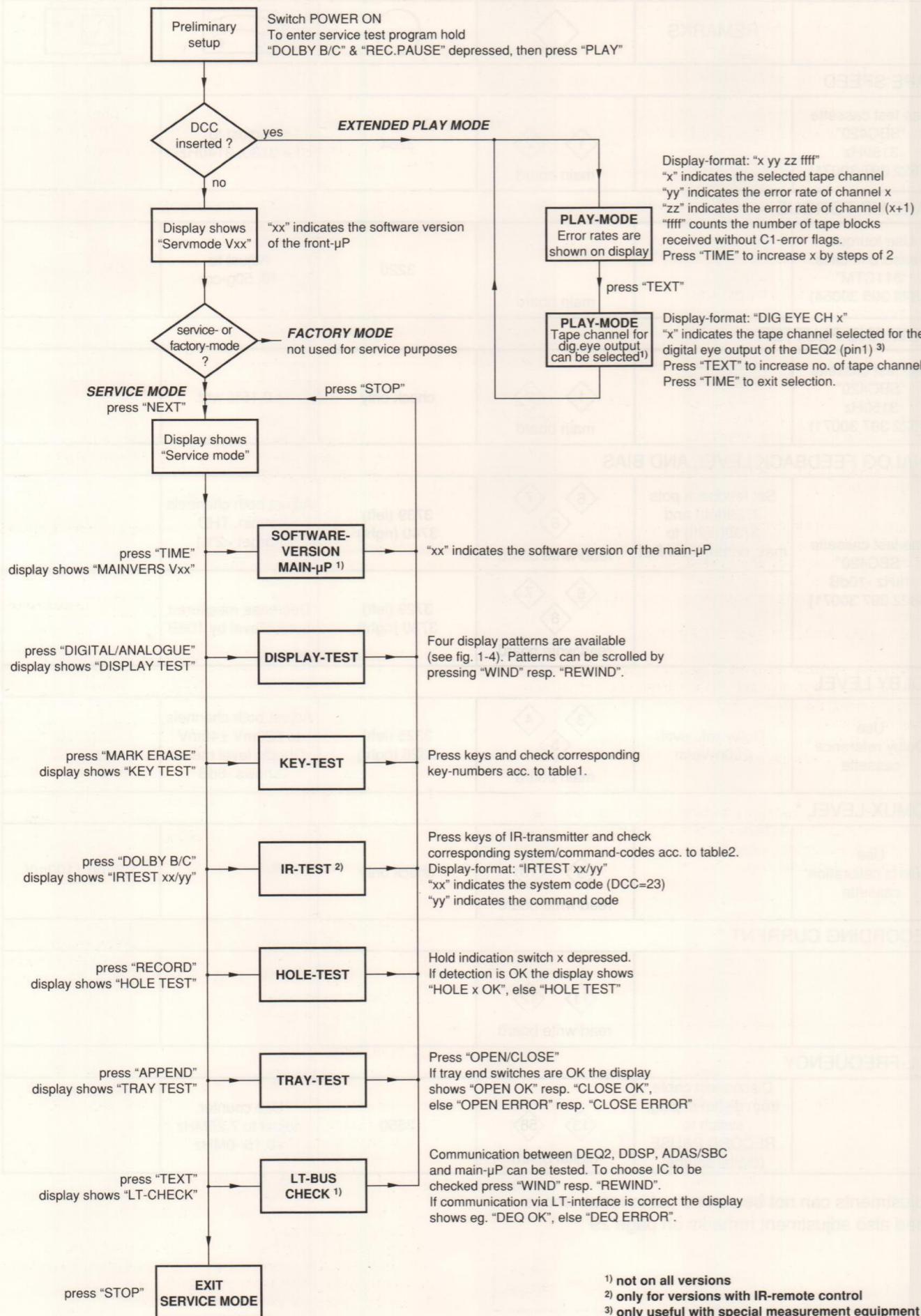
Signal name	Signal flow	Function	Explanation
QA	write amp → head	data line	Signal for write heads
QB	write amp → head	data line	
QC	write amp → head	data line	
QD	write amp → head	data line	
QE	write amp → head	data line	
QF	write amp → head	data line	
QG	write amp → head	data line	
QH	write amp → head	data line	
QI	write amp → head	data line	
QJ	write amp → head	data line	
QU. REV	tape end sensor → quick reverse detection → Main μP	control line	At tape end of a digital compact cassette the reflecting foil causes a HIGH pulse out of the light barrier. The quick reverse detection circuit works as a Schmitt trigger in order to suppress disturbances. The output pulse controls the main μP. If a valid pulse is detected the main μP switches the tap transport to reverse operation immediately in order to overjump the non magnetic transparent leader tape of the cassette.
R-IN	Relay 1307 → ADC	signal line	Analog signal input right channel for ADC from recording amplifier or dolby IC selected by relay 1307.
R-OUT	DAC → line out amp	signal line	Analog signal output right channel of DAC.
RASN	DDSP → DRAM	row address strobe negative	row address strobe for DRAM.
RDATA0	DDSP ↔ DRAM	RAM data bus	Bidirectional data bus between DDSP and DRAM. On DRAM IC these lines are called DQ1..DQ4.
RDATA1	DDSP ↔ DRAM	RAM data bus	
RDATA2	DDSP ↔ DRAM	RAM data bus	
RDATA3	DDSP ↔ DRAM	RAM data bus	
RDCLK	DEQ → read amp	read clock	Data clock (3,072 MHz) for the read amplifier. The data of 8 data channels and 1 aux channel is transferred during 10 RDCLK periods.
RDMUX	read amp → DEQ	read multiplex	Read multiplexer output from read amplifier to DEQ. See also VIN.
RDSYNC	DEQ → read amp	read synchronization	Control output of DEQ to read amplifier to synchronize the read amplifier multiplexer and the DEQ demultiplexer.
READB	DDSP → ERCO	read enable	Read enable for ERCO. When active the ERCO reads data from DDSP on data bus ED0..ED9.
REF1	read amp ←	reference	Reference voltage input of read amplifier.
REF2	read amp ←	reference	
REF3	read amp ←	reference	
REF4	read amp ←	reference	
REFCK	DAI ← gnd	clock input	Reference clock for frequency accuracy check (normal 9,408 MHz). Not used in our set.
REFN	DEQ ←	reference	Lower reference voltage for internal ADC.
REFP	DEQ ←	reference	Upper reference voltage for internal ADC
RESET	Main μP → ADAS Main μP → DDSP Main μP → DAI	control line	Reset.
RESETC	DDSP → ERCO	reset erco	Control output from DDSP to ERCO to reset ERCO.
RESOL0	ADAS ← +5V	control line	Resolution selection. 10 = 18 bit selected
RESOL1	ADAS ← gnd	control line	
REWIND	Main μP → wind motor driver	control line	Control signal to switch the driver bridge of the wind motor to the desired function. High output level is rewind mode.

Signal name	Signal flow	Function	Explanation
ROEN	DDSP → DRAM	output enable	Output enable for DRAM.
RTS	Main μP ← Front μP	control line	Request to send
RSENSE	write amp ←	sense line	feedback voltage
RX1	DAI ← COAX in	receive data	IEC format digital audio data input for coaxial input. (min 200mVpp)
RX2	DAI ← OPT in	receive data	IEC format digital audio data input for optical input. (logical CMOS levels)
RXCKI	DAI ← VCO	receive clock input	Input for VCO frequency (256Fs).
RXCKO	DAI → VCO	receive clock output	Output for VCO frequency (256Fs).
RXSEL	DAI ← +5V	receiving mode selection	Selection between reception inputs RX1 and RX2. (1 = RX1)
SBCL	SBC ↔ ERCO	sub-band clock	SBCL line is part of the S(ub)-B(and)-I ² S interface and provides the bit clock. See also CLAB.
SBDA	SBC ↔ ERCO	sub-band data	Sub-band I ² S interface line for serial data transfer between SBC and ERCO.
SBDIR	SBC ← DDSP	sub-band direction	Control line from DDSP to SBC to indicate the direction of the data flow between ERCO and SBC on SBDA line. See also SBPLB.
SBEF	SBC ← ERCO	sub band error flag	I ² S error flag to give the error status of bytes being transferred during data playback to the SBC (see also EFAB).
SBMCLK	SBC → ERCO	sub-band master clock	Master clock (6.144 MHz) for ERCO (see also IOSC)
SBPLB	DDSP → SBC	sub-band direction	Control line from DDSP to SBC to indicate the direction of the data flow between ERCO and SBC on SBDA line. See also SBDIR.
SBWS	SBC ↔ ERCO SBC → DDSP	sub-band word select	The SBWS signal indicates the channel of the sample (either left or right) and is equal to the sampling frequency Fs. On the ERCO and DDSP devices the signal is called WS (see also WS).
SCL	SBC → SBF SBC → ADAS SBC ↔ DAI DAI → I ² S adaption of ADC	serial clock	Bit clock for the I ² S-interface. Clock frequency is 64x sampling frequency. See also BCKI and SCLK.
SDO	DAI → n.c.	serial data output	Serial data output for digital audio data bus.
SDA	DAI ↔ SBF DAI → DAC ADC → DAI	serial data	Serial data line of I ² S-bus. The data line carries digital audio (broad band data) according I ² S-format. Two samples (left- and right channel) are transferred during one SWS-period. The ADC outputs broad band data via its SDATA pin, the DAI receives data on its ADSDI pin and outputs data on SDA, the DAC on SDI1 and SDI2.
SDATA	ADC → DAI	serial data	Serial data output of AD convertor which is transferred to DAI data input ADSDI (see also ADSDI).
SEARCH	DDSP ← DEQ	control line	search level detection input

Signal name	Signal flow	Function	Explanation
SELERFI	DDSP → ERCO	select ERCO/FIFO	Control line output of DDSP to determine the nature of data transferred to ERCO. If SELERFI=1 the transfers are to and from the error correction section. If SELERFI=0 transfers are to and from I ² S-interface section of the ERCO device.
SENSRES			Pin for external resistor.
SETDAT	ERCO ← DDSP	settings data register	Data settings line for the settings register of the ERCO. SETDAT determines the operational mode of the ERCO device. See also SETERCO.
SETERCO	DDSP → ERCO	set ERCO	Output of DDSP to transfer control settings of the ERCO (see also SETDAT). These settings determine whether ERCO should encode or decode and it also designates the direction of data transfer for the I ² S-interface.
SETINH	ERCO ← DDSP	settings inhibit	When SETINH is active the ERCO can receive settings data (via SETDAT line) from DDSP for its operation mode (see also INHERCO, SETDAT and SETERCO).
SETPIN1 SETPIN2	DDSP →	control line	Port expander output pins of DDSP. See also RESET SBC
SETSY	DAI ← SBC	settings sync	DAI latches new settings in internal register when SETSY is active. SETSY is sent by SBC which takes care for external clock source synchronization (see also SYNCDAI).
SILENCE	silence detection → Main μP	control line	Indication for main μP that pause has been detected.
SH0 SH1	DEQ ← gnd	control line	Selection of desired 8bit window of internal ADC of DEQ2.
SPEED	DDSP → speed control circuit	speed control	Pulse width modulated control output of DDSP for phase regulating the speed of the capstan in the tape deck (tape speed).
SRESET	Front μP → Main μP	reset	Software reset.
STARTSEG	DDSP → μP	start segment	STARTSEG indicates the start of a new segment. The STARTSEG output from the DDSP is used as a timing reference for transfer of SYSINFO and AUX information between the microprocessor and the DDSP.
STBY	Front μP → power supply	control line	Switches +5V power supply off and subsequently +12V, -12V and -5V.
STMPB	DDSP → ERCO	start error correction program	STMPB initiates the execution of the error correction program, to begin processing a new code word and causes activation of the new settings for both I ² S-interface and the ERCO.
STOP	RE Deck electronic → Main μP	control line	Indicates if the head support is in play or stop position. Stop position is high level.
SWS	SBC → ADAS SBC → SBF SBC ↔ DAI SBC → I ² S Adaptor	word select	Word select line (at sampling frequency) for I ² S interface. SBC acts as the master with the exception of the mode digital recording. In that case DAI is the master. SWS of the SBC is connected to SWS of the DAI, to SWS of the I ² S Adaptor and to SWS of the DAC.
SYNCDAI	SBC → DAI	synchronize DAI	With SYNCDAI (identical with SETSY) the settings for the DAI are latched. These settings are transferred via the LT-bus.
TCH0	Main μP → DAI		Prepared for future use.
	DDSP ← DEQ	channel input	Parallel input lines of DDSP receiving sliced (digital)

Signal name	Signal flow	Function	Explanation
TCH1	analog left channel from 4-CH to digital with DEQ		information of DEQ (see also AUX and CH0..CH7).
TCH2	analog mid band from 4-CH to digital with DEQ		
TCH3	analog right channel from 4-CH to digital with DEQ		
TCH4	analog tape channel from 4-CH to digital with DEQ		
TCH5	analog tape channel from 4-CH to digital with DEQ		
TCH6	analog tape channel from 4-CH to digital with DEQ		
TCH7	analog tape channel from 4-CH to digital with DEQ		
TCHAUX	analog tape channel from 4-CH to digital with DEQ		
TX	DAI → digital out	transmit data	Digital data output of DAI according IEC format.
UNLOCK	DAI → n.c.	unlock VCO	UNLOCK indicates that VCO frequency is locked/unlocked to received data.
URDA	DDSP → SBC	unreliable data	Only during playback URDA indicates that, regardless of all other flag information, all main data, system information or AUX data is unusable. URDA occurs during a mode change from data recording to playback or if the DDSP must re-synchronize with the tape signals.
USYNCI	DAI ← μP	sync input	Indicates the start of a new data frame when in transparent mode.
USYNCO	DAI →	sync output	Indicates start of a new data frame when in reception mode.
VAL	DEQ → test pin	validation data	Validation signal output for data bits. To test equalization performance it is possible to output the equalized channels. The DEQ has for this purpose two digital outputs present: DIGEYE and VAL (see also DIGEYE).
VIN	DEQ ← read amp	voltage input	DEQ inputs via VIN time multiplexed data from read amplifier. See also RDMUX.
VIRGIN	DEQ → DDSP	virgin detection	Control output of DEQ to inform the microprocessor if a blank tape is inserted. See also SEARCH.
WCLK	write amp ← DDSP	write clock	Clock signal for the write amplifier as timing reference (f = 3.072MHz). See also WCLOCK.
WCLOCK	DDSP → write amp	write clock	Write clock for write amplifier coming from DDSP. See also WCLK.
WDATA	DDSP → write amp	write data	Serial data signal of the 8 main channels and AUX channel, directed to the write amplifier.
WIND	Main μP → wind motor driver	control line	Control signal to switch the driver bridge of the wind motor to the desired function. High level in FastForward and in Play mode.
+WIND -WIND	Wind motor driver → wind motor	supply line	Connection to terminal of wind motor.
WRN	DDSP → DRAM	write enable	Write enable of the DRAM.
WS	ERCO ↔ SBC DDSP ← SBC	word select	I ² S-interface word selection I/O line. Is connected to SBWS pin of SBC. See also SBWS.
X22IN X22OUT	SBC ← SBC →	crystal input crystal output	22,5792 MHz
X24IN X24OUT	SBC ← SBC →	crystal input crystal output	24,576 MHz
XTAL1 XTAL2	DAC ← 256Fs DAC → n.c.		
XSYS	DAC → n.c.		buffered output from crystal oscillator.

SERVICE TEST PROGRAM - FLOW CHART



SERVICE TEST PROGRAM – DESCRIPTIONS

1. General

The test program is equipped with a service modes, a factory mode and an extended playback-mode. The service mode includes tests for display, detection of hole switches, IR-remote control and keys. The factory mode is a special test program, which is used in the production process of the front board, but not useful for service purposes. In the extended playback-mode the error rates (C1 error flags from DDSP) are shown on the display. Also the tape channel for the digital eye output of the DEQ2 (pin1) can be selected.

2. Preliminary setup EXTENDED PLAYMODE

- Switch POWER ON
- Follow "start-up procedure" as described on page 27
- Insert a DCC
- Hold DOLBY B/C & REC.PAUSE depressed, then press PLAY
- The set is now in the extended playmode, music will be audible. The display shows "x yy zzz".
- "x" indicates the selected tape channel
- "yy" indicates the error rate of channel x
- "zz" indicates the error rate of channel (x+1)
- "ffff" counts the number of tape blocks received without C1-error flags. Press "TIME" to increase x by steps of 2
- press "TEXT"
- Display-format: "DIG EYE CH x"
- "x" indicates the tape channel selected for the digital eye output of the DEQ2 (pin1)³⁾
- "ffff" counts the number of tape blocks received without C1-error flags. The counter will be resetted with every C1-error.
- Press TIME to increase x by steps of 2.
- To select the tape channel for the digital eye output of the DEQ2 press TEXT. The display shows "DIG EYE CH x". To increase the number of the tape channel press TIME, again.

3. Preliminary setup SERVICE MODE

- Turn POWER ON
- Follow "start-up procedure" as described on page 27
- Remove cassette from the cassette compartment
- Hold DOLBY B/C & REC.PAUSE depressed, then press PLAY
- To enter servicemode1 press the NEXT-button.
- The display shows "SERVICE MODE". The available tests can now be entered pressing the corresponding buttons (see also flow chart, page 25).
- To exit service mode press the STOP-button.

3.1. Display software version of main-μP (not on all versions)

To display the software version of the main-μP fulfil preliminary setup and press the TIME-button. The display shows "MAINVERS Vxx".

"xx" indicates the software version of the main-μP.

To exit this test press the STOP-button.

3.2. Display test

To enter the display test fulfil preliminary setup and press the DIGITAL/ANALOGUE-button. The display shows "DISPLAY TEST". Four display test pattern are available (see fig. 1-4). The patterns can be scrolled forward resp. backward by pressing the WIND- resp. REWIND-button.

To exit the display test press the STOP-button.

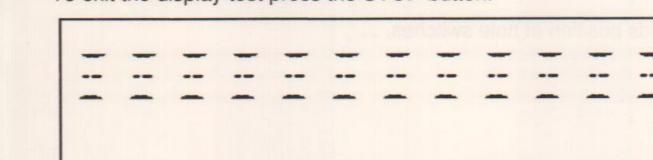


fig.1

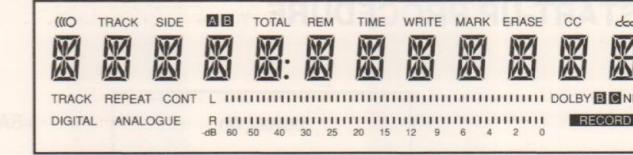


fig.4

3.3. Key test

To enter the key test fulfil preliminary setup and press the MARK ERASE-button. The display shows "KEY TEST". Press keys and check their corresponding key-numbers acc. to table1. To exit the key test press the STOP-button.

KEY	KEY NUMBER	KEY	KEY NUMBER
RENUMBER	01	RECORD	27
OPEN/CLOSE	03	DOLBY B/C	33
WRITE MARK	05	BACKWARD	35
PREVIOUS	09	CD SYNCHRO	37
APPEND	11	REPEAT	39
DIGITAL/ANALOGUE	17	SIDE A/B	41
FORWARD	19	RECORD/PAUSE	43
ERASE MARK	21	TEXT	65
PLAY	23	TIME	67
NEXT	25	RESET	73

table1

3.4. IR-test (only for versions with IR-remote control)

To enter the IR-test fulfil preliminary setup and press the DOLBY B/C-button. The display shows "IRTEST xx/yy".

"xx" indicates the system code (DCC=23)

"yy" indicates the command code

Press keys and check their corresponding system / command-code acc. to table2.

To exit the IR-test press the STOP-button.

SYSTEM-CODE 23 (DCC)			
KEY	COMMAND-CODE	KEY	COMMAND-CODE
0	00	PREVIOUS	33
1	01	RECORD MUTE	42
2	02	OPEN/CLOSE	45
3	03	SIDE A/B	47
4	04	RECORD PAUSE	48
5	05	RESET	49
6	06	BACKWARD	50
7	07	FORWARD	52
8	08	PLAY	53
9	09	STOP	54
TEXT	11	RECORD	55
STAND BY	12	MARK WRITE	114
REPEAT	28	APPEND	117
NEXT	32	TIME	122

table2

3.5. Hole test

To enter the hole test fulfil preliminary setup and press the RECORD-button. The display shows "HOLE TEST". Hold indication switch x depressed and check display. If detection is OK the display shows "HOLE x OK", else "HOLE TEST".

To exit the hole test press the STOP-button.

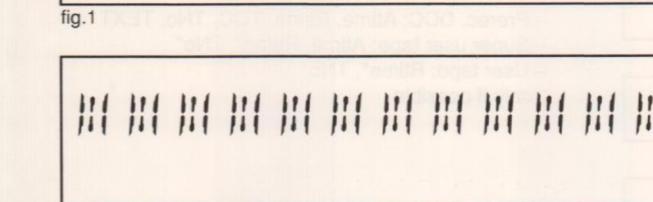


fig.2

3.6. Tray test

To enter the tray test fulfil preliminary setup and press the APPEND-button. The display shows "TRAY TEST". If tray end switches are OK the display shows "OPEN OK" resp. "CLOSE OK", else "OPEN ERROR" resp. "CLOSE ERROR".

To exit the tray test press the STOP-button.

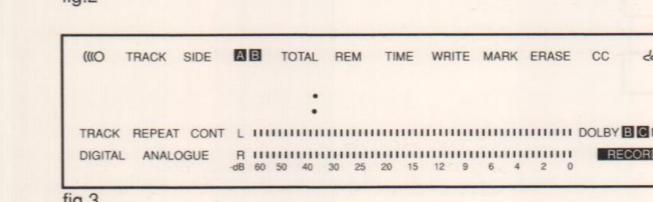


fig.3

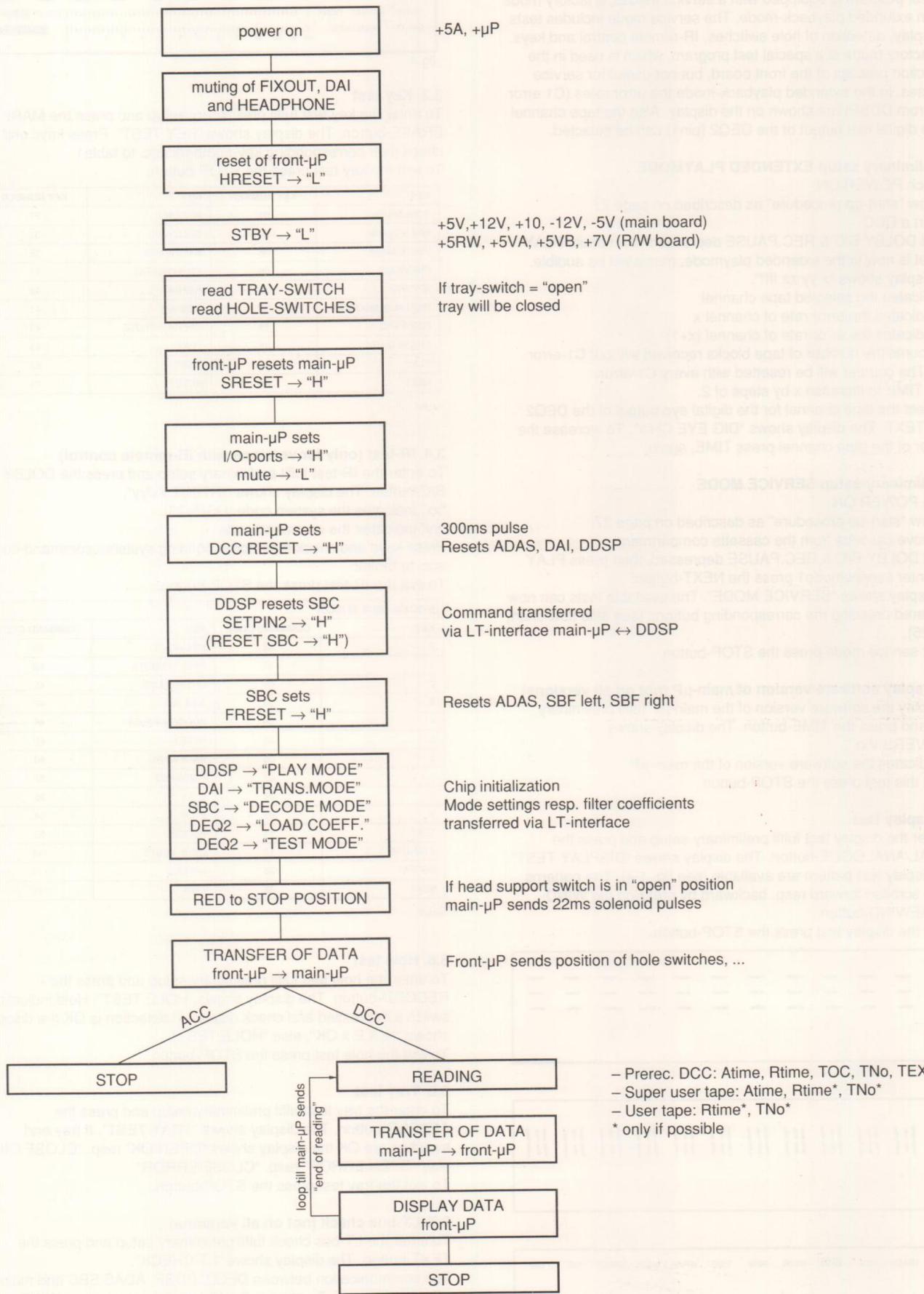
3.7. LT-bus check (not on all versions)

To enter the LT-bus check fulfil preliminary setup and press the TEXT-button. The display shows "LT-CHECK".

The communication between DEQ2, DDSP, ADAS/SBC and main-μP can be tested. To choose the IC to be checked press WIND resp. REWIND. If the communication via LT-interface is correct the display shows e.g. "DEQ OK", else "DEQ ERROR".

To exit the LT-bus check press the STOP-button.

START-UP PROCEDURE



ADJUSTMENT TABLE

	REMARKS				
TAPE SPEED					
Use test cassette "SBC420" 3150Hz (4822 397 30071)	 main board		3264	Adjust to f = 3130..3140Hz	
TAKE UP TORQUE					
Use torque measurem. cass. "811CTM" (4822 395 30054)			3220	Adjust to 40..50g-cm	
WOW AND FLUTTER					
Use test cassette "SBC420" 3150Hz (4822 397 30071)	 main board		check only	< 0,15% wtd.	
ANALOG FEEDBACK LEVEL AND BIAS					
Use test cassette "SBC420" 1kHz -10dB (4822 397 30071)	 read/write board	Set feedback pots 3729(left) and 3730(right) to max. output level first	3739 (left) 3740 (right)	Adjust both channels to min. THD (target <2%)	
	 read/write board		3729 (left) 3730 (right)	Decrease measured output level by 10dB	
DOLBY LEVEL					
Use Dolby reference cassette	 main board	Dolby ref. level 200nWb/m	3325 (left) 3326 (right)	Adjust both channels to 620mV ±40mV Check: level meter shows -6dB	
RDMUX-LEVEL *					
Use 9,6kHz calibration cassette	 read/write board		check only		1,2Vpp ±100mV
RECORDING CURRENT *					
	 read/write board				
PLL-FREQUENCY					
	 digital board	Disconnect cable from digital-in plug, switch to RECORD PAUSE (digital source)	3550	Use counter, adjust to 7,25MHz +0,15/-0MHz	

* Adjustments can not be carried out in repair shops.

Read also adjustment remarks on page 29

DCC-head

The heads used in the DCC-system are called "thin film head" and made by repeating 20 times or more of multiple evaporation and spatterings as in fabricating ICs.

Accordingly, the heads have different features and characteristics from those of coil winding type heads used in conventional analog cassette tape decks:

- The playback head uses a magnetic resistive element (MRH)
- The MRH needs magnetic bias to obtain its maximum output. So, a bias conductor which is equivalent to a coil to develop the magnetic bias is installed.
- Moreover, the analog playback head needs a magnetic feedback to increase linearity. This is realized by giving a magnetic field proportional to the MR-element output from a bias conductor.

For terminals and structure of the DCC-head see page 50, circuit diagram read/write board.

Handling DCC-heads

Caution: The heads are susceptible to electrostatic voltage higher than 150Vdc. The heads are protected from external electrostatic charging by connecting the head flexible cables to the read/write board. When disconnecting the cables, always place the deck on a bench with required electrostatic discharging measures taken and wear an electrostatic discharging band. Moreover, always mount the short-clip on the flexible cables removed.

The heads are also susceptible to strong external magnetic field and the analog output may be affected. Do not use a head demagnetizer, etc.

WARNING

DO NOT USE A DEMAGNETIZER CASSETTE !

Adjustment remark - recording current

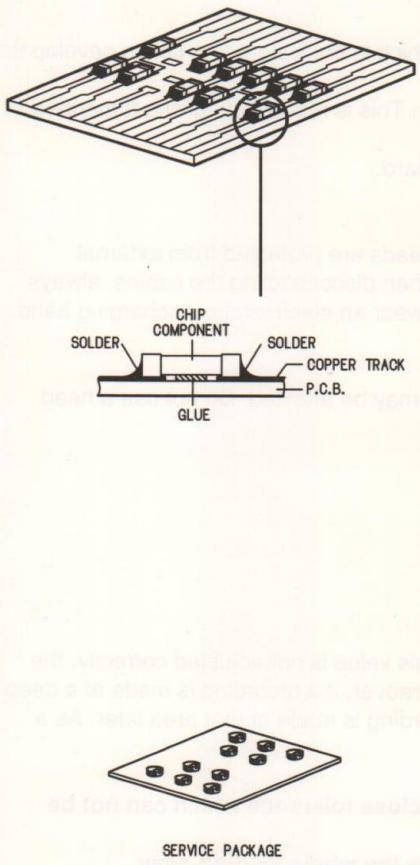
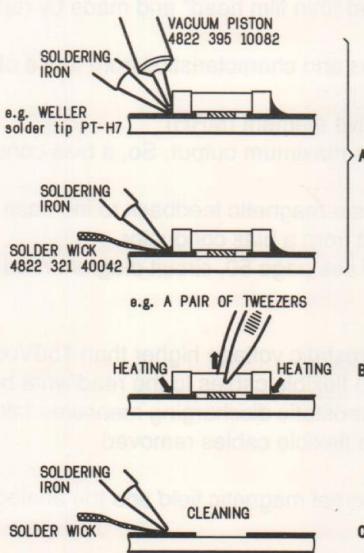
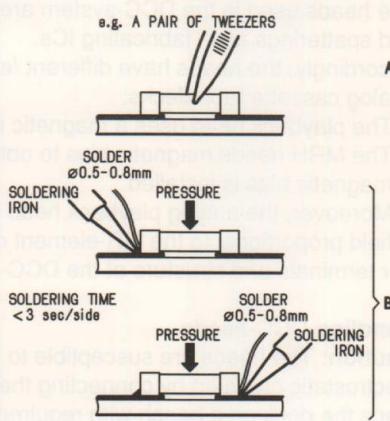
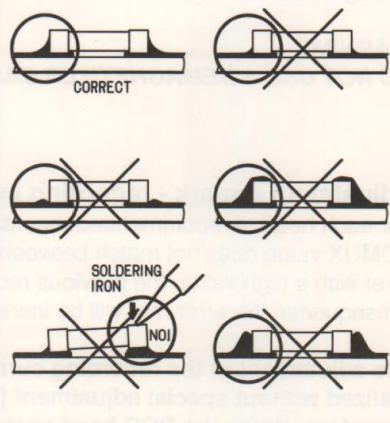
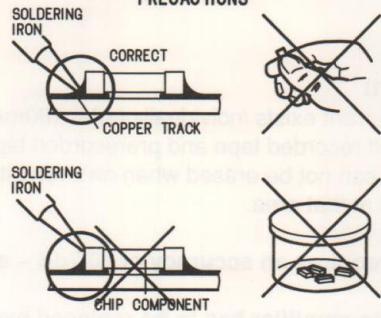
For each head, a recommended recording current exists individually (120..180mA). If this value is not adjusted correctly, the RDIMUX value does not match between a self recorded tape and prerecorded tape. Moreover, if a recording is made at a deep layer with a high value, the previous records can not be erased when an overwrite recording is made at that area later. As a consequence the error rate will be increased at that area.

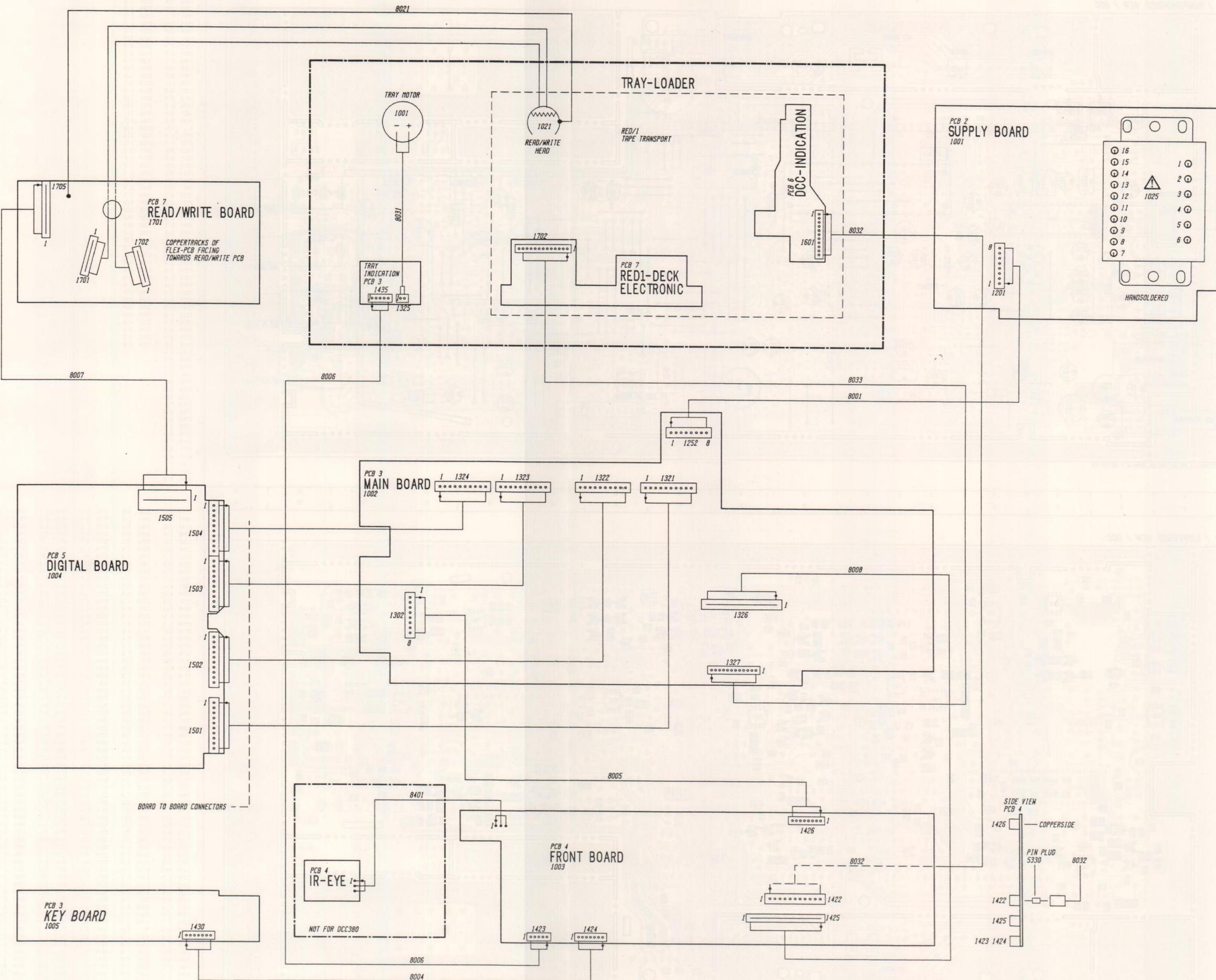
The adjustment of the recording current requires an accuracy of $\pm 0,2\text{dB}$ – a very close tolerance which can not be realized without special adjustment jigs.

Therefore: When the DCC-head or the write-amplifier has to be replaced exchange the whole loading-assy (4822 691 20833). The loading assy, delivered, is adjusted by the factory. Only adjustments which have to be carried out on the main-board have to be done in the repair shop (tape speed, take-up torque, dolby level).

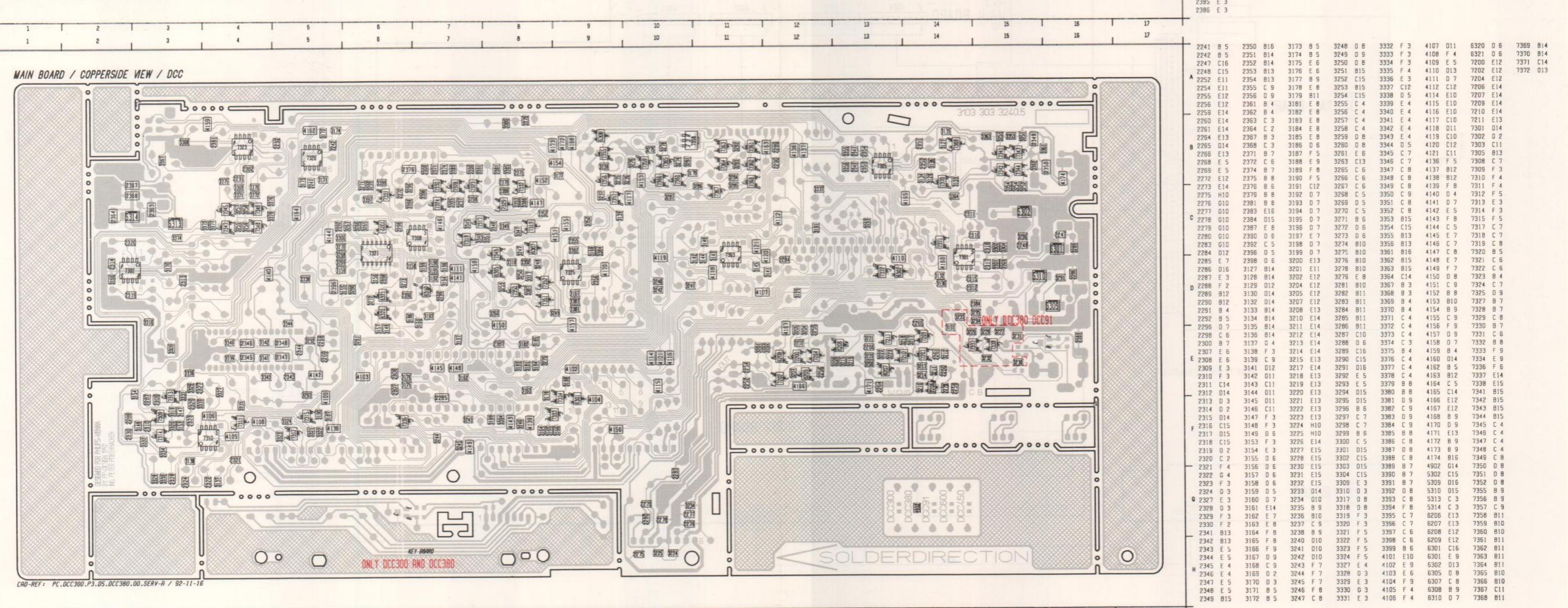
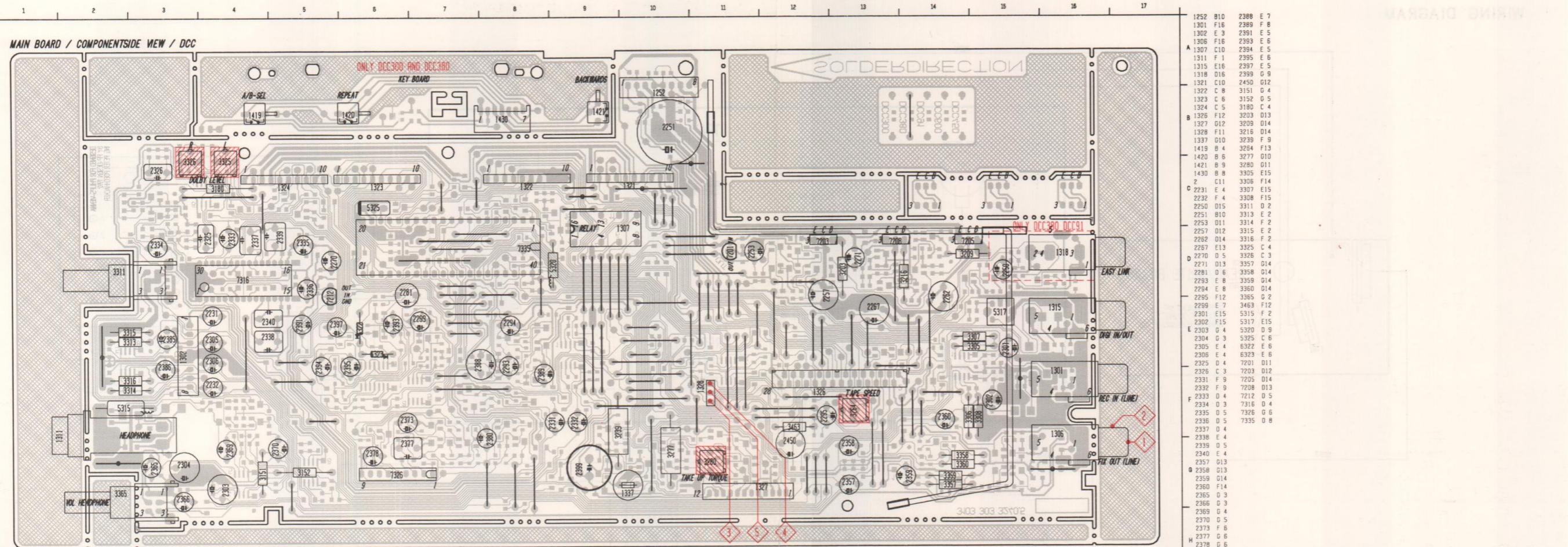
SERVICE TOOLS

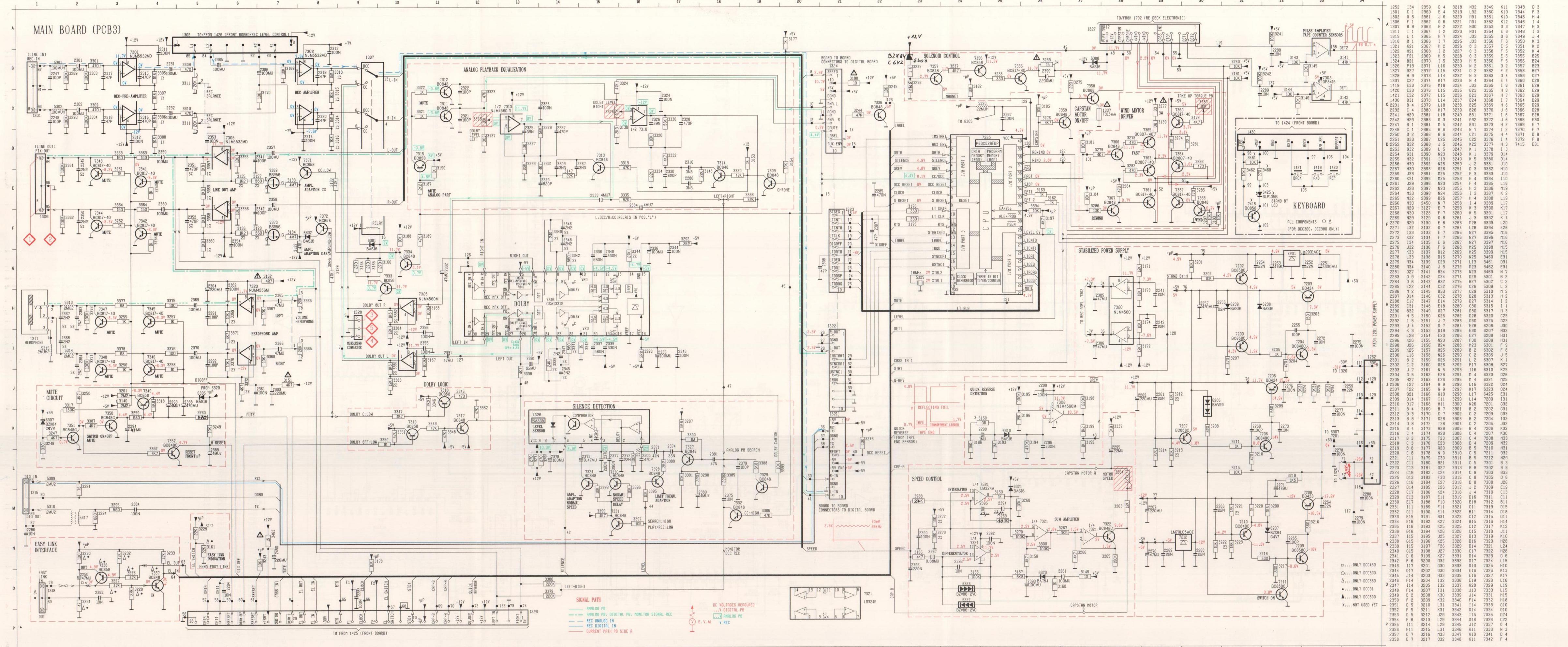
TORX screwdriver set "SBC163"	4822 395 50145
Universal analog test cassette CrO₂ "SBC419"	4822 397 30069
Universal analog test cassette Fe "SBC420"	4822 397 30071
9,6kHz calibration cassette (prerecorded DCC)	4822 397 30264
Audio performance test cassette (prerecorded DCC)	4822 397 30255
General purpose test cassette (prerecorded DCC)	4822 397 30256
Torque measurement cassette "811CTM"	4822 395 30054
Tape guide gauge "ABEX THG-801"	4822 310 50108
Head mounting support (see page 58)	4822 403 70846

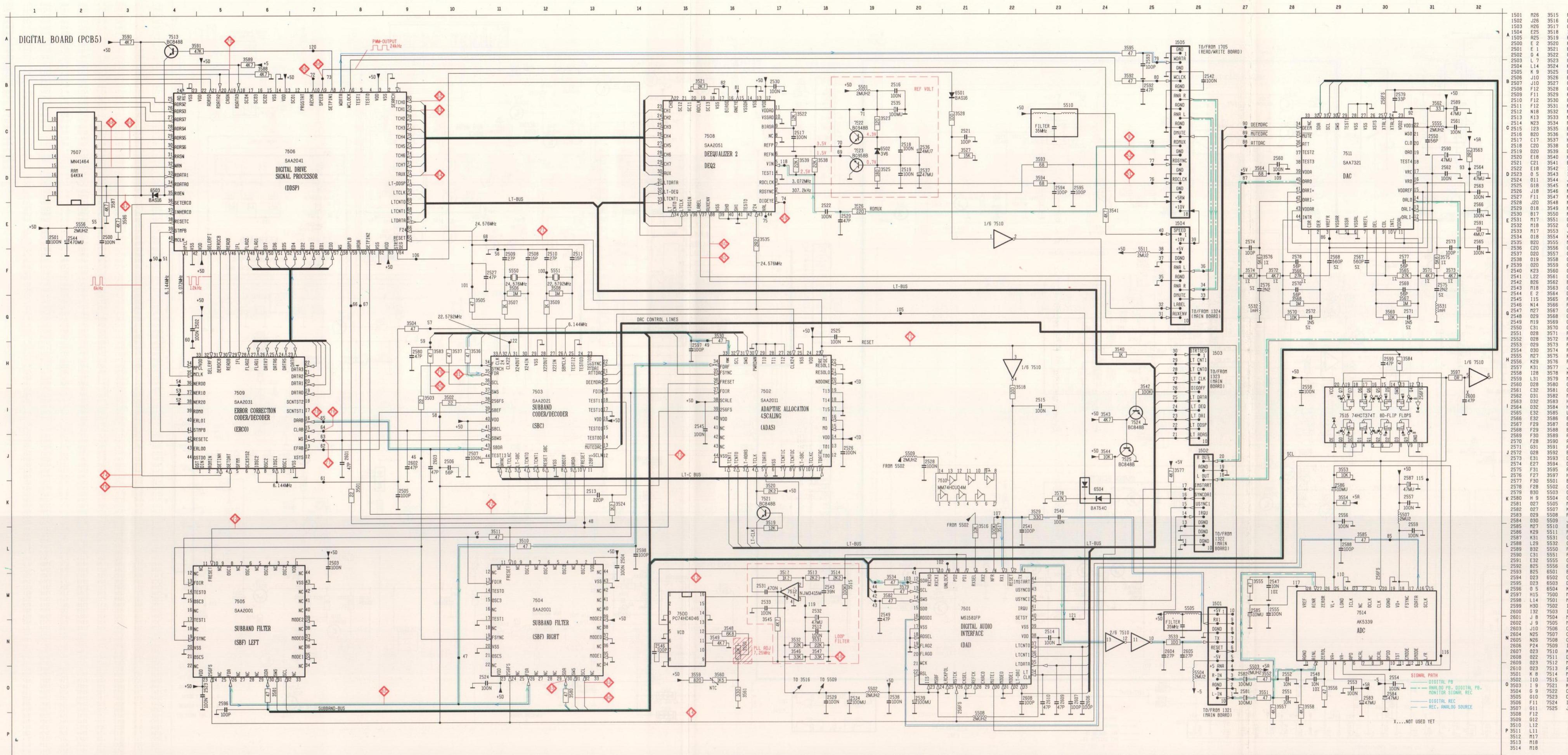
HANDLING CHIP COMPONENTS**GENERAL****DISMOUNTING****MOUNTING****EXAMPLES****PRECAUTIONS**



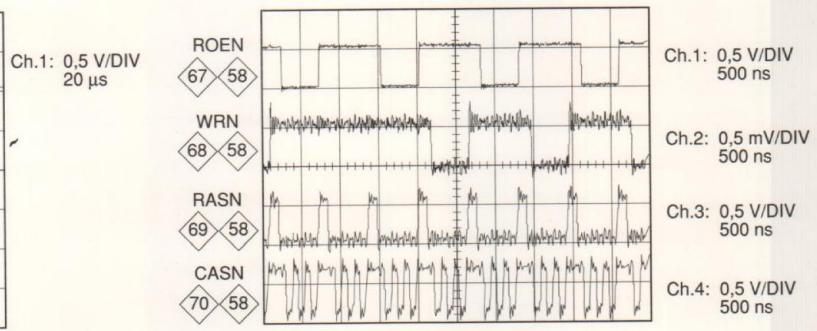
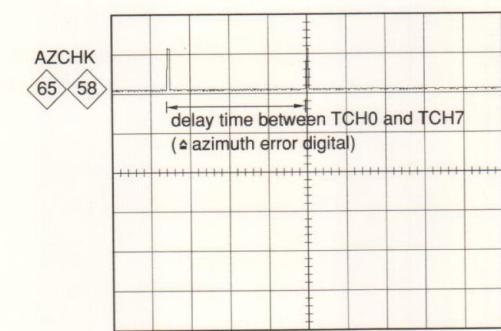
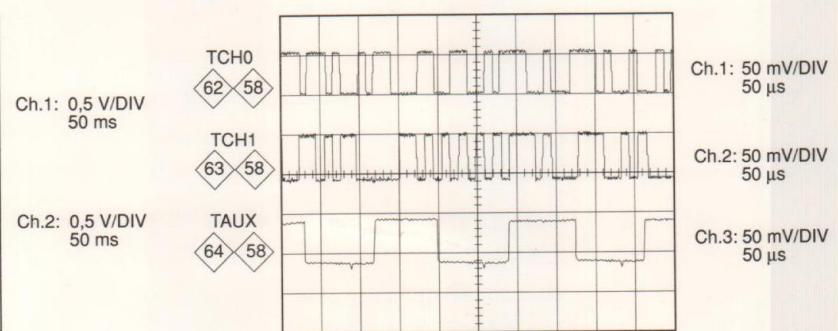
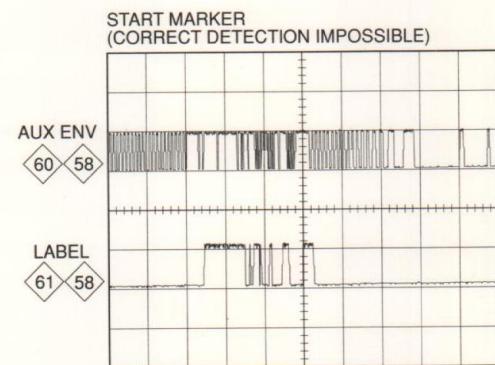
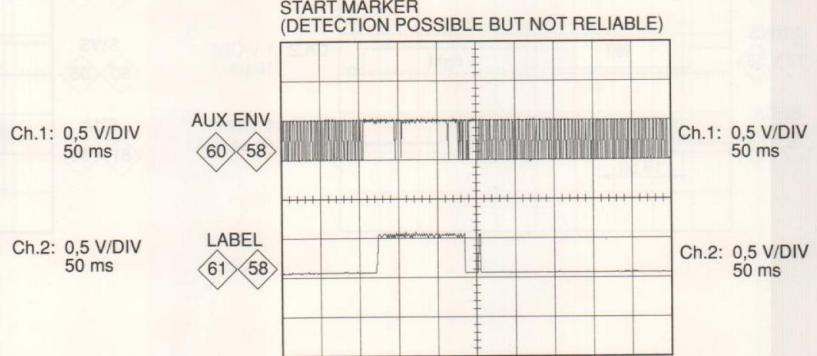
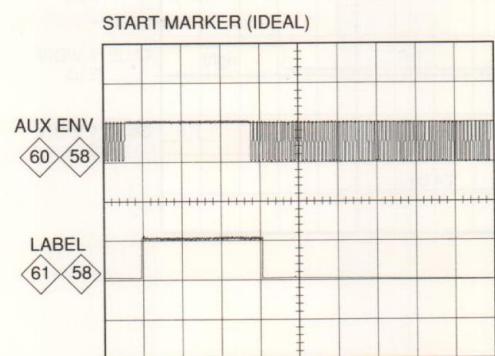
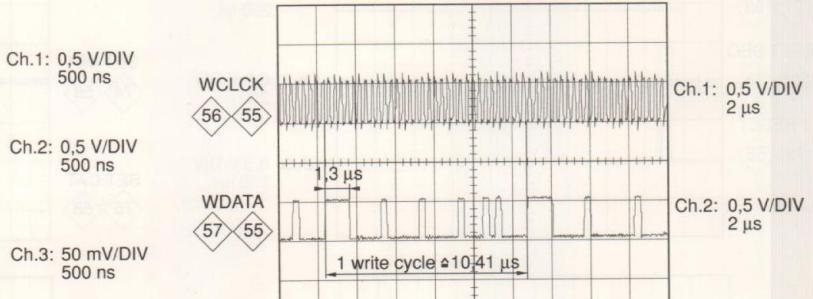
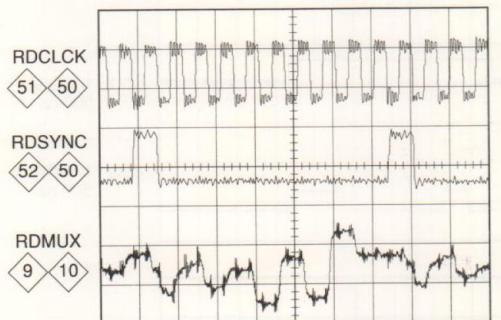
IF NOT OTHERWISE STATED ALL PCB'S SHOWN FROM THE COMPONENT-SIDE



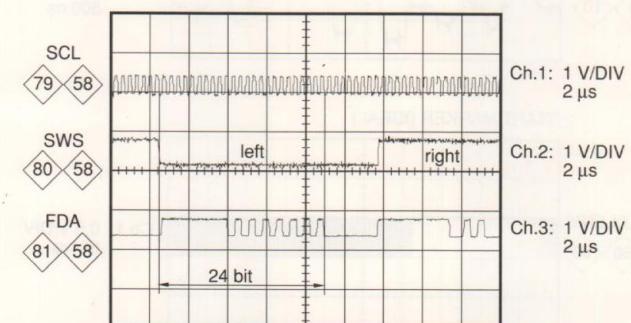
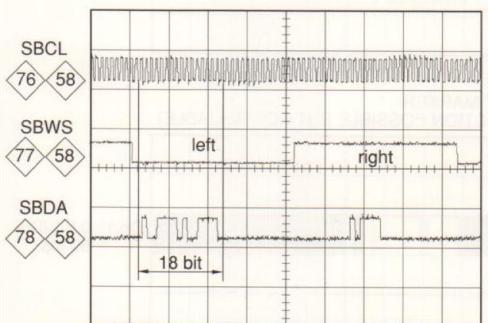
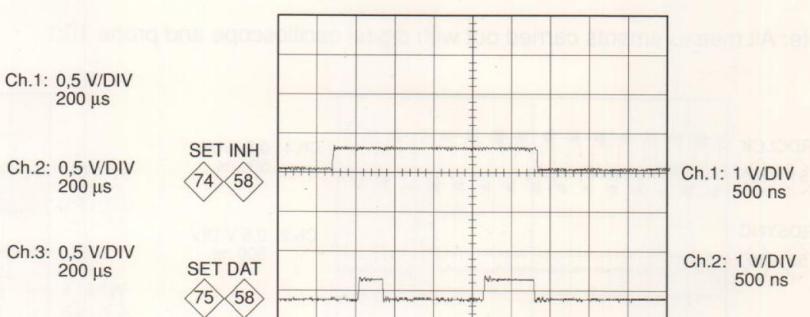
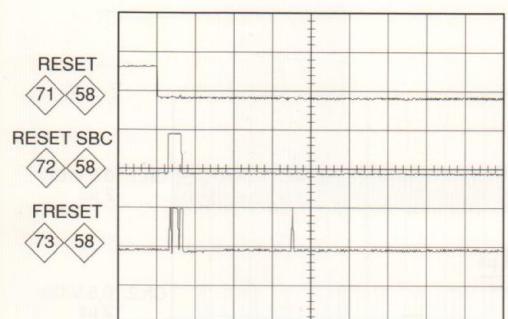




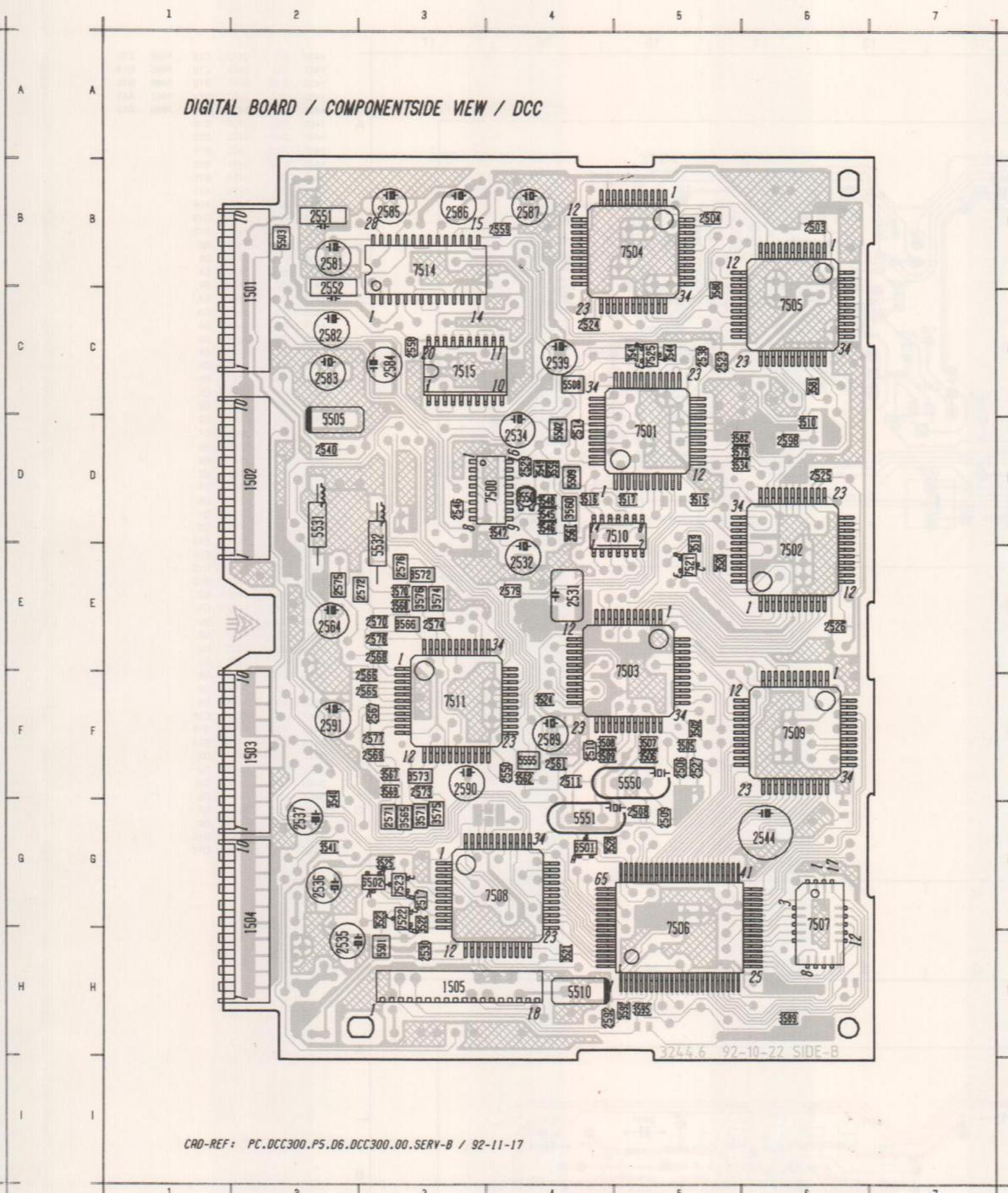
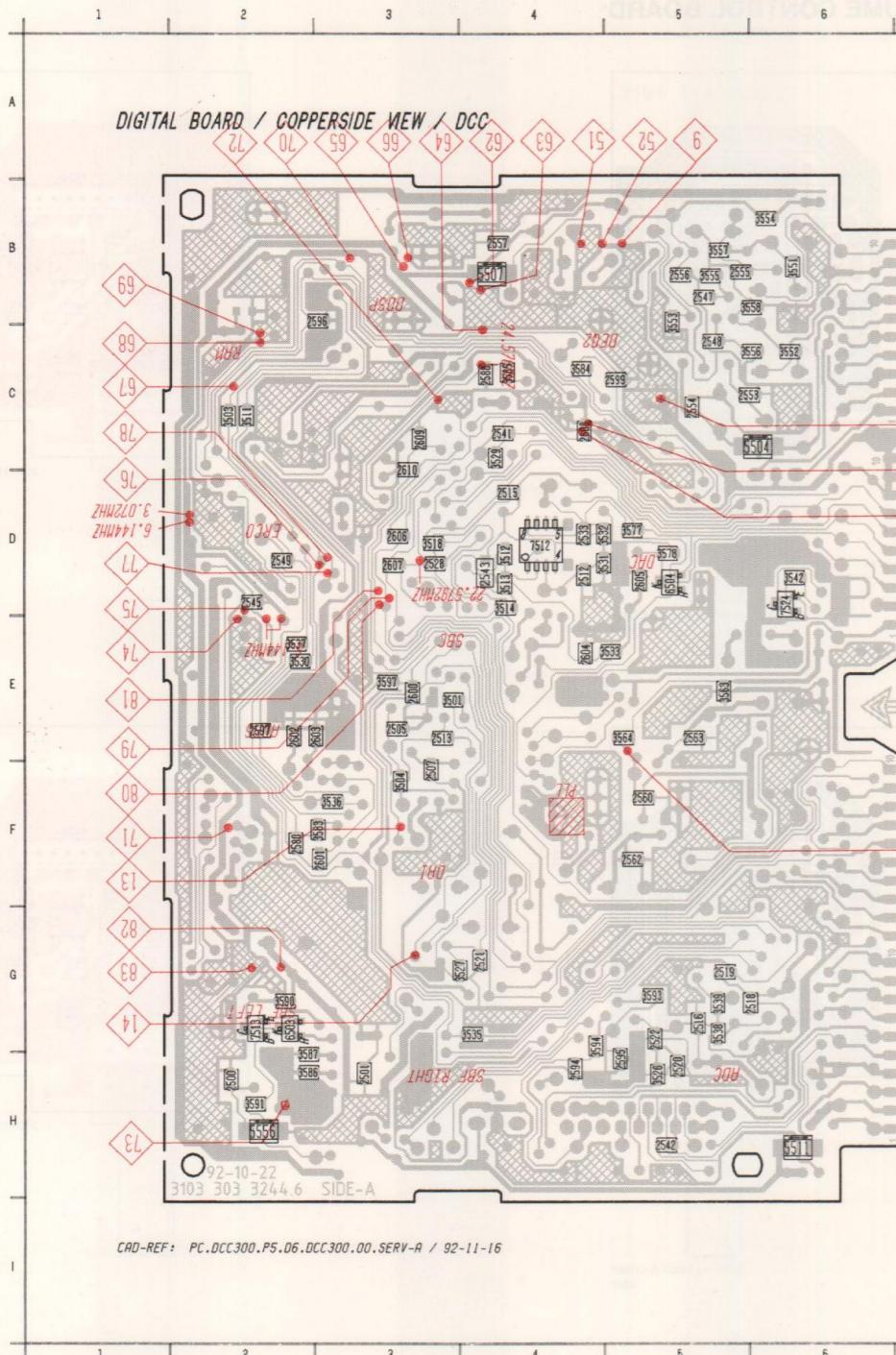
Note: All measurements carried out with digital oscilloscope and probe 10:1



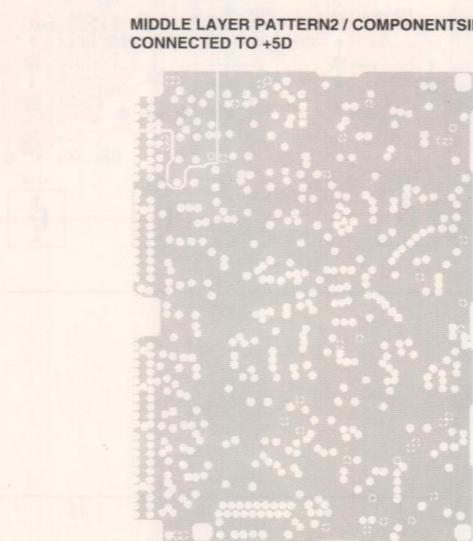
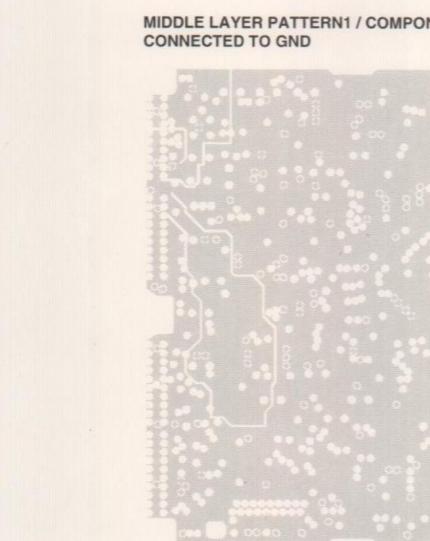
MEASUREMENTS ON DIGITAL BOARD PCB5 (CONTINUED)



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 2501 H 3 3563 E 5
 2505 E 3 3564 E 5
 2507 F 3 3577 D 5
 2512 D 4 3578 D 5
 2513 E 3 3583 F 3
 2515 D 4 3584 C 4
 2516 G 5 3585 C 4
 2518 G 6 3586 H 2
 2519 G 5 3587 H 2
 2520 H 5 3589 G 2
 2521 O 4 3591 H 2
 2522 O 5 3593 G 5
 2528 D 3 3594 G 4
 2533 D 4 3597 E 3
 2541 C 4 5504 C 6
 2542 H 5 5507 B 4
 2543 D 4 5511 H 6
 2545 D 2 5556 H 2
 2547 B 5 6503 G 2
 2548 C 5 6504 D 5
 2549 D 2 7512 D 4
 2553 C 6 7513 G 2
 2554 C 5 7524 D 6
 2555 B 5
 2557 B 4
 2560 F 5
 2562 F 5
 2563 E 5
 2580 F 2
 2588 C 4
 2594 H 4
 2595 H 5
 2598 B 3
 2597 E 2
 2599 C 5
 2600 E 3
 2601 F 3
 2602 E 2
 2603 E 3
 2604 E 4
 2605 D 5
 2606 D 3
 2607 D 3
 2608 C 4
 2609 C 3
 2610 C 3
 3501 E 3
 3503 C 2
 3504 F 3
 3511 C 2
 3512 D 4
 3513 D 4
 3514 D 4
 3518 D 3
 3526 H 5
 3527 G 4
 3529 C 4
 3530 E 2
 3531 D 4
 3532 D 4
 3533 E 5
 3535 G 4
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 3557 B 5

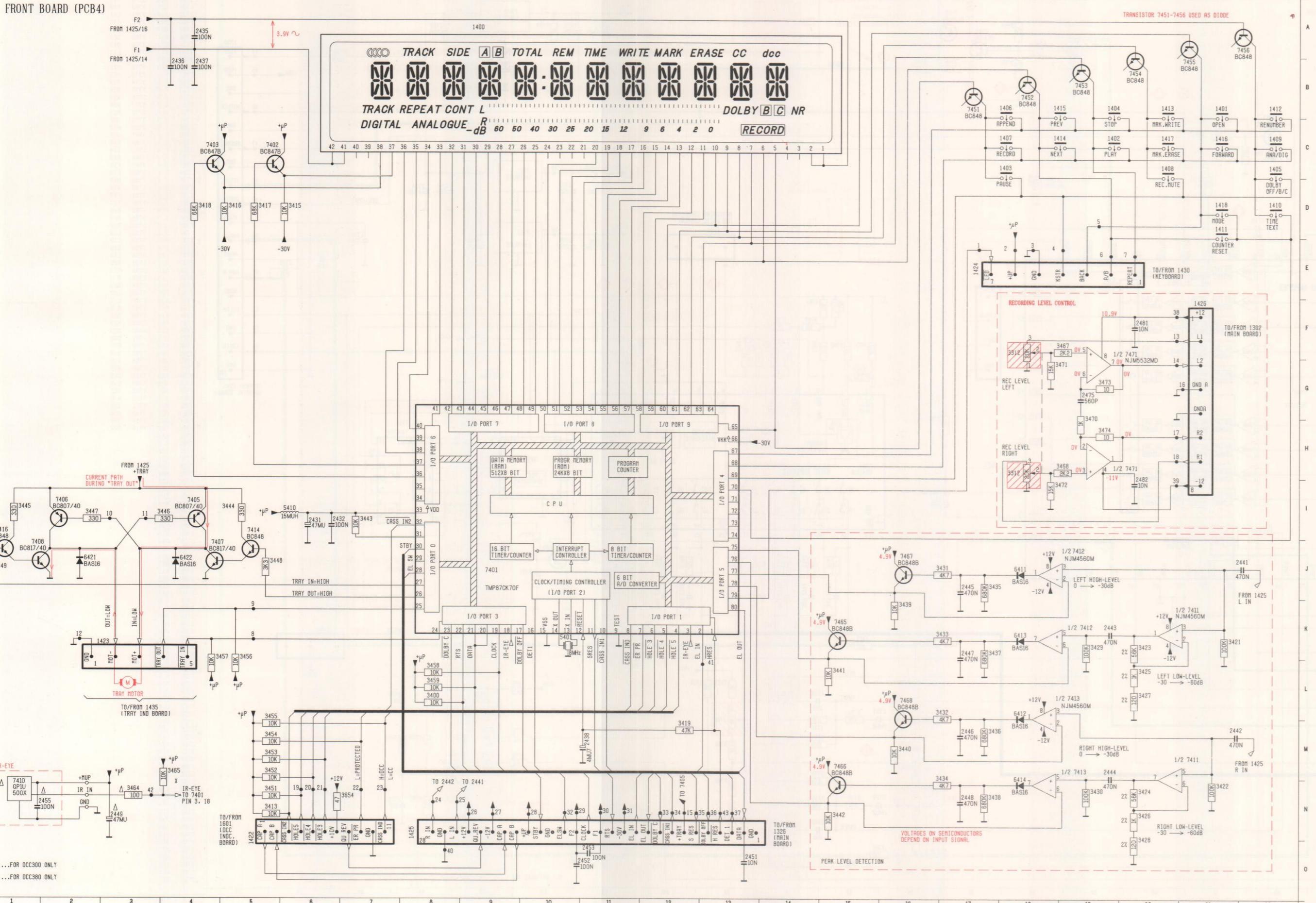


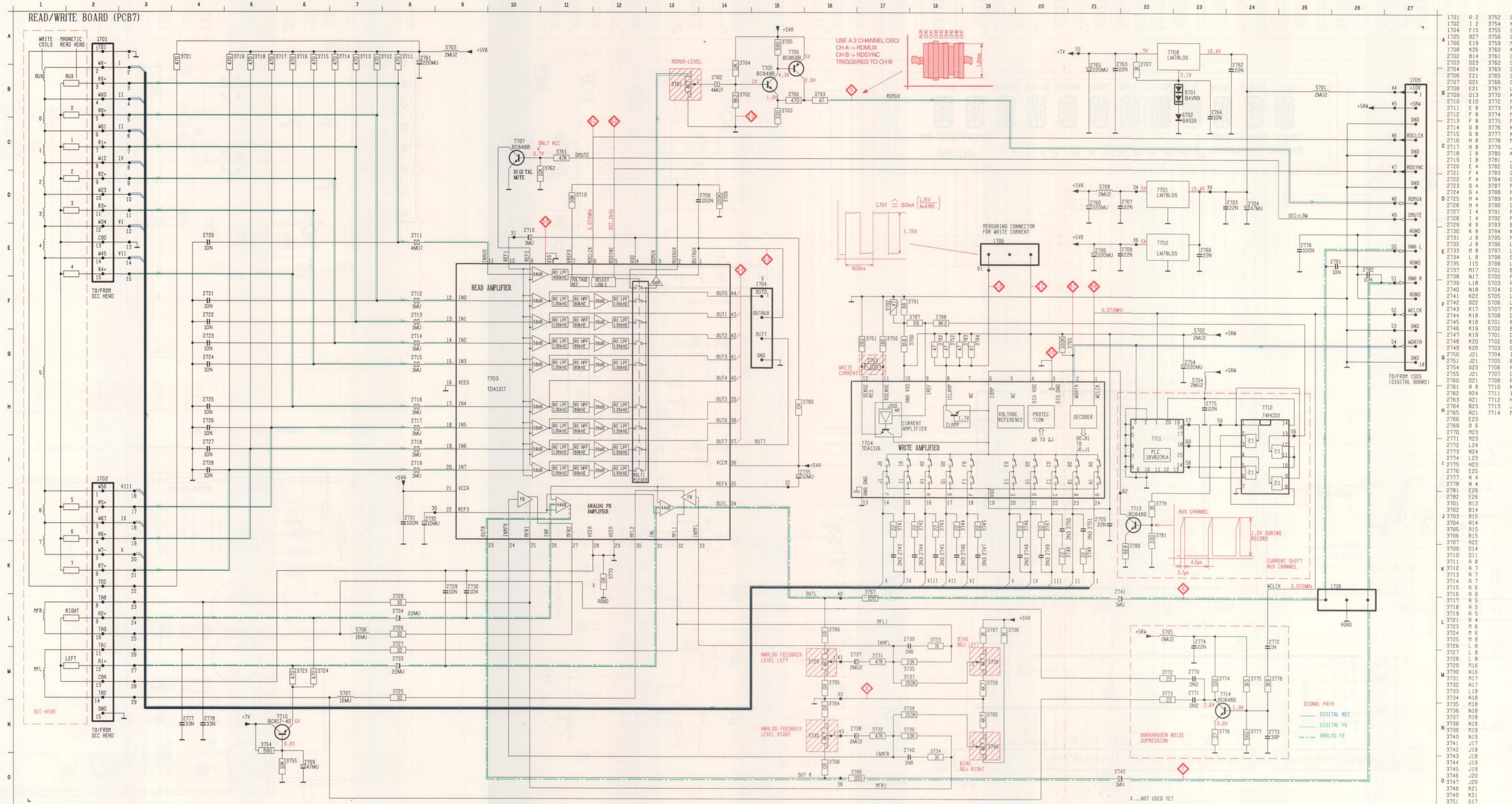
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 2506 F 5 3534 D 5
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 2509 G 5 3541 G 2
 2510 F 4 3543 C 5
 2511 F 4 3544 C 5
 2514 D 4 3545 D 4
 2517 G 3 3546 D 4
 2523 C 5 3547 D 4
 2541 C 4 3548 D 4
 2524 D 6 3549 D 4
 2526 E 6 3550 D 4
 2527 F 5 3559 D 4
 2528 B 5 3560 D 4
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 2531 E 4 3562 F 4
 2532 E 4 3563 G 3
 2533 D 4 3566 E 3
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 2536 G 2 3568 E 3
 2537 G 2 3569 F 3
 2538 C 5 3570 E 3
 2539 C 4 3571 G 3
 2540 D 2 3572 E 3
 2544 G 6 3573 F 3
 2544 D 3 3574 E 3
 2550 F 4 3575 G 3
 2551 B 2 3576 E 3
 2552 B 2 3579 D 5
 2554 C 3 3580 C 5
 2556 B 4 3581 C 6
 2561 F 4 3582 D 5
 2564 E 2 3589 H 6
 2565 F 3 3592 H 5
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 2579 E 4 3599 G 4
 2581 B 2 3599 D 3
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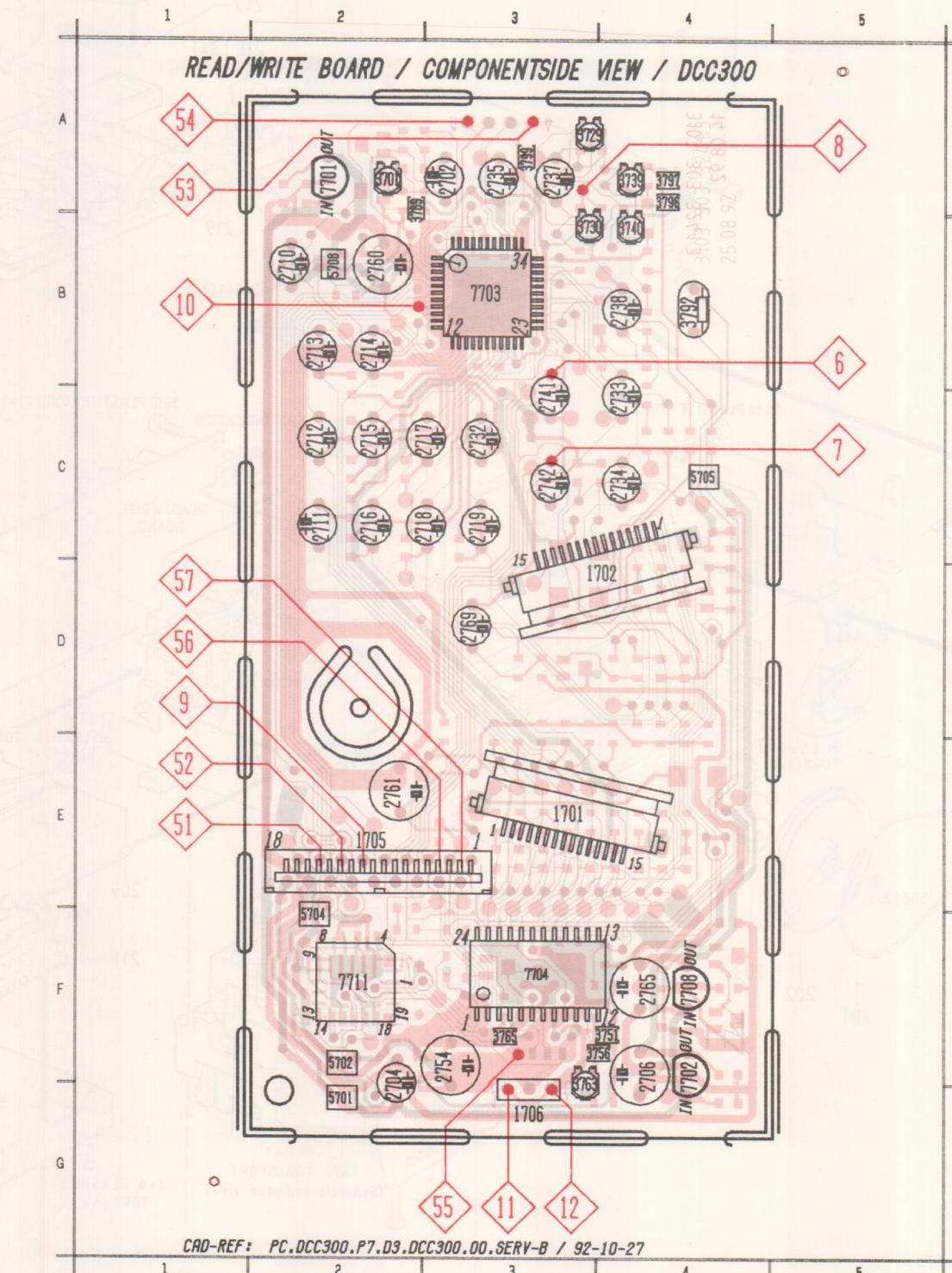
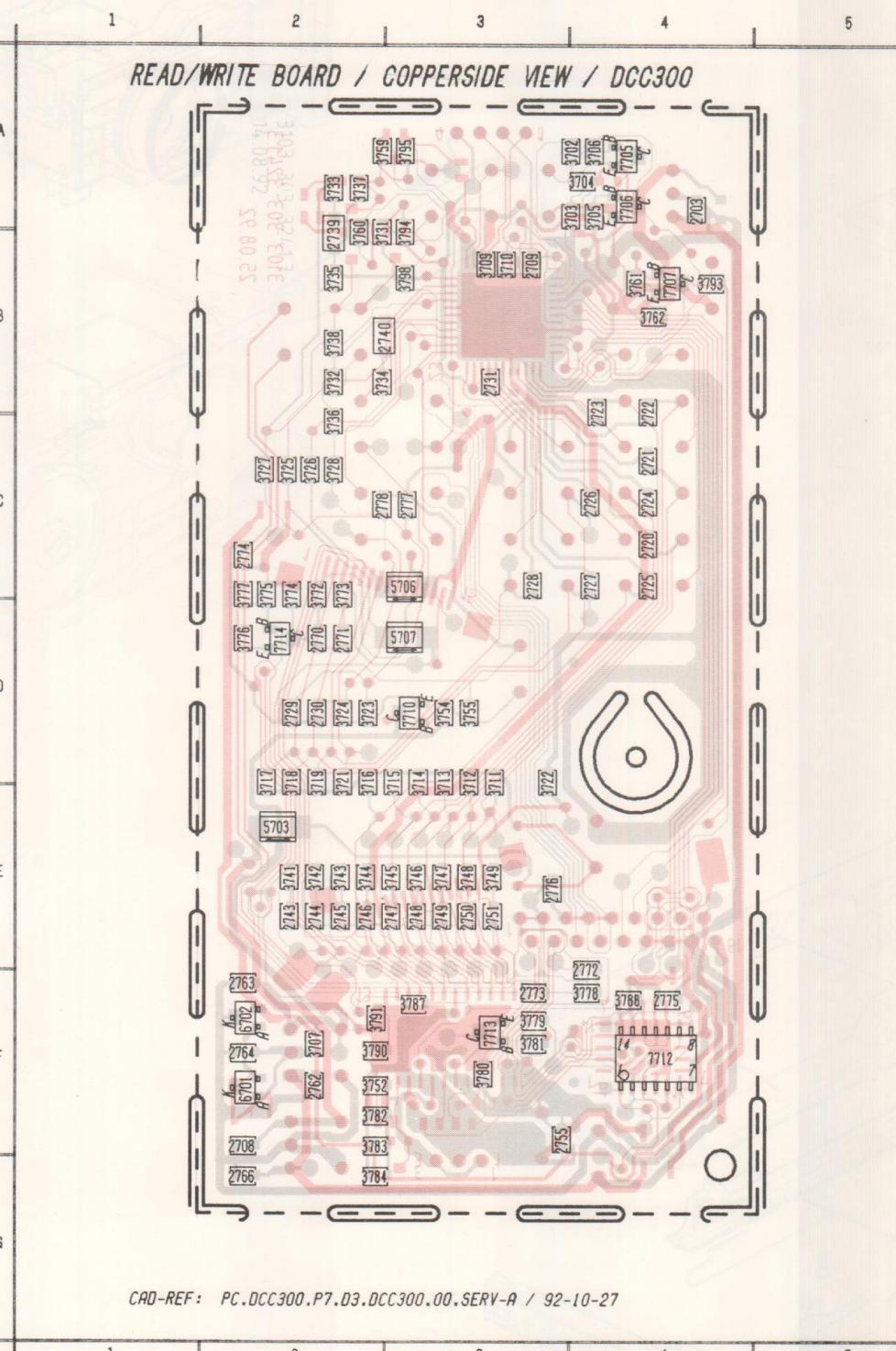
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 1401 B21 1406 B17 1411 C17 1416 C21 1424 E17 2435 M21 2447 K17 2453 011 3312 F18 3416 D 5 3422 N21 3427 L20 3432 J17 3437 K17 3442 N15 3447 L 8 3458 H19 3474 G19 6412 J18 7401 I 2 7413 L19 7453 B19 7466 M15
 1402 C19 1407 C17 1412 B22 1417 C20 1425 0 8 2436 B 4 3423 K19 2448 N17 2455 3312 F18 3417 D 5 3424 N20 3433 K17 3438 N17 3443 L 7 3448 L 8 3459 H19 3475 G19 6413 J18 7402 C 5 7408 J 1 7414 I 5 7454 B20 7467 J16
 1403 C17 1408 C20 1413 B20 1418 D21 1426 F21 2437 G19 3400 L 8 3418 D 4 3424 N3 3429 K19 3434 M17 3439 K16 3444 I 4 3449 J 1 3455 L 5 3454 N 3 3457 L 8 3458 H19 3476 G19 6414 J18 7403 C 5 7409 J 1 7416 I 1 7455 B21 7468 L16
 1404 B19 1409 C22 1414 C11 1422 D 5 2431 013 3419 3425 L20 3430 N19 3435 M12 3445 I 1 3451 N 5 3456 M 4 3465 I 6 3450 K 5 3465 J 2 7405 I 4 7411 K21 7451 B17 7456 R22 7471 F19

1025 A 6 1203 C 7 1206 R 5 2212 B 7 2216 B 3 3201 D 3 3204 E 4 5202 B 9 6202 C 4 6205 E 2
 1201 A 1 1204 B 9 1207 B 5 2213 E 4 3202 F 2 3205 D 4 5203 B 9 6203 D 4 7201 E 3
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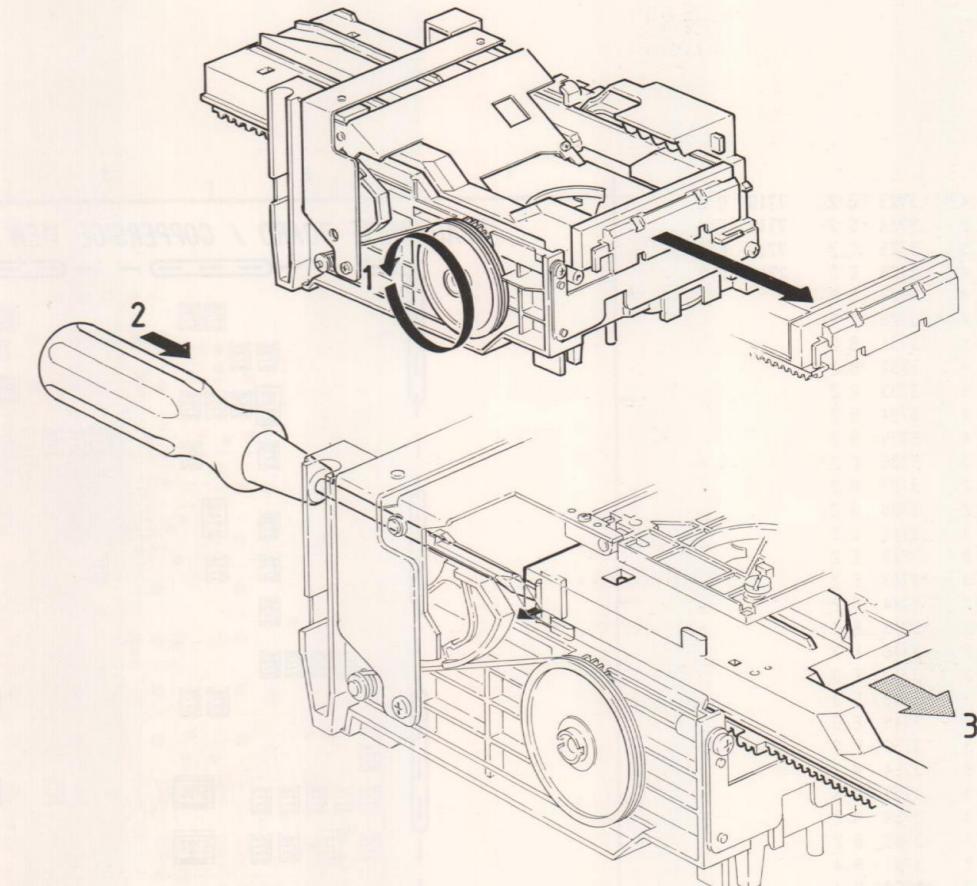
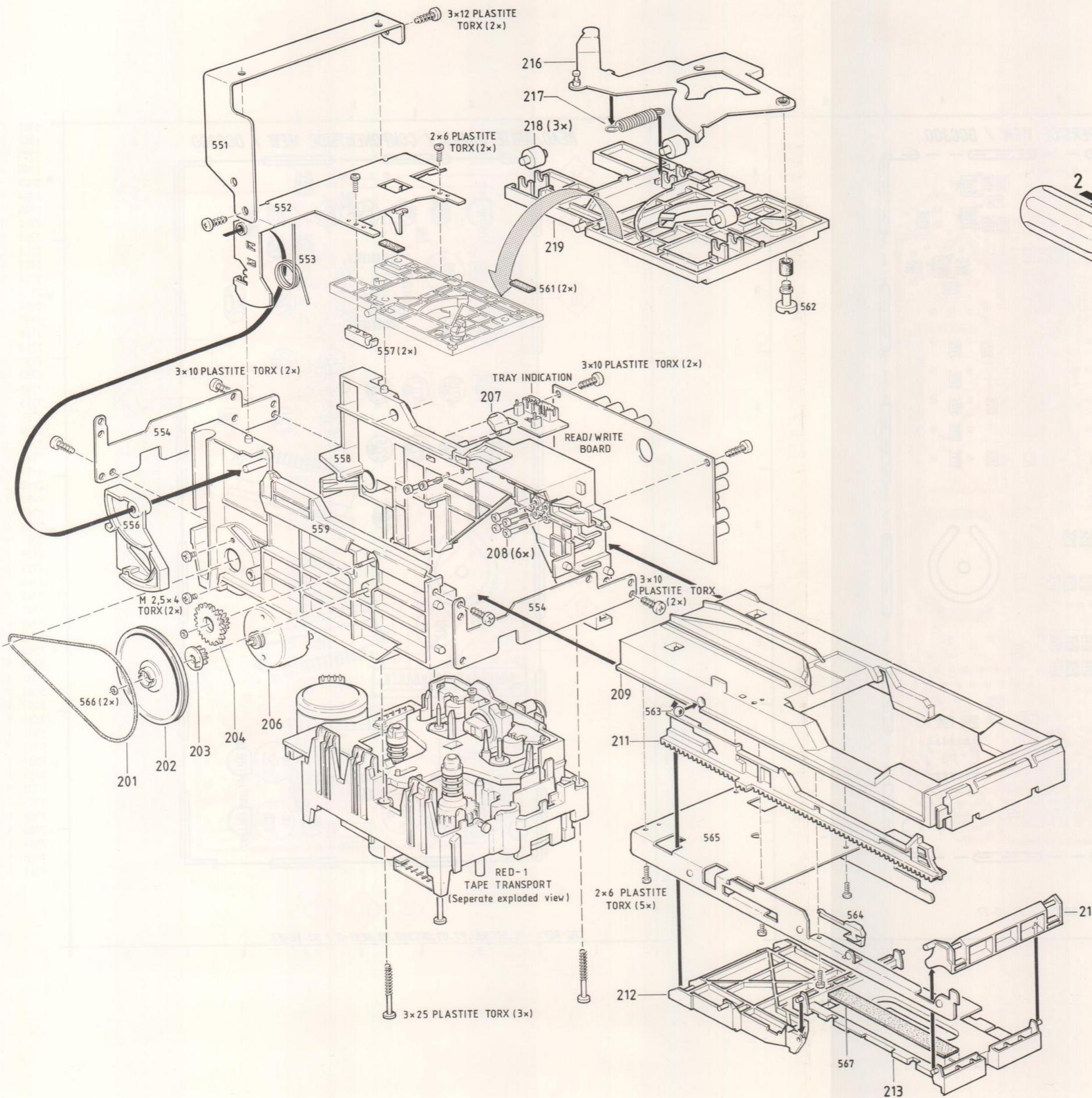


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3718	D	2	6702	F	2			
3719	D	2	7705	A	4			
3721	D	2	7706	A	4			
3722	D	3	7707	B	4			



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	1702	D	4
	1705	E	2
	1706	G	3
A	2702	A	3
	2704	F	2
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	2734	C	4
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	3792	B	4
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	3797	A	4
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	5705	C	4
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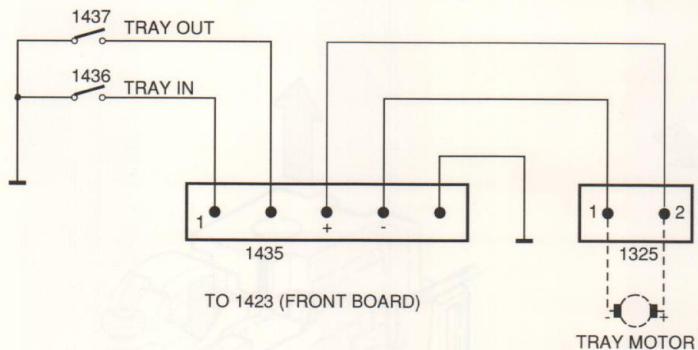
Exploded view Loading Assy



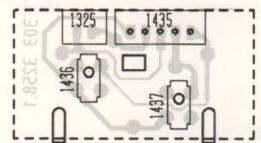
MECHANICAL PARTS LOADING ASSY

4822 691 20833 COMPLETE LOADING ASSY

201	4822 358 30223	DRIVING BELT
202	4822 528 81495	BELT-WHEEL
203	4822 522 33347	GEAR (PUSH TRAY)
204	4822 522 33346	GEAR
206	4822 361 21598	MOTOR + PULLEY
207	4822 402 50303	SWITCH LEVER
208	4822 528 70809	GUIDING WHEEL
209	4822 443 63838	DRAWER
211	4822 522 20453	TOOTH RACK
212	4822 403 70851	HINGE BACK
213	4822 466 93225	LIFTPLATE
214	4822 403 70849	HINGE FRONT
216	4822 402 30169	CATCH LEVER ASSY
217	4822 492 33386	SPRING CATCH LEVER
218	4822 528 90639	ROLLER
219	4822 466 93226	STABILIZER
562	4822 502 21281	SCREW (CATCH LEVER)
	4822 502 21282	SCREW 2 x 5 PLASTITE
	4822 502 13886	SCREW 2 x 6 PLASTITE
	4822 502 21283	SCREW M 2,5 x 4

TRAY INDICATION BOARD

TRAY INDICATION BOARD / DCC300
COMPONENTS SIDE VIEW



CAD-REF : PC.DCC300.P0.DI.DCC300.00.SERV-B
92-II-04

Partslist

- | | | |
|------|----------------|----------------------------------|
| 1436 | 4822 276 12889 | SWITCH 100mA / 30V _{DC} |
| 1437 | 4822 276 12889 | SWITCH 100mA / 30V _{DC} |

Dismantling DCC Head

MECHANICAL PARTS LOADING AREA

For dismantling / assembling the DCC head follow picture 1 to 4.

The DCC head is very sensitiv against ESD. Therefore it's **absoluteley necessary** to wear a wrist wrap.

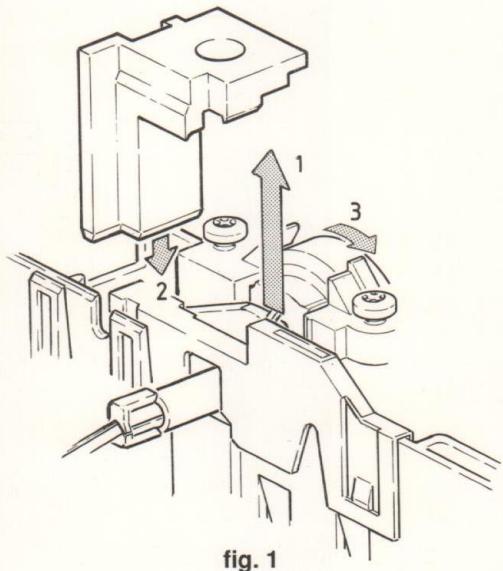


fig. 1

- 1.1 Remove bearing clamp (pos. 140)
- 1.2 Insert mounting support (4822 403 70846)
- 1.3 Turn DCC head to bring tooth segment lever (pos.65) in correct position. See fig. 2

- 2.1 Lift the DCC head
- 2.2 Press to remove the DCC head

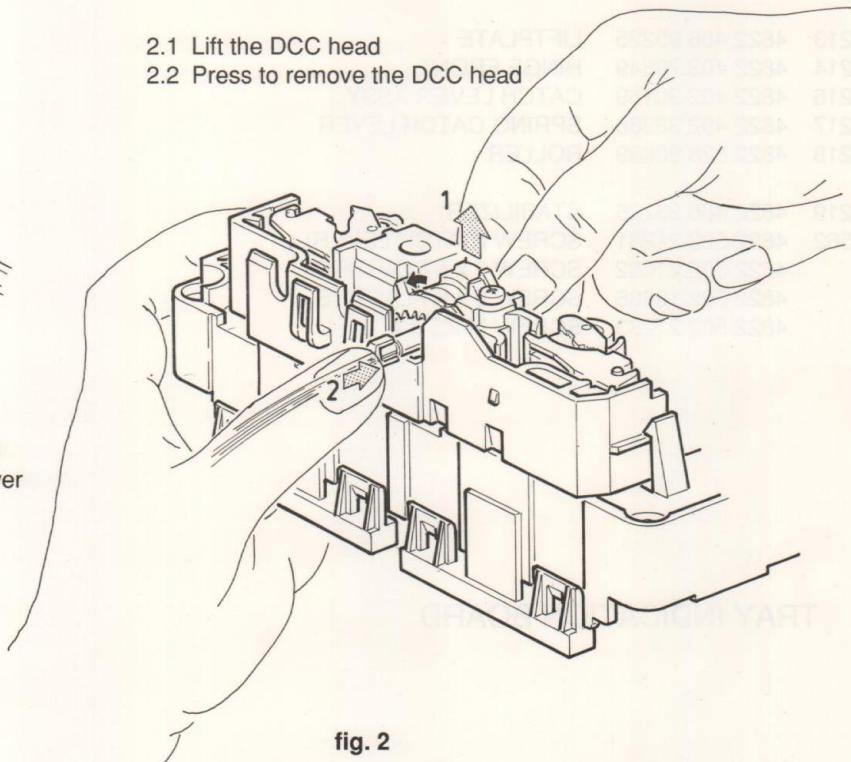


fig. 2

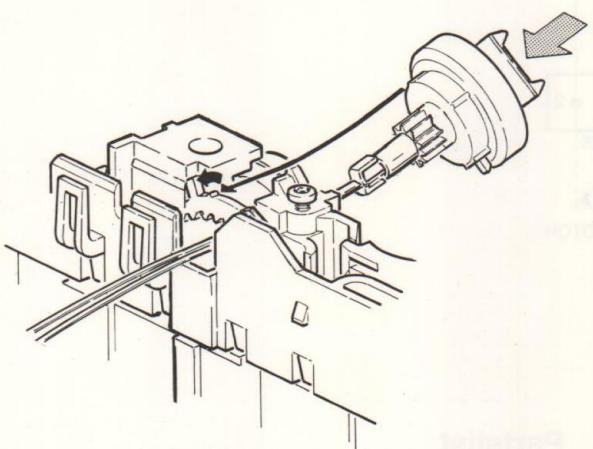


fig. 3

- 3.1 Check if the tooth segment lever (pos. 65) is in the correct position.
- 3.2 Insert DCC head so that ribs on the head align to the ribs of the tooth segment lever.

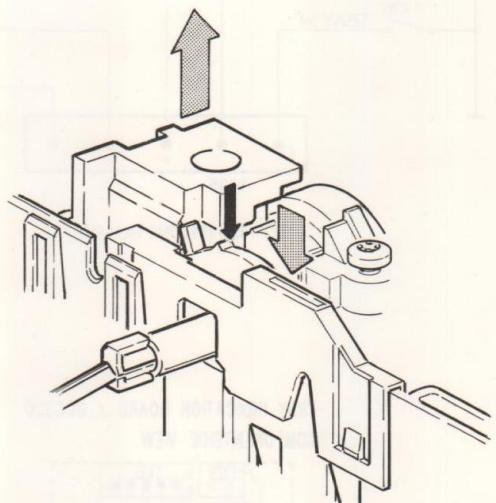
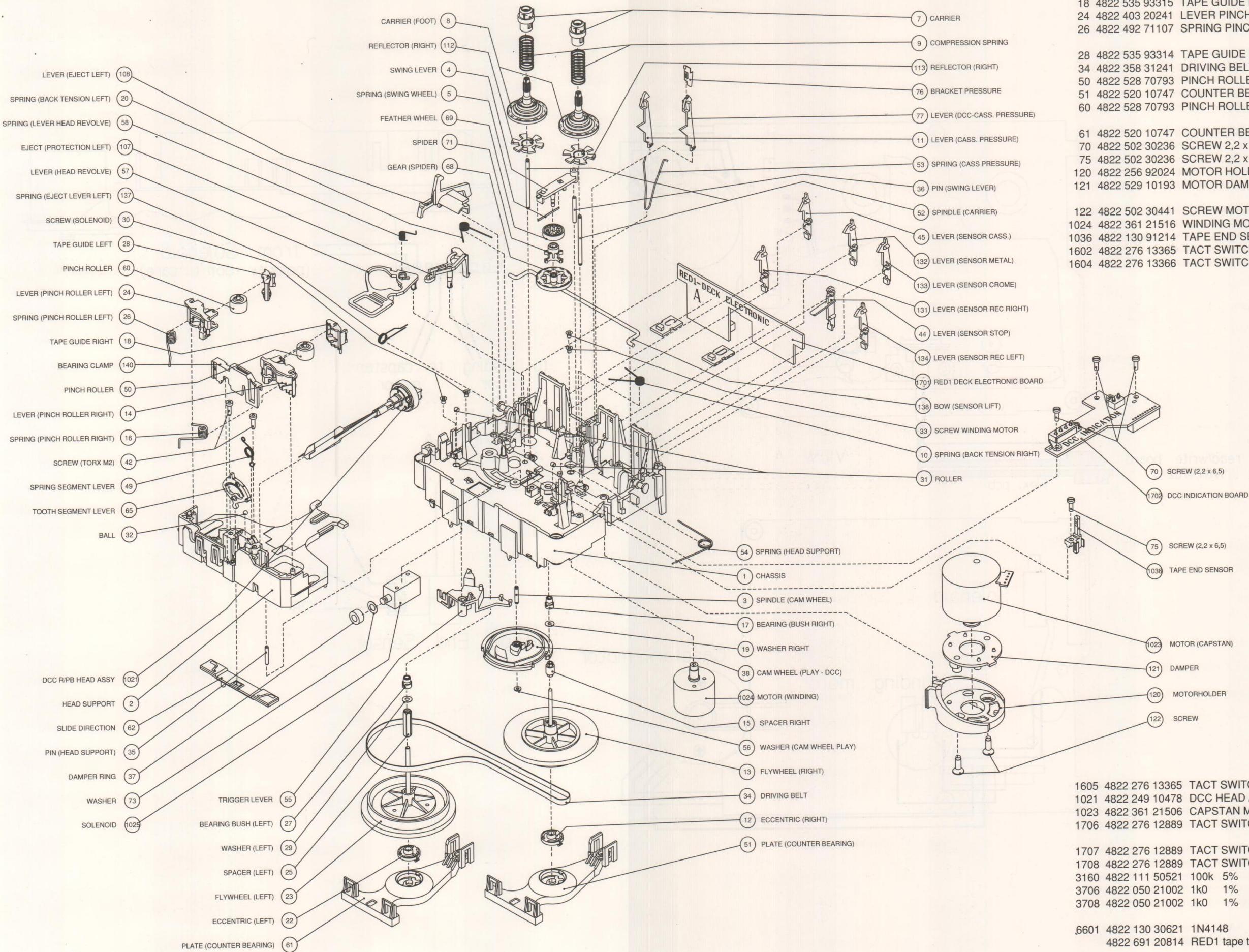
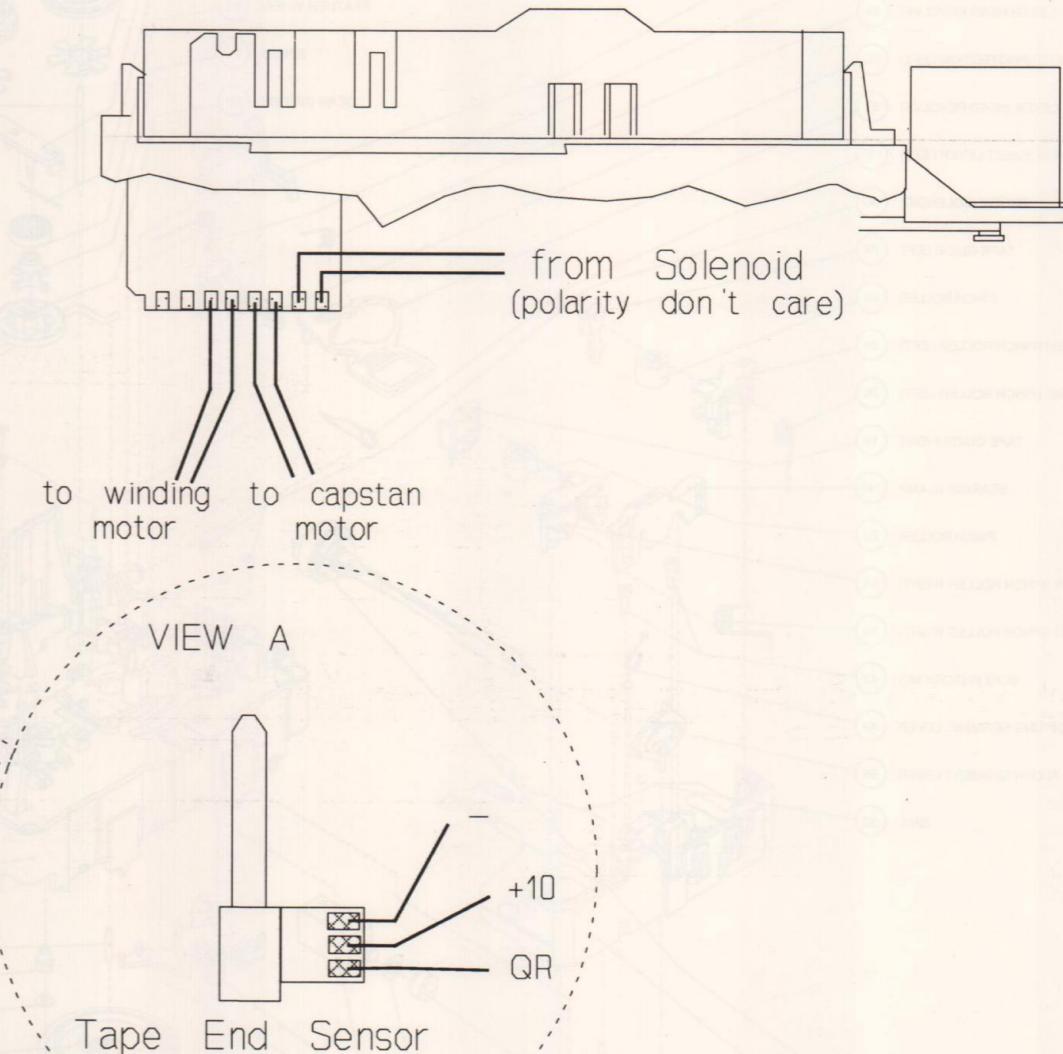
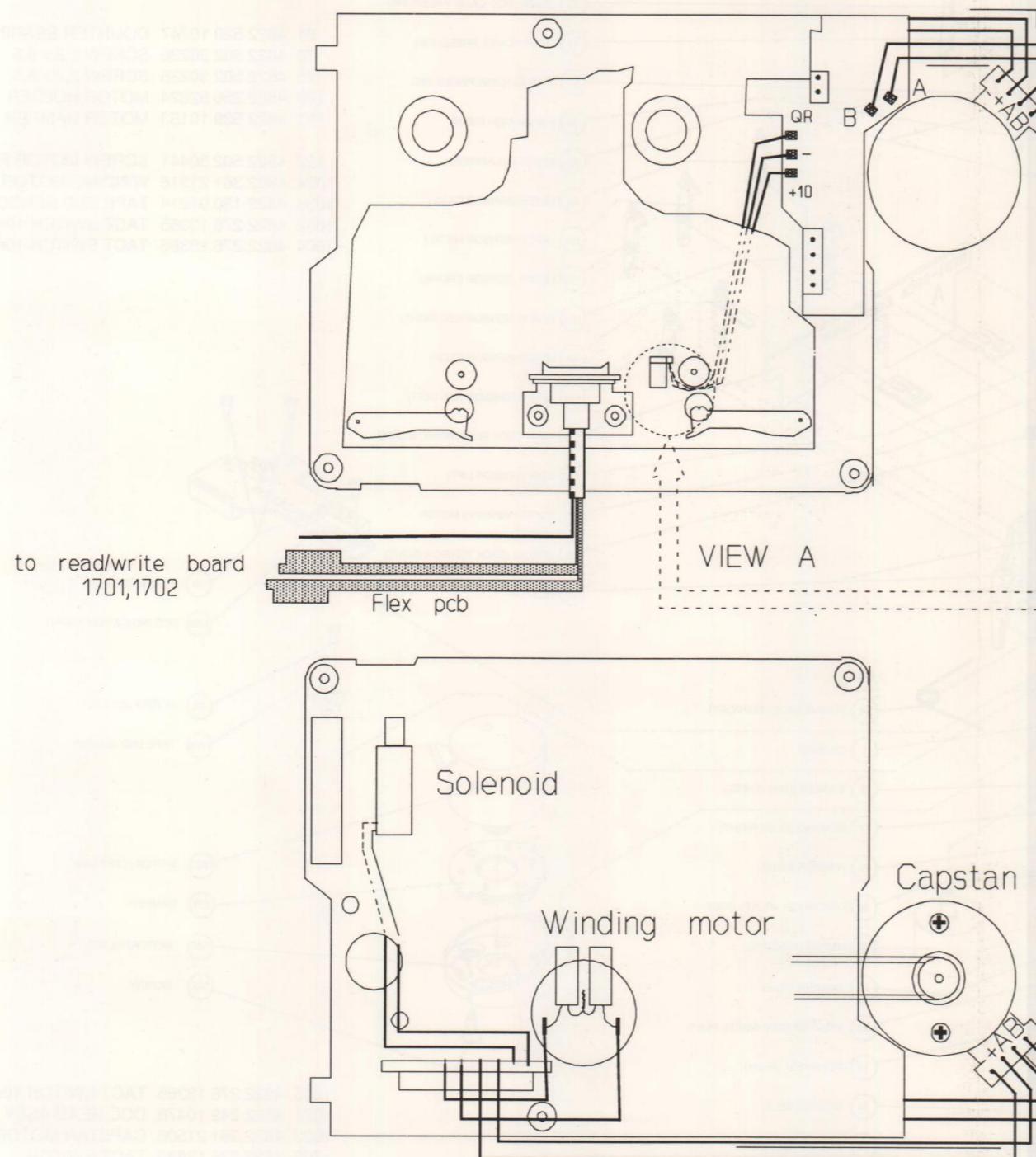


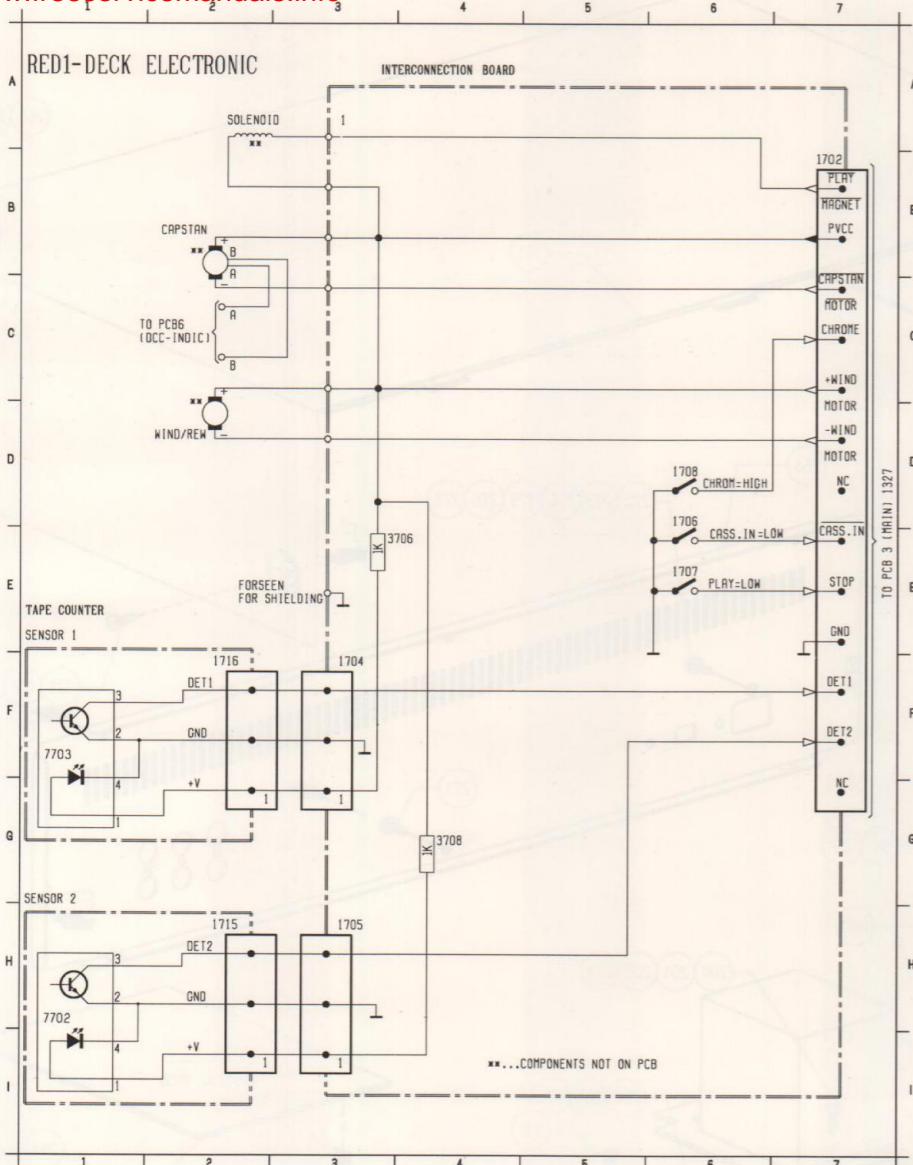
fig. 4

- 4.1 Insert bearing clamp
- 4.2 Remove mounting support

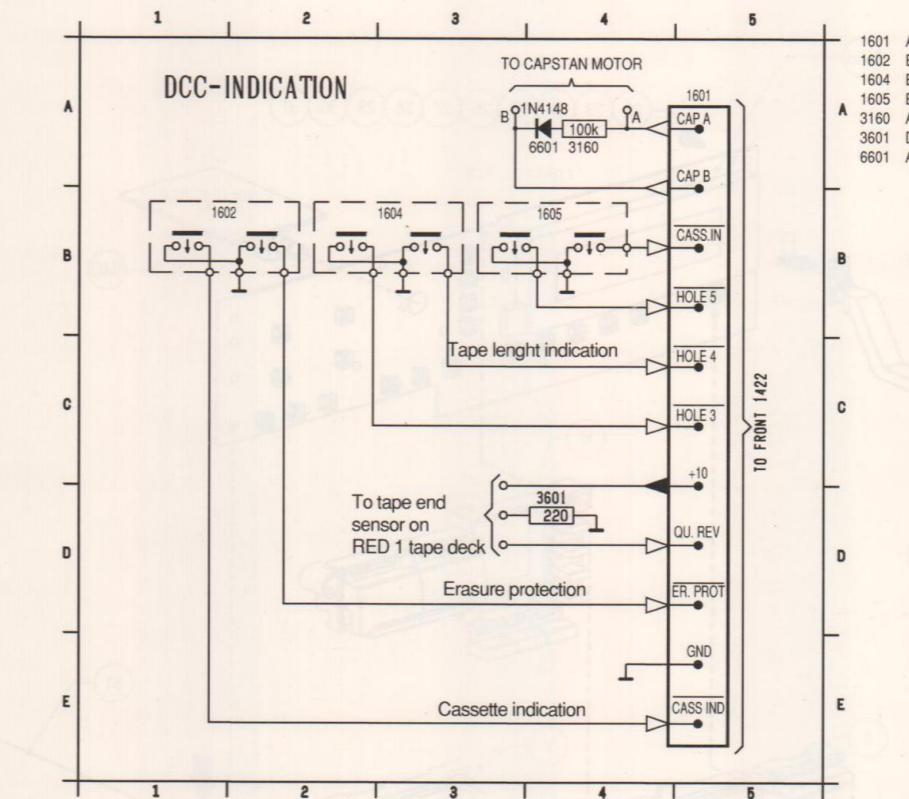
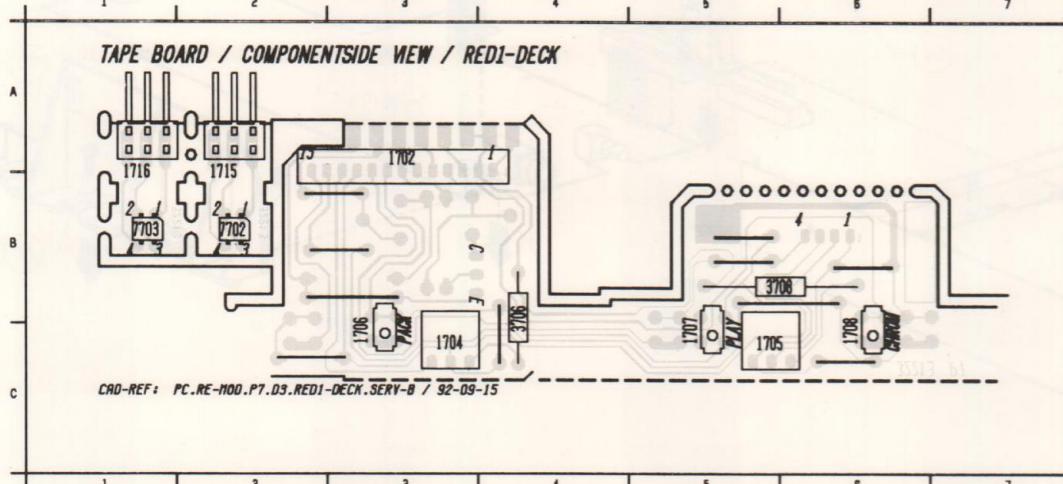
Exploded view RED Tape Transport







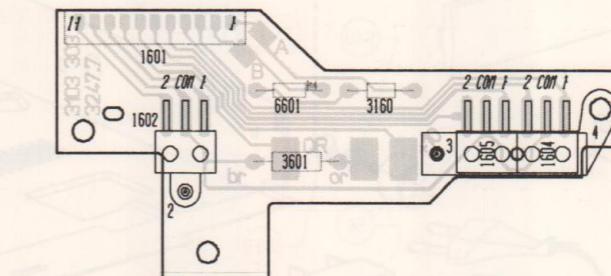
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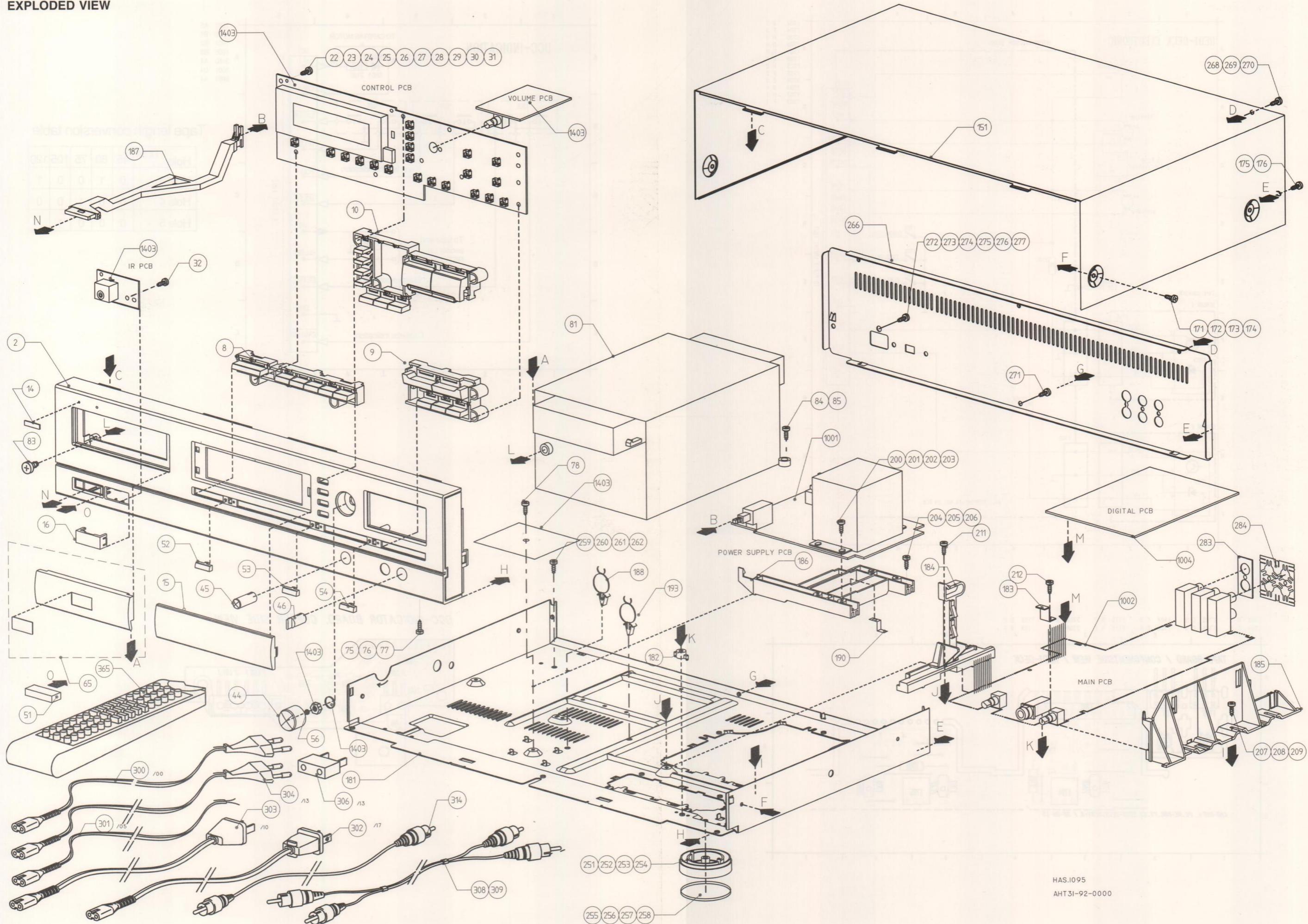
Tape length conversion table

Hole	Time	45	60	75	105	120
Hole 3		0	1	0	0	1
Hole 4		0	0	1	0	0
Hole 5		0	0	0	1	1

DCC-INDICATOR BOARD, COPPER SIDE VIEW



EXPLODED VIEW



HAS.I095
AHT3I-92-0000

MECHANICAL PARTSLIST

Partslist cabinet

2	4822 444 40633	FRONT ASSY
8	4822 410 62407	LEFT BUTTON ASSY
9	4822 410 62408	RIGHT BUTTON ASSY
10	4822 410 62409	BUTTON ASSY
14	4822 459 10887	WORDMARK
15	4822 450 62002	WINDOW
16	4822 450 62003	IR WINDOW
44	4822 413 41758	LEVEL KNOB
45	4822 413 41759	BALANCE KNOB
46	4822 413 41759	VOLUME KNOB
51	4822 462 71808	POWER CAP
52 - 54	4822 410 62406	FIXED BUTTON
56	4822 492 63086	FIXATION RING
65	4822 444 40634	TRAY FRONT
151	4822 444 60897	COVER
187	4822 535 93361	POWERROD
251 - 254	4822 462 41715	FOOT
255 - 258	4822 462 41479	FELT
272 - 277	4822 502 21287	P3 X 10
300	▲ 4822 321 10249	MAINS CORD only /00
301	▲ 4822 321 10719	MAINS CORD only /05
302	▲ 4822 321 10492	MAINS CORD only /06
302	▲ 4822 321 10788	MAINS CORD only /17
308	4822 321 22832	SBC1072
309	4822 321 22832	SBC1072
314	4822 321 61452	
365	4822 218 10492	RH6923/00

Not mentioned parts are only available during production period on special request.

Screws

Taptite	M3x6:	75, 76, 77, 78, 207, 208 209	Plastite	M3x10:	22, 23, 24, 25, 26, 27 28, 29, 30, 31, 32, 83 200, 201, 202, 203
Taptite	M3x10:	84, 85, 204, 205, 206 206, 211, 212	Plastite	M3,5x8:	259, 260, 261, 262
Taptite	M3x10 + pin + washer:	171, 172, 173, 174, 175 176, 268, 269, 270, 271			

ELECTRICAL PARTS LIST

MAIN PANEL			
MISCELLANEOUS			
7 ▲	4822 255 40128	CLIP	2285 4822 122 33325 470nF +80-20% 16V
8 ▲	4822 255 40128	CLIP	2286 4822 122 33177 10nF 20% 50V
9 ▲	4822 255 40128	CLIP	2287 4822 122 33891 3,3nF 10% 63V
1307	4822 280 80762	MINI RELAY	2288 4822 122 33891 3,3nF 10% 63V
1311 ▲	4822 267 31453	SOCKET	2289 4822 122 33177 10nF 20% 50V
1337 ▲	4822 071 53151	FUSE T315mA	2290 4822 122 33177 10nF 20% 50V
1337 ▲	4822 253 10146	FUSE T500mA only /17	2291 5322 122 32531 100pF 5% 50V
			2292 5322 122 32531 100pF 5% 50V
			2293 4822 124 40246 4,7μF 20% 63V
			2294 4822 124 41584 100μF 20% 10V
			2295 5322 124 41431 22μF 20% 35V
			2296 4822 126 12102 330nF +80-20% 50V
			2298 4822 126 10002 100nF 20% 25V
			2299 4822 124 40242 1μF 20% 63V
			2300 5322 122 32268 470pF 10% 50V
			2301 4822 124 41584 100μF 20% 10V
			2302 4822 124 41584 100μF 20% 10V
			2303 4822 124 22263 220μF 20% 25V
			2304 4822 124 22263 220μF 20% 25V
			2305 4822 124 40433 47μF 20% 25V
			2306 4822 124 40433 47μF 20% 25V
			2307 5322 122 32452 47pF 5% 63V
			2308 5322 122 32452 47pF 5% 63V
			2309 4822 122 33891 3,3nF 10% 63V
			2310 4822 122 33891 3,3nF 10% 63V
			2311 4822 126 10002 100nF 20% 25V
			2312 4822 126 10002 100nF 20% 25V
			2313 4822 126 10002 100nF 20% 25V
			2314 4822 126 10002 100nF 20% 25V
			2315 5322 122 32268 470pF 10% 50V
			2316 5322 122 32268 470pF 10% 50V
			2317 5322 122 32452 47pF 5% 63V
			2318 5322 122 32452 47pF 5% 63V
			2319 5322 122 32452 47pF 5% 63V
			2320 5322 122 32452 47pF 5% 63V
			2321 5322 122 32531 100pF 5% 50V
			2322 5322 122 32531 100pF 5% 50V
			2323 4822 126 10002 100nF 20% 25V
			2324 4822 126 10002 100nF 20% 25V
			2325 4822 121 51256 39nF 10% 50V
			2326 4822 121 70266 39nF 10% 50V
			2327 5322 122 32268 470pF 10% 50V
			2328 5322 122 32268 470pF 10% 50V
			2329 4822 122 33806 820pF 10% 63V
			2330 4822 122 33806 820pF 10% 63V
			2331 4822 124 40433 47μF 20% 25V
			2332 4822 124 40433 47μF 20% 25V
			2333 4822 124 40246 4,7μF 20% 63V
			2334 4822 124 40246 4,7μF 20% 63V
			2335 4822 124 41643 100μF 20% 16V
			2336 4822 124 41643 100μF 20% 16V
			2337 4822 121 51412 560nF 5% 63V
			2338 4822 121 51412 560nF 5% 63V
			2339 5322 121 42661 330nF 5% 63V

2340	5322 121 42661	330nF 5% 63V	2395	4822 124 40246	4,7μF 20% 63V
2341	5322 122 32531	100pF 5% 50V	2396	4822 122 32927	220nF +80-20% 50V
2342	5322 122 32531	100pF 5% 50V	2397	4822 124 41583	0,68μF 20% 25V BIP
2343	4822 126 10002	100nF 20% 25V	2398	4822 122 33177	10nF 20% 50V
2344	4822 126 10002	100nF 20% 25V	2399	4822 124 80409	2200μF 20% 16V
2345	4822 122 32999	2,2nF 5% NPO	2450	4822 124 22263	220μF 20% 25V
2346	4822 122 32999	2,2nF 5% NPO			
2347	4822 122 32999	2,2nF 5% NPO			
2348	4822 122 32999	2,2nF 5% NPO			
2349	4822 122 32999	2,2nF 5% NPO			
2350	4822 122 32999	2,2nF 5% NPO			
2351	5322 122 32268	470pF 10% 50V			
2352	5322 122 32268	470pF 10% 50V			
2353	4822 126 10002	100nF 20% 25V			
2354	4822 126 10002	100nF 20% 25V			
2355	4822 126 10002	100nF 20% 25V			
2356	4822 126 10002	100nF 20% 25V			
2357	4822 124 41584	100μF 20% 10V			
2358	4822 124 41584	100μF 20% 10V			
2359	4822 124 41643	100μF 20% 16V			
2360	4822 124 41643	100μF 20% 16V			
2361	4822 126 10002	100nF 20% 25V			
2362	4822 126 10002	100nF 20% 25V			
2363	4822 122 32999	2,2nF 5% NPO			
2364	4822 122 32999	2,2nF 5% NPO			
2365	4822 124 40433	47μF 20% 25V			
2366	4822 124 40433	47μF 20% 25V			
2367	4822 122 32999	2,2nF 5% NPO			
2368	4822 122 32999	2,2nF 5% NPO			
2369	4822 124 41643	100μF 20% 16V			
2370	4822 124 41643	100μF 20% 16V			
2371	4822 122 33797	47nF 20% 50V			
2372	4822 122 33809	22nF 20% 50V			
2373	4822 124 40239	0,47μF 20% 63V			
2374	4822 122 33342	33nF 10% 63V			
2375	4822 122 33177	10nF 20% 50V			
2376	4822 122 32927	220nF +80-20% 50V			
2377	5322 121 42502	390nF 5% 63V			
2378	4822 124 41643	100μF 20% 16V			
2379	5322 122 32531	100pF 5% 50V			
2380	4822 124 40246	4,7μF 20% 63V			
2381	4822 122 33797	47nF 20% 50V			
2383	5322 122 32531	100pF 5% 50V			
2384	4822 126 10002	100nF 20% 25V			
2385	4822 124 41584	100μF 20% 10V			
2386	4822 124 41584	100μF 20% 10V			
2387	4822 126 10002	100nF 20% 25V			
2388	4822 124 41997	470μF 20% 10V			
2389	4822 124 40246	4,7μF 20% 63V			
2390	4822 126 10002	100nF 20% 25V			
2391	5322 124 41431	22μF 20% 35V			
2392	4822 126 10002	100nF 20% 25V			
2393 ▲	4822 124 41584	100μF 20% 10V			
2394	4822 124 40246	4,7μF 20% 63V			

3175	4822 051 20331	330Ω 5% 0,1W	3236	4822 051 20472	4k7 5% 0,1W
3176	4822 051 20331	330Ω 5% 0,1W	3237	4822 051 20472	4k7 5% 0,1W
3177	4822 051 10102	1k 2% 0,25W	3238	4822 051 20331	330Ω 5% 0,1W
3178	4822 051 20103	10k 5% 0,1W	3239 ▲	4822 053 12159	15Ω 5% 3W
3179	4822 051 20472	4k7 5% 0,1W	3240	4822 051 20103	10k 5% 0,1W
3180 ▲	4822 052 10109	10Ω 5% 0,33W	3241	4822 051 20104	100k 5% 0,1W
3181	4822 051 20103	10k 5% 0,1W	3242	4822 051 20104	100k 5% 0,1W
3182	4822 051 20103	10k 5% 0,1W	3243	4822 051 20103	10k 5% 0,1W
3183	4822 051 20103	10k 5% 0,1W	3244	4822 051 20473	47k 5% 0,1W
3184	4822 051 20103	10k 5% 0,1W	3245	4822 051 20103	10k 5% 0,1W
3185	4822 051 20103	10k 5% 0,1W	3246	4822 051 20103	10k 5% 0,1W
3186	4822 051 20153	15k 5% 0,1W	3247	4822 051 20472	4k7 5% 0,1W
3187	4822 051 20222	2k2 5% 0,1W	3248	4822 051 20331	330Ω 5% 0,1W
3188	4822 051 20103	10k 5% 0,1W	3249	4822 051 20103	10k 5% 0,1W
3189	4822 051 20473	47k 5% 0,1W	3250	4822 051 20472	4k7 5% 0,1W
3190	4822 051 10102	1k 2% 0,25W	3251	4822 051 10102	1k 2% 0,25W
3191	4822 051 20103	10k 5% 0,1W	3252	4822 051 10102	1k 2% 0,25W
3192	4822 051 10102	1k 2% 0,25W	3253	4822 051 10102	1k 2% 0,25W
3193	4822 051 20105	1M 5% 0,1W	3254	4822 051 10102	1k 2% 0,25W
3194	4822 051 20474	470k 5% 0,1W	3255	4822 051 10102	1k 2% 0,25W
3195	4822 051 20104	100k 5% 0,1W	3256	4822 051 10102	1k 2% 0,25W
3196	4822 051 20153	15k 5% 0,1W	3257	4822 051 10102	1k 2% 0,25W
3197	4822 051 20472	4k7 5% 0,1W	3258	4822 051 10102	1k 2% 0,25W
3198	4822 051 20224	220k 5% 0,1W	3259	4822 051 20681	680Ω 5% 0,1W
3199	4822 051 20122	1k2 5% 0,1W	3260	4822 051 20681	680Ω 5% 0,1W
3200	4822 051 20152	1k5 5% 0,1W	3261	4822 051 20225	2M2 5% 0,1W
3201	4822 051 20103	10k 5% 0,1W	3263	4822 051 20123	12k 5% 0,1W
3202	4822 051 20103	10k 5% 0,1W	3264	4822 100 11875	CARBTR 4k7 25%LIN
3203	4822 050 21501	150Ω 1% 0,6W	3265	4822 051 20154	150k 5% 0,1W
3204	4822 051 20471	470Ω 5% 0,1W	3266	4822 051 20472	4k7 5% 0,1W
3205	4822 051 10102	1k 2% 0,25W	3267	4822 051 20392	3k9 5% 0,1W
3207	4822 051 20471	470Ω 5% 0,1W	3268	4822 051 20103	10k 5% 0,1W
3208	4822 051 20821	820Ω 5% 0,1W	3269	4822 051 20274	270k 5% 0,1W
3209	4822 050 25601	560Ω 1% 0,6W	3270	4822 051 20104	100k 5% 0,1W
3210	4822 051 20471	470Ω 5% 0,1W	3271	4822 051 20271	270Ω 5% 0,1W
3211	4822 051 10102	1k 2% 0,25W	3272	4822 051 20103	10k 5% 0,1W
3212	4822 116 83704	12k 2% 0,2W	3273	4822 051 20103	10k 5% 0,1W
3213	4822 051 20103	10k 5% 0,1W	3274	4822 051 20229	22Ω 5% 0,1W
3214	4822 051 20273	27k 5% 0,1W	3275	4822 051 20221	220Ω 5% 0,1W
3215	4822 051 20103	10k 5% 0,1W	3276	4822 051 20472	4k7 5% 0,1W
3216	4822 050 25601	560Ω 1% 0,6W	3277 ▲	4822 053 12339	33Ω 5% 2,5W
3217	4822 051 20331	330Ω 5% 0,1W	3278	4822 051 20471	470Ω 5% 0,1W
3218	4822 051 20331	330Ω 5% 0,1W	3279	4822 051 20472	4k7 5% 0,1W
3219	4822 051 20152	1k5 5% 0,1W	3280	4822 100 11676	CARBTR 10k 30%LIN
3220	4822 051 10102	1k 2% 0,25W	3281	4822 051 20229	22Ω 5% 0,1W
3221	4822 116 83704	12k 2% 0,2W	3282	4822 051 20471	470Ω 5% 0,1W
3222	4822 051 20103	10k 5% 0,1W	3283	4822 051 20471	470Ω 5% 0,1W
3223	4822 051 20183	18k 5% 0,1W	3284	4822 051 20471	470Ω 5% 0,1W
3224	4822 051 20332	3k3 5% 0,1W	3285	4822 051 20471	470Ω 5% 0,1W
3225	4822 051 20332	3k3 5% 0,1W	3286	4822 051 20103	10k 5% 0,1W
3229	4822 051 20223	22k 5% 0,1W	3287	4822 051 20103	10k 5% 0,1W
3233	4822 051 20473	47k 5% 0,1W	3288	4822 051 20683	68k 5% 0,1W
3234	4822 051 20332	3k3 5% 0,1W	3289	4822 051 20104	100k 5% 0,1W
3235	4822 051 20153	15k 5% 0,1W	3290	4822 051 20104	100k 5% 0,1W

3291	4822 051 20759	75Ω 5% 0,1W	3346	4822 051 20222	2k2 5% 0,1W
3292	4822 051 20228	2Ω2 5% 0,1W	3347	4822 051 20472	4k7 5% 0,1W
3293	4822 051 20228	2Ω2 5% 0,1W	3348	4822 051 20473	47k 5% 0,1W
3294	4822 051 20681	680Ω 5% 0,1W	3349	4822 051 20473	47k 5% 0,1W
3295	4822 051 20561	560Ω 5% 0,1W	3350	4822 051 10102	1k 2% 0,25W
3296	4822 051 20104	100k 5% 0,1W	3351	4822 051 20473	47k 5% 0,1W
3297	4822 051 20562	5k6 5% 0,1W	3352	4822 051 20473	47k 5% 0,1W
3298	4822 051 20221	220Ω 5% 0,1W	3353	4822 051 20151	150Ω 5% 0,1W
3299	4822 051 20271	270Ω 5% 0,1W	3354	4822 051 20151	150Ω 5% 0,1W
3300	4822 051 20104	100k 5% 0,1W	3355	4822 051 20103	10k 5% 0,1W
3301	4822 051 20471	470Ω 5% 0,1W	3356	4822 051 20103	10k 5% 0,1W
3302	4822 051 20471	470Ω 5% 0,1W	3357 ▲	4822 050 21802	1k8 1% 0,6W
3303	4822 051 20104	100k 5% 0,1W	3358 ▲	4822 050 21802	1k8 1% 0,6W
3304	4822 051 20104	100k 5% 0,1W	3359 ▲	4822 050 21802	1k8 1% 0,6W
3305 ▲	4822 050 21802	1k8 1% 0,6W	3360 ▲	4822 050 21802	1k8 1% 0,6W
3306 ▲	4822 050 21802	1k8 1% 0,6W	3361	4822 051 20104	100k 5% 0,1W
3307 ▲	4822 050 21802	1k8 1% 0,6W	3362	4822 051 20104	100k 5% 0,1W
3308 ▲	4822 050 21802	1k8 1% 0,6W	3363	4822 051 20151	150Ω 5% 0,1W
3309	4822 051 20471	470Ω 5% 0,1W	3364	4822 051 20151	150Ω 5% 0,1W
3310	4822 051 20471	470Ω 5% 0,1W	3365	4822 100 90108	CARBPOT 20k 20%
3311	4822 100 90109	CARBPOT 2x 20k	3367	4822 051 20103	10k 5% 0,1W
3313 ▲	4822 050 21503	15k 1% 0,6W	3368	4822 051 20103	10k 5% 0,1W
3314 ▲	4822 050 21503				

4101	4822 051 10008	0Ω 5% 0,25W
4102	4822 051 10008	0Ω 5% 0,25W
4103	4822 051 10008	0Ω 5% 0,25W
4104	4822 051 10008	0Ω 5% 0,25W
4105	4822 051 10008	0Ω 5% 0,25W
4106	4822 051 10008	0Ω 5% 0,25W
4107	4822 051 10008	0Ω 5% 0,25W
4108	4822 051 10008	0Ω 5% 0,25W
4109	4822 051 10008	0Ω 5% 0,25W
4110	4822 051 10008	0Ω 5% 0,25W
COILS - CRYSTALS - RESONATORS		
4111	4822 051 10008	0Ω 5% 0,25W
4112	4822 051 10008	0Ω 5% 0,25W
4114	4822 051 10008	0Ω 5% 0,25W
4115	4822 051 10008	0Ω 5% 0,25W
4116	4822 051 10008	0Ω 5% 0,25W
4117	4822 051 10008	0Ω 5% 0,25W
4118	4822 051 10008	0Ω 5% 0,25W
4119	4822 051 10008	0Ω 5% 0,25W
4120	4822 051 10008	0Ω 5% 0,25W
4121	4822 051 10008	0Ω 5% 0,25W
4136	4822 051 10008	0Ω 5% 0,25W
4137	4822 051 10008	0Ω 5% 0,25W
4138	4822 051 10008	0Ω 5% 0,25W
4139	4822 051 10008	0Ω 5% 0,25W
4140	4822 051 10008	0Ω 5% 0,25W
4141	4822 051 10008	0Ω 5% 0,25W
4142	4822 051 10008	0Ω 5% 0,25W
4143	4822 051 10008	0Ω 5% 0,25W
4144	4822 051 10008	0Ω 5% 0,25W
4145	4822 051 10008	0Ω 5% 0,25W
4146	4822 051 10008	0Ω 5% 0,25W
4147	4822 051 10008	0Ω 5% 0,25W
4148	4822 051 10008	0Ω 5% 0,25W
4149	4822 051 10008	0Ω 5% 0,25W
4150	4822 051 10008	0Ω 5% 0,25W
4151	4822 051 10008	0Ω 5% 0,25W
4152	4822 051 10008	0Ω 5% 0,25W
4153	4822 051 10008	0Ω 5% 0,25W
4154	4822 051 10008	0Ω 5% 0,25W
4155	4822 051 10008	0Ω 5% 0,25W
4156	4822 051 10008	0Ω 5% 0,25W
4157	4822 051 10008	0Ω 5% 0,25W
4158	4822 051 10008	0Ω 5% 0,25W
4159	4822 051 10008	0Ω 5% 0,25W
4160	4822 051 10008	0Ω 5% 0,25W
4162	4822 051 10008	0Ω 5% 0,25W
4163	4822 051 10008	0Ω 5% 0,25W
4164	4822 051 10008	0Ω 5% 0,25W
4165	4822 051 10008	0Ω 5% 0,25W
4166	4822 051 10008	0Ω 5% 0,25W
4167	4822 051 10008	0Ω 5% 0,25W
4168	4822 051 10008	0Ω 5% 0,25W
4170	4822 051 10008	0Ω 5% 0,25W
4171	4822 051 10008	0Ω 5% 0,25W

4172	4822 051 10008	0Ω 5% 0,25W	7210	5322 130 42136	BC848C	7361	4822 130 42615	BC817-40
4173	4822 051 10008	0Ω 5% 0,25W	7211	4822 130 42513	BC858C	7362	4822 130 42615	BC817-40
4174	4822 051 10008	0Ω 5% 0,25W	7212 ▲	4822 209 73233	MC79L05ACP	7363	5322 130 60123	BC807-40
4901	4822 051 10008	0Ω 5% 0,25W	7301	4822 209 32002	NJM5532MD	7364	5322 130 60123	BC807-40
4902	4822 051 10008	0Ω 5% 0,25W	7302	4822 209 32002	NJM5532MD	7365	5322 130 60123	BC807-40
4903	4822 051 10008	0Ω 5% 0,25W	7303	4822 209 73157	NJM3415M	7366	4822 130 61207	BC848
4904	4822 051 10008	0Ω 5% 0,25W	7305	4822 209 32002	NJM5532MD	7367	4822 130 61207	BC848
4905	4822 051 10008	0Ω 5% 0,25W	7308	4822 209 83357	NJM4560M	7368	4822 130 61207	BC848
COILS - CRYSTALS - RESONATORS			7309	4822 130 61207	BC848	7369	4822 130 42633	BSR56
5301	4822 157 62216	COIL 100µH 2%	7310	4822 209 83357	NJM4560M	7370	4822 130 42633	BSR56
5302	4822 157 62216	COIL 100µH 2%	7311	4822 130 61207	BC848	7371	5322 130 42012	BC858
5309	4822 157 70299	SMC IND 2,2µH 5%	7312	4822 130 61207	BC848	7372	5322 130 42012	BC858
5310	4822 157 70299	SMC IND 2,2µH 5%	7313	4822 130 61207	BC848			
5313	4822 157 70299	SMC IND 2,2µH 5%	7314	4822 130 61207	BC848			
5314	4822 157 70299	SMC IND 2,2µH 5%	7315	5322 130 42012	BC858			
5315	4822 157 60363	SMC IND 2,2µH 5%	7316	4822 209 31134	CXA1331S			
5317	4822 148 80281	DIG.OUT TRANSFORMER	7317	4822 130 61207	BC848			
5320	4822 157 52983	22µH 10%	7318	5322 130 42012	BC858			
5325	4822 242 81357	RESONATOR 16MHz	7319	5322 130 42012	BC858			
DIODES			7320	4822 209 83357	NJM4560M			
6206	5322 130 34337	BAV99	7321	4822 209 31615	LM324A			
6207	5322 130 31937	BZX84-B4V7	7322	5322 130 42136	BC848C			
6208	5322 130 31928	BAS16	7323	4822 209 31378	NJM4556M			
6209	5322 130 31928	BAS16	7324	4822 130 61207	BC848			
6301	5322 130 31928	BAS16	7325	4822 209 83357	NJM4560M			
6302	5322 130 31928	BAS16	7326	4822 209 83706	BA335			
6305	5322 130 31928	BAS16	7327	4822 130 61207	BC848			
6307	5322 130 31937	BZX84-B4V7	7328	4822 130 61207	BC848			
6308	5322 130 31928	BAS16	7329	4822 130 61207	BC848			
6310	5322 130 34331	BAV70	7330	4822 130 61207	BC848			
6320	4822 130 80622	BAT54	7331	4822 130 61207	BC848			
6321	5322 130 31928	BAS16	7332	4822 130 61207	BC848			
6322	4822 130 81424	BZV86-2V0	7333	5322 130 42012	BC858			
6323	5322 130 81424	BZV86-2V0	7334	4822 130 61207	BC848			
TRANSISTORS & IC's			7335	4822 209 32009	P87C528EBPN			
7200	4822 130 42513	BC858C	7336	4822 130 61207	BC848			
7201	5322 209 60749	LM2931Z-5.0	7341	4822 130 42615	BC817-40			
7202	4822 130 42513	BC858C	7342	4822 130 42615	BC817-40			
7203	4822 130 40995	BD434	7343	4822 130 42615	BC817-40			
7204	5322 130 42136	BC848C	7344	4822 130 42615	BC817-40			
7205	4822 130 40995	BD434	7345	4822 130 42615	BC817-40			
7206	5322 130 42136	BC848C	7346	4822 130 42615	BC817-40			
7207	4822 130 42513	BC858C	7347	4822 130 42615	BC817-40			
7208	4822 130 40982	BD433	7348	4822 130 42615	BC817-40			

DIGITAL PANEL		
2543	4822 122 33608	39nF 10% 63V
2544	4822 124 41997	470µF 20% 10V
2545	4822 126 10002	100nF 20% 25V
2546	5322 122 32531	100pF 5% 50V
2547	4822 122 33177	10nF 20% 50V
2548	4822 122 33177	10nF 20% 50V
2549	5322 122 32452	47pF 5% 63V
2550	5322 122 32661	56pF 5% 50V
2551	4822 121 41857	10nF 5% 250V
2552	4822 121 41857	10nF 5% 250V
2553	4822 126 10002	100nF 20% 25V
2554	4822 126 10002	100nF 20% 25V
2555	4822 126 10002	100nF 20% 25V
2556	4822 126 10002	100nF 20% 25V
2557	4822 126 10002	100nF 20% 25V
2558	4822 126 10002	100nF 20% 25V
2559	4822 126 10002	100nF 20% 25V
2560	4822 126 10002	100nF 20% 25V
2561	4822 126 10002	100nF 20% 25V
2562	4822 126 10002	100nF 20% 25V
2563	4822 126 10002	100nF 20% 25V
2564	4822 124 40433	47µF 20% 25V
2565	4822 126 10002	100nF 20% 25V
2566	4822 126 10002	100nF 20% 25V
2567	5322 116 80853	560pF 5% 63V
2568	5322 116 80853	560pF 5% 63V
2569	5322 122 32661	56pF 5% 50V
2570	5322 122 32661	56pF 5% 50V
2571	5322 126 10328	1500pF 2% 63V
2572	5322 126 10328	1500pF 2% 63V
2573	5322 122 32531	100pF 5% 50V
2574	5322 122 32531	100pF 5% 50V
2575	4822 122 32999	2,2nF 5% NPO
2576	4822 122 32999	2,2nF 5% NPO
2577	5322 122 32661	56pF 5% 50V
2578	5322 122 32661	56pF 5% 50V
2579	5322 122 32659	33pF 5% 50V
2580	5322 122 32452	47pF 5% 63V
2581	4822 124 41584	100µF 20% 10V
2582	4822 124 41584	100µF 20% 10V
2583	4822 124 40433	47µF 20% 25V
2584	4822 124 40433	47µF 20% 25V
2585	4822 124 40248	10µF 20% 63V
2586	4822 124 40248	10µF 20% 63V
2587	4822 124 40433	47µF 20% 25V
2588	5322 122 32531	100pF 5% 50V
2589	4822 124 40433	47µF 20% 25V
2590	4822 124 40433	47µF 20% 25V
2591	4822 124 40246	4,7µF 20% 63V
2592	5322 122 32452	47pF 5% 63V
2593	5322 122 32531	100pF 5% 50V
2594	5322 122 32531	100pF 5% 50V
2595	5322 122 32531	100pF 5% 50V
2596	5322 122 32531	100pF 5% 50V

RESISTORS		
3501	4822 051 20229	22Ω 5% 0,1W
3502	4822 051 20229	22Ω 5% 0,1W
3503	4822 051 20229	22Ω 5% 0,1W
3504	4822 051 20479	47Ω 5% 0,1W
3505	4822 051 20479	47Ω 5% 0,1W
3506	4822 051 20105	1M 5% 0,1W
3507	4822 051 10102	1k 2% 0,25W
3508	4822 051 20105	1M 5% 0,1W
3509	4822 051 10102	1k 2% 0,25W
3510	4822 051 20479	47Ω 5% 0,1W
3511	4822 051 20479	47Ω 5% 0,1W
3512	4822 051 20122	1k2 5% 0,1W
3513	4822 051 20222	2k2 5% 0,1W
3514	4822 051 20222	2k2 5% 0,1W
3515	4822 051 20104	100k 5% 0,1W
3516	4822 051 20103	10k 5% 0,1W
3517	4822 051 20104	100k 5% 0,1W
3518	4822 051 20101	100Ω 5% 0,1W
3519	4822 051 20123	12k 5% 0,1W
3520	4822 051 20222	2k2 5% 0,1W
3521	4822 051 20272	2k7 5% 0,1W
3522	4822 051 20123	12k 5% 0,1W
3523	4822 051 20121	120Ω 5% 0,1W
3524	4822 051 20122	1k2 5% 0,1W
3525	4822 051 20181	180Ω 5% 0,1W
3526	4822 051 20221	220Ω 5% 0,1W
3527	4822 051 20153	15k 5% 0,1W
3528	4822 051 20101	100Ω 5% 0,1W
3529	4822 051 20331	330Ω 5% 0,1W
3530	4822 051 20479	47Ω 5% 0,1W
3531	4822 051 20223	22k 5% 0,1W
3532	4822 051 20223	22k 5% 0,1W
3533	4822 051 20101	100Ω 5% 0,1W
3534	4822 051 20479	47Ω 5% 0,1W
3535	4822 051 20228	2Ω2 5% 0,1W
3536	4822 051 20479	47Ω 5% 0,1W
3537	4822 051 20479	47Ω 5% 0,1W
3538	4822 051 20223	22k 5% 0,1W
3539	4822 051 20223	22k 5% 0,1W
3540	4822 051 10102	1k 2% 0,25W
3541	4822 051 20472	4k7 5% 0,1W
3542	4822 051 20104	100k 5% 0,1W
3543	4822 051 20472	4k7 5% 0,1W
3544	4822 051 20103	10k 5% 0,1W
3545	4822 051 20472	4k7 5% 0,1W
3546	4822 051 20333	33k 5% 0,1W
3547	4822 051 20333	33k 5% 0,1W
3548	4822 051 20682	6k8 5% 0,1W
3549	4822 051 20472	4k7 5% 0,1W
3550	4822 100 11956	CERMTR 10k 25%
3551	4822 051 20479	47Ω 5% 0,1W
3552	4822 051 20479	47Ω 5% 0,1W
3553	4822 051 20103	10k 5% 0,1W
3554	4822 051 20479	47Ω 5% 0,1W
3555	4822 051 20479	47Ω 5% 0,1W
3556	4822 051 20479	47Ω 5% 0,1W
3557	4822 051 20472	4k7 5% 0,1W
3558	4822 051 20472	4k7 5% 0,1W
3559	4822 051 20821	820Ω 5% 0,1W
3560	4822 116 30441	1k5 20% NTC
3561	4822 051 20331	330Ω 5% 0,1W
3562	4822 051 20339	33Ω 5% 0,1W
3563	4822 051 20228	2Ω2 5% 0,1W
3564	4822 051 20689	68Ω 5% 0,1W
3565	4822 117 10167	27k 1% 0,125W
3566	4822 117 10167	27k 1% 0,125W
3567	4822 051 20105	1M 5% 0,1W
3568	4822 051 20105	1M 5% 0,1W
3569	4822 051 20103	10k 5% 0,1W
3570	4822 051 20103	10k 5% 0,1W
3571	4822 117 10166	4k7 1% 0,125W
3572	4822 117 10166	4k7 1% 0,125W
3573	4822 117 10166	4k7 1% 0,125W
3574	4822 117 10166	4k7 1% 0,125W
3575	4822 117 10165	5k6 1% 0,125W
3576	4822 117 10165	5k6 1% 0,125W
3577	4822 051 20472	4k7 5% 0,1W
3578	4822 051 20473	47k 5% 0,1W
3579	4822 051 20479	47Ω 5% 0,1W
3580	4822 051 20479	47Ω 5% 0,1W
3581	4822 051 20479	47Ω 5% 0,1W
3582	4822 051 20479	47Ω 5% 0,1W
3583	4822 051 20479	47Ω 5% 0,1W
3584	4822 051 20479	47Ω 5% 0,1W
3585	4822 051 20479	47Ω 5% 0,1W
3586	4822 051 20472	4k7 5% 0,1W
3587	4822 051 20472	4k7 5% 0,1W
3588	4822 051 20472	4k7 5% 0,1W
3589	4822 051 20472	4k7 5% 0,1W
3590	4822 051 20472	4k7 5% 0,1W
3591	4822 051 20473	47k 5% 0,1W

3592	4822 051 20479	47Ω 5% 0,1W		7522 ▲ 5322 130 41982 BC848B		
3593	4822 051 20689	68Ω 5% 0,1W		7523 5322 130 41983 BC858B		
3594	4822 051 20689	68Ω 5% 0,1W		7524 ▲ 5322 130 41982 BC848B		
3595	4822 051 20479	47Ω 5% 0,1W		7525 ▲ 5322 130 41982 BC848B		
3597	4822 051 20008	0Ω 5% 0,1W				
COILS - CRYSTALS - RESONATORS						
5501	4822 157 70299	SMC IND 2,2μH 5%				
5502	4822 157 70299	SMC IND 2,2μH 5%				
5503	4822 157 70299	SMC IND 2,2μH 5%				
5504	4822 157 70299	SMC IND 2,2μH 5%				
5505	4822 242 81361	CER FILT 35MHz				
5507	4822 157 70299	SMC IND 2,2μH 5%				
5508	4822 157 70299	SMC IND 2,2μH 5%				
5509	4822 157 70299	SMC IND 2,2μH 5%				
5510	4822 242 81361	CER FILT 35MHz				
5511	4822 157 70299	2,2μH 5%				
5531	4822 157 70301	560μH				
5532	4822 157 70301	560μH				
5550	4822 242 81358	CRYSTAL 24,576MHz				
5551	4822 242 81359	CRYSTAL 22,579MHz				
5555	4822 157 70299	SMC IND 2,2μH 5%				
5556	4822 157 70299	SMC IND 2,2μH 5%				
DIODES						
6501	5322 130 31928	BAS16				
6502	4822 130 83249	BZX84-B3V6				
6503	5322 130 31928	BAS16				
6504	4822 130 82594	BAT54C				
TRANSISTORS & IC's						
7500	4822 209 30144	74HC4046A				
7501	4822 209 30718	M51581FP				
7502	4822 209 32004	SAA2011GP				
7503	4822 209 32005	SAA2021GP/M1A				
7504	4822 209 32003	SAA2001GP				
7505	4822 209 32003	SAA2001GP				
7506	4822 209 32007	SAA2041GP				
7507	4822 209 31999	MN41464				
7508	4822 209 32008	SAA2051GP				
7509	4822 209 32006	SAA2031GP				
7510	4822 209 30704	MC74HCU04D				
7511	4822 209 30706	SAA7321GP/M5				
7512	4822 209 73157	NJM3415M				
7513 ▲	5322 130 41982	BC848B				
7514	4822 209 31622	AK5339				
7515	5322 209 12479	PC74HCT374T				
7521 ▲	5322 130 41982	BC848B				

CONTROL & DISPLAY PANEL		
MISCELLANEOUS		
RESISTORS		
2481	4822 122 33177	10nF 20% 50V
2482	4822 122 33177	10nF 20% 50V
3312	4822 102 10459	POTM 2x 20k
3400	4822 051 20103	10k 5% 0,1W
3413	4822 051 20103	10k 5% 0,1W
3415	4822 051 20103	10k 5% 0,1W
3416	4822 051 20103	10k 5% 0,1W
3417	4822 051 20683	68k 5% 0,1W
3418	4822 051 20683	68k 5% 0,1W
3419	4822 051 20473	47k 5% 0,1W
3421	4822 051 20104	100k 5% 0,1W
3422	4822 051 20104	100k 5% 0,1W
3423	4822 051 20563	56k 5% 0,1W
3424	4822 051 20563	56k 5% 0,1W
3425	4822 051 20102	1k 5% 0,1W
3426	4822 051 20102	1k 5% 0,1W
3427	4822 051 20121	120Ω 5% 0,1W
3428	4822 051 20121	120Ω 5% 0,1W
3429	4822 051 20104	100k 5% 0,1W
3430	4822 051 20104	100k 5% 0,1W
3431	4822 051 20472	4k7 5% 0,1W
3432	4822 051 20472	4k7 5% 0,1W
3433	4822 051 20472	4k7 5% 0,1W
3434	4822 051 20472	4k7 5% 0,1W
3435	4822 051 20684	680k 5% 0,1W
3436	4822 051 20684	680k 5% 0,1W
3437	4822 051 20684	680k 5% 0,1W
3438	4822 051 20684	680k 5% 0,1W
3439	4822 051 20103	10k 5% 0,1W
3440	4822 051 20103	10k 5% 0,1W
3441	4822 051 20103	10k 5% 0,1W
3442	4822 051 20103	10k 5% 0,1W
3443	4822 051 20103	10k 5% 0,1W
3444	4822 051 20331	330Ω 5% 0,1W
3445	4822 051 20331	330Ω 5% 0,1W
3446	4822 051 20331	330Ω 5% 0,1W
3447	4822 051 20331	330Ω 5% 0,1W
3448	4822 051 20332	3k3 5% 0,1W
3449	4822 051 20332	3k3 5% 0,1W
3451	4822 051 20103	10k 5% 0,1W
3452	4822 051 20103	10k 5% 0,1W
3453	4822 051 20103	10k 5% 0,1W
3454	4822 051 20103	10k 5% 0,1W
3455	4822 051 20103	10k 5% 0,1W
3456	4822 051 20103	10k 5% 0,1W
3457	4822 051 20103	10k 5% 0,1W
3458	4822 051 20103	10k 5% 0,1W
3459	4822 051 20103	10k 5% 0,1W
3460	4822 051 20271	270Ω 5% 0,1W
3461	4822 051 20103	10k 5% 0,1W

3462	4822 051 20103	10k 5% 0,1W
3464	4822 051 20101	100Ω 5% 0,1W
3470	4822 051 10102	1k 2% 0,25W
3471	4822 051 20153	15k 5% 0,1W
3472	4822 051 20153	15k 5% 0,1W
3473	4822 051 20109	10Ω 5% 0,1W
3474	4822 051 20109	10Ω 5% 0,1W
3475	4822 051 20224	220k 5% 0,1W
COIL - RESONATOR		
5401 ▲	5322 242 73697	RESONATOR 8MHz
5410	4822 157 70298	15µH 5%
DIODES		
6411	5322 130 31928	BAS16
6412	5322 130 31928	BAS16
6413	5322 130 31928	BAS16
6414	5322 130 31928	BAS16
6421	5322 130 31928	BAS16
6422	5322 130 31928	BAS16
6425	4822 130 82955	GL3PR8
TRANSISTORS & IC's		
7401	4822 209 32001	TMP87PM70F
7402	4822 130 60511	BC847B
7403	4822 130 60511	BC847B
7405	5322 130 60123	BC807-40
7406	5322 130 60123	BC807-40
7407	4822 130 42615	BC817-40
7408	4822 130 42615	BC817-40
7410	4822 214 51795	IR RECEIVER GP1U500X
7411	4822 209 83357	NJM4560M
7412	4822 209 83357	NJM4560M
7413	4822 209 83357	NJM4560M
7414	4822 130 61207	BC848
7415	5322 130 42012	BC858
7416	4822 130 61207	BC848
7451	4822 130 61207	BC848
7452	4822 130 61207	BC848
7453	4822 130 61207	BC848
7454	4822 130 61207	BC848
7455	4822 130 61207	BC848
7456	4822 130 61207	BC848
7465	4822 130 61207	BC848
7466	4822 130 61207	BC848
7467	4822 130 61207	BC848
7468	4822 130 61207	BC848
7471	4822 209 32002	NJM5532MD

POWER SUPPLY PANEL			TRANSISTOR
MISCELLANEOUS			7201 4822 130 41324 BC327-40
1202 ▲ 4822 265 31015 MAINSINLET			
1202 ▲ 4822 265 31016 MAINSINLET only /17			
1203 ▲ 4822 272 10269 VOLTAGE SELECTOR			
1204 ▲ 4822 276 13277 MAINSSWITCH			
1205 ▲ 4822 071 51602 FUSE T1,6A			
1205 ▲ 4822 253 50137 FUSE T2,5A only /17			
1206 ▲ 4822 071 56301 FUSE T630mA			
1206 ▲ 4822 253 10138 FUSE T1A only /17			
1207 ▲ 4822 071 56301 FUSE T630mA			
1207 ▲ 4822 253 10138 FUSE T1A only /17			
CAPACITORS			
2211 4822 124 23172 470µF 20% 50V			
2212 ▲ 4822 121 70087 47nF 10% 250V			
2213 4822 124 22031 4,7µF 20% 63V			
2215 4822 124 21511 2200µF 20% 25V			
2216 4822 124 80443 2200µF 20% 25V			
2217 4822 124 41591 6800µF 20% 16V			
2218 ▲ 4822 121 70087 47nF 10% 250V			
RESISTORS			
3201 4822 050 23302 3k3 1% 0,6W			
3202 ▲ 4822 050 23302 3k3 1% 0,6W			
3203 4822 052 10399 39Ω 5% 0,33W			
COILS - TRANSFORMER			
5001 ▲ 4822 146 31192 MAINSTRANSF /00 /05			
5001 ▲ 4822 146 31207 MAINSTRANSF /06			
5201 ▲ 4822 157 63756 1mH			
5202 4822 157 50963 2,2µH 20%			
5203 4822 157 50963 2,2µH 20%			
DIODES			
6201 ▲ 4822 130 82079 D3SBA20			
6202 ▲ 4822 130 82079 D3SBA20			
6203 ▲ 5322 130 30684 1N4002GP			
6204 4822 130 34398 BZX79-C24			
6205 4822 130 34233 BZX79-C5V1			

Service Service Service

Product Service Group CE Audio

Service Information

Already published Service Informations: A93-362 (4822 725 24912)
A94-353 (4822 725 24934)

REPLACEMENT OF THE DCC-HEAD

As already stated in the service manual the DCC-head cannot be replaced without special adjustment tools. In case of defective heads or write amplifiers the complete loading assy has to be sent to Philips Consumer Service for repair and adjustment of the recording current. Loose DCC-heads are not supplied. The service codenumber of the DCC-head, published in the partslist of the RED1 tape transport (page 60), has been referred to the service code of the complete loading assembly (4822 691 20833).

CORRECTIONS TO THE SERVICE MANUAL

- Partslist of RED1 tape transport (page 60)
Service codenumber of capstan motor, item 1023, should read 4822 361 21646.
- Partslist of digital board PCB5 (page 76)
Service codenumber of A/D-converter AK5339, item 7514, should read 4822 209 33849.

SERVICE TOOLS

- Dolby testcassette MTT-150 is available under service codenumber 4822 397 30271, Dolby testcassette TCC-130 via codenumber 4822 397 30269. With one of these cassettes the Dolby adjustment can be achieved.
- The DCC cleaning cassette SBC3500 is available under service codenumber 4822 015 20646.

Service Service Service

Product Service Group CE Audio

Service Information

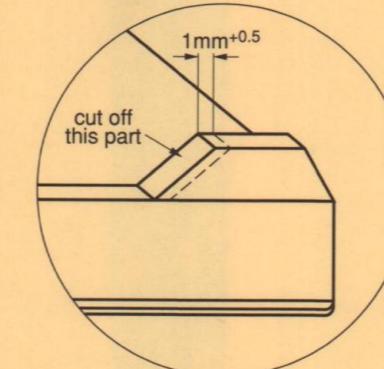
Already published Service Informations : A93-362 (4822 725 24912)

SERVICE HINT

SYMPTOM : When the cassette is lifted at the left side, in order to remove it from the opened tray, the cassette gets stuck behind the Philips-logo.

CAUSE : The drawer (pos. 209) does not open far enough, because the tray-out switch (pos. 1437) is actuated too early.

CURE : The switch is opened/closed by a ridge, located at the back righthand corner of the drawer.
To delay the switch actuation cut off some plastic of the ridge according to the sketch below.



REMARK : This modification also takes an influence on the feature "touch to close". If too much plastic is removed the tray might already close while a cassette is inserted ! Try to find an optimal compromise.