

V Dikshita Rao

B.Tech.
KL University ,Vijayawada

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EDUCATION

Degree/Certificate	Institute/Board	CGPA/Percentage	Year
B.Tech.	KL University ,Vijayawada	9.15 (Current)	2022-Present
Senior Secondary	CBSE Board	89.0%	2022
Secondary	CBSE Board	94.0%	2020

EXPERIENCE

- **BHILAI STEEL PLANT**
Industrial Intern
May 2024 - Jul. 2024
Bhilai, Chhattisgarh
- **PRODIGY infotech**
Academic Intern
May. 2024 - Jun. 2024
online, webdev.
 - Front End
 - Web Dev.

PROJECTS

- **Designing of SRAM cell using 45nm technology in Cadence**
Guntur , Vijayawada
Dec. 2023 - Feb. 2024
Drive
- **Traffic Congestion Control System**
Guntur , Vijayawada
Dec. 2023 - Mar. 2024
Github

SKILLS

- **Programming:** Verilog HDL, Python, C/C++, Java,
- **Skills:** Software tools: MATLAB, CADENCE EDA, Xilinx Vivado
- **Operating Systems:** Windows, Red Hat Linux
- **Non Technical:** Communication, Problem-Solving, Teamwork, Time Management, Leadership

CERTIFICATIONS

- **IOT:** Embedded System Application and IoT Programming [Certificate](#)
- **IIoT:** IIoT Programming Automation [Certificate](#)

POSITIONS OF RESPONSIBILITY

- **Volunteer/Member,** Always@VLSI Club, KL University
– Conducted so and so event for this many audience
Dec. 2022 - Jul. 2022

ACHIEVEMENTS

- **SEMI-Custom design in cadence EDA,** Workshop, KL University, VIJAYAWADA [Certificate](#) 2024
- **PRODIGY Infotech,** Did front end projects 2024

EXTRACURRICULARS

- **Core Member,** Always@VLSI
- **Student Volunteer,** Always@VLSI