CPE301 – FALL 2019

Design Assignment 6

Student Name: Dillon Archibald
Student #: 5004439916
Student Email: Archid1@unlv.nevada.edu
Primary Github address: https://github.com/Dil-bert/Alabaster.git
Directory: **Alabaster**/DesignAssignments/DA6/**DA6**/

Submit the following for all Labs:

1. In the document, for each task submit the modified or included code (only) with highlights and justifications of the modifications. Also, include the comments.
2. Use the previously create a Github repository with a random name (no CPE/301, Lastname, Firstname). Place all labs under the root folder ESD301/DA, sub-folder named LABXX, with one document and one video link file for each lab, place modified asm/c files named as LabXX-TYY.asm/c.
3. If multiple asm/c files or other libraries are used, create a folder LabXX-TYY and place these files inside the folder.
4. The folder should have a) Word document (see template), b) source code file(s) and other include files, c) text file with youtube video links (see template).

1. COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS

List of Components used

- 1x atmega 328p
- 1x mpu6050
- Wires and bread board

2. INITIAL/MODIFIED/DEVELOPED CODE OF TASK 1/A

Insert initial code here

```
/*
* USART_RS232_C_file.c
* http://www.electronicwings.com
*
*/

#include "uart.h"                                       /* Include USART header file */

void USART_Init(unsigned long BAUDRATE)                         /* USART initialize function
*/
{
UCSR0B |= (1 << RXEN0) | (1 << TXEN0);                          /* Enable USART transmitter
and receiver */
UCSR0C |= (1 << UCSZ00) | (1 << UCSZ01);    /* Write USCRC for 8 bit data and 1 stop bit */
UBRR0L = BAUD_PRESCALE;                                                 /* Load UBRRL with
lower 8 bit of prescale value */
UBRR0H = (BAUD_PRESCALE >> 8);                                  /* Load UBRRH with upper 8 bit of
prescale value */
}

char USART_RxChar()                                                             /* Data
receiving function */
{
while (!(UCSR0A & (1 << RXC0)));                                /* Wait until new data receive
*/
return(UDR0);                                                           /* Get and return
received data */
}

void USART_TxChar(char data)                                    /* Data transmitting function
*/
{
UDR0 = data;                                                                    /* Write data
to be transmitting in UDR */
while (!(UCSR0A & (1<<UDRE0)));                                 /* Wait until data transmit
and buffer get empty */
}

void USART_SendString(char *str)                                /* Send string of USART data
function */
{
int i=0;
```

```
while (str[i]!=0)
{
USART_TxChar(str[i]);                                   /* Send each char of string till the
NULL */
i++;
}
}

/*
* USART_RS232_H_file.h
* http://www.electronicwings.com
*
*/


#ifndef USART_RS232_H_FILE_H_                          /* Define library H file if not defined */
#define USART_RS232_H_FILE_H_

#define F_CPU 16000000UL                                        /* Define CPU clock Frequency
e.g. here its 8MHz */
#include <avr/io.h>                                             /* Include AVR std. library
file */
#define BAUD_PRESCALE (((F_CPU / (BAUDRATE * 16UL))) - 1)    /* Define prescale value */

void USART_Init(unsigned long);                                 /* USART initialize function */
char USART_RxChar();                                            /* Data receiving function */
void USART_TxChar(char);                                        /* Data transmitting function */
void USART_SendString(char*);                          /* Send string of USART data function */


#endif                                                                  /*
USART_RS232_H_FILE_H_ */

/*
* MPU6050_res_define.h
*
* Created: 04/21/2016 22:47:10
*  Author: Suraj
*/


#ifndef MPU6050_RES_DEFINE_H_
#define MPU6050_RES_DEFINE_H_

#include <avr/io.h>

#define XG_OFFS_TC 0x00
#define YG_OFFS_TC 0x01
#define ZG_OFFS_TC 0x02
#define X_FINE_GAIN 0x03
#define Y_FINE_GAIN 0x04
#define Z_FINE_GAIN 0x05
#define XA_OFFS_H 0x06
#define XA_OFFS_L_TC 0x07
#define YA_OFFS_H 0x08
#define YA_OFFS_L_TC 0x09
#define ZA_OFFS_H 0x0A
```

```
#define ZA_OFFS_L_TC 0x0B
#define XG_OFFS_USRH 0x13
#define XG_OFFS_USRL 0x14
#define YG_OFFS_USRH 0x15
#define YG_OFFS_USRL 0x16
#define ZG_OFFS_USRH 0x17
#define ZG_OFFS_USRL 0x18
#define SMPLRT_DIV 0x19
#define CONFIG 0x1A
#define GYRO_CONFIG 0x1B
#define ACCEL_CONFIG 0x1C
#define FF_THR 0x1D
#define FF_DUR 0x1E
#define MOT_THR 0x1F
#define MOT_DUR 0x20
#define ZRMOT_THR 0x21
#define ZRMOT_DUR 0x22
#define FIFO_EN 0x23
#define I2C_MST_CTRL 0x24
#define I2C_SLV0_ADDR 0x25
#define I2C_SLV0_REG 0x26
#define I2C_SLV0_CTRL 0x27
#define I2C_SLV1_ADDR 0x28
#define I2C_SLV1_REG 0x29
#define I2C_SLV1_CTRL 0x2A
#define I2C_SLV2_ADDR 0x2B
#define I2C_SLV2_REG 0x2C
#define I2C_SLV2_CTRL 0x2D
#define I2C_SLV3_ADDR 0x2E
#define I2C_SLV3_REG 0x2F
#define I2C_SLV3_CTRL 0x30
#define I2C_SLV4_ADDR 0x31
#define I2C_SLV4_REG 0x32
#define I2C_SLV4_DO 0x33
#define I2C_SLV4_CTRL 0x34
#define I2C_SLV4_DI 0x35
#define I2C_MST_STATUS 0x36
#define INT_PIN_CFG 0x37
#define INT_ENABLE 0x38
#define DMP_INT_STATUS 0x39
#define INT_STATUS 0x3A
#define ACCEL_XOUT_H 0x3B
#define ACCEL_XOUT_L 0x3C
#define ACCEL_YOUT_H 0x3D
#define ACCEL_YOUT_L 0x3E
#define ACCEL_ZOUT_H 0x3F
#define ACCEL_ZOUT_L 0x40
#define TEMP_OUT_H 0x41
#define TEMP_OUT_L 0x42
#define GYRO_XOUT_H 0x43
#define GYRO_XOUT_L 0x44
#define GYRO_YOUT_H 0x45
#define GYRO_YOUT_L 0x46
#define GYRO_ZOUT_H 0x47
#define GYRO_ZOUT_L 0x48
#define EXT_SENS_DATA_00 0x49
#define EXT_SENS_DATA_01 0x4A
#define EXT_SENS_DATA_02 0x4B
#define EXT_SENS_DATA_03 0x4C
```

```
#define EXT_SENS_DATA_04 0x4D
#define EXT_SENS_DATA_05 0x4E
#define EXT_SENS_DATA_06 0x4F
#define EXT_SENS_DATA_07 0x50
#define EXT_SENS_DATA_08 0x51
#define EXT_SENS_DATA_09 0x52
#define EXT_SENS_DATA_10 0x53
#define EXT_SENS_DATA_11 0x54
#define EXT_SENS_DATA_12 0x55
#define EXT_SENS_DATA_13 0x56
#define EXT_SENS_DATA_14 0x57
#define EXT_SENS_DATA_15 0x58
#define EXT_SENS_DATA_16 0x59
#define EXT_SENS_DATA_17 0x5A
#define EXT_SENS_DATA_18 0x5B
#define EXT_SENS_DATA_19 0x5C
#define EXT_SENS_DATA_20 0x5D
#define EXT_SENS_DATA_21 0x5E
#define EXT_SENS_DATA_22 0x5F
#define EXT_SENS_DATA_23 0x60
#define MOT_DETECT_STATUS 0x61
#define I2C_SLV0_DO 0x63
#define I2C_SLV1_DO 0x64
#define I2C_SLV2_DO 0x65
#define I2C_SLV3_DO 0x66
#define I2C_MST_DELAY_CTRL 0x67
#define SIGNAL_PATH_RESET 0x68
#define MOT_DETECT_CTRL 0x69
#define USER_CTRL 0x6A
#define PWR_MGMT_1 0x6B
#define PWR_MGMT_2 0x6C
#define BANK_SEL 0x6D
#define MEM_START_ADDR 0x6E
#define MEM_R_W 0x6F
#define DMP_CFG_1 0x70
#define DMP_CFG_2 0x71
#define FIFO_COUNTH 0x72
#define FIFO_COUNTL 0x73
#define FIFO_R_W 0x74
#define WHO_AM_I 0x75



#endif /* MPU6050_RES_DEFINE_H_ */

/*
* I2C_Master_C_file.c
* http://www.electronicwings.com
*
*/


#include "i2c_master.h"                                                 /* Include I2C
header file */

void I2C_Init()
       /* I2C initialize function */
{
```

```
TWBR = BITRATE(TWSR = 0x00);                                    /* Get bit rate
register value by formula */
}


uint8_t I2C_Start(char slave_write_address)                             /* I2C start
function */
{
uint8_t status;                                                                 /*
Declare variable */
TWCR = (1<<TWSTA)|(1<<TWEN)|(1<<TWINT);                         /* Enable TWI,
generate start condition and clear interrupt flag */
while (!(TWCR & (1<<TWINT)));                                   /* Wait until TWI
finish its current job (start condition) */
status = TWSR & 0xF8;                                                   /* Read TWI
status register with masking lower three bits */
if (status != 0x08)                                                             /*
Check weather start condition transmitted successfully or not? */
return 0;
       /* If not then return 0 to indicate start condition fail */
TWDR = slave_write_address;                                             /* If yes then
write SLA+W in TWI data register */
TWCR = (1<<TWEN)|(1<<TWINT);                                    /* Enable TWI and
clear interrupt flag */
while (!(TWCR & (1<<TWINT)));                                   /* Wait until TWI
finish its current job (Write operation) */
status = TWSR & 0xF8;                                                   /* Read TWI
status register with masking lower three bits */
if (status == 0x18)                                                             /*
Check weather SLA+W transmitted & ack received or not? */
return 1;
       /* If yes then return 1 to indicate ack received i.e. ready to accept data byte */
if (status == 0x20)                                                             /*
Check weather SLA+W transmitted & nack received or not? */
return 2;
       /* If yes then return 2 to indicate nack received i.e. device is busy */
else
return 3;
       /* Else return 3 to indicate SLA+W failed */
}

uint8_t I2C_Repeated_Start(char slave_read_address)             /* I2C repeated start function
*/
{
uint8_t status;                                                                 /*
Declare variable */
TWCR = (1<<TWSTA)|(1<<TWEN)|(1<<TWINT);                         /* Enable TWI,
generate start condition and clear interrupt flag */
while (!(TWCR & (1<<TWINT)));                                   /* Wait until TWI
finish its current job (start condition) */
status = TWSR & 0xF8;                                                   /* Read TWI
status register with masking lower three bits */
if (status != 0x10)                                                             /*
Check weather repeated start condition transmitted successfully or not? */
return 0;
       /* If no then return 0 to indicate repeated start condition fail */
TWDR = slave_read_address;                                              /* If yes then
write SLA+R in TWI data register */
```

```
TWCR = (1<<TWEN)|(1<<TWINT);                                    /* Enable TWI and
clear interrupt flag */
while (!(TWCR & (1<<TWINT)));                                   /* Wait until TWI
finish its current job (Write operation) */
status = TWSR & 0xF8;                                                   /* Read TWI
status register with masking lower three bits */
if (status == 0x40)                                                             /*
Check weather SLA+R transmitted & ack received or not? */
return 1;
        /* If yes then return 1 to indicate ack received */
if (status == 0x20)                                                             /*
Check weather SLA+R transmitted & nack received or not? */
return 2;
        /* If yes then return 2 to indicate nack received i.e. device is busy */
else
return 3;
        /* Else return 3 to indicate SLA+W failed */
}

void I2C_Stop()
        /* I2C stop function */
{
TWCR=(1<<TWSTO)|(1<<TWINT)|(1<<TWEN);                   /* Enable TWI, generate stop
condition and clear interrupt flag */
while(TWCR & (1<<TWSTO));                                               /* Wait until
stop condition execution */
}

void I2C_Start_Wait(char slave_write_address)                   /* I2C start wait function */
{
uint8_t status;                                                                 /*
Declare variable */
while (1)
{
TWCR = (1<<TWSTA)|(1<<TWEN)|(1<<TWINT);                 /* Enable TWI, generate start
condition and clear interrupt flag */
while (!(TWCR & (1<<TWINT)));                           /* Wait until TWI finish its
current job (start condition) */
status = TWSR & 0xF8;                                           /* Read TWI status
register with masking lower three bits */
if (status != 0x08)                                                     /* Check
weather start condition transmitted successfully or not? */
continue;                                                                       /* If
no then continue with start loop again */
TWDR = slave_write_address;                                     /* If yes then write
SLA+W in TWI data register */
TWCR = (1<<TWEN)|(1<<TWINT);                            /* Enable TWI and clear
interrupt flag */
while (!(TWCR & (1<<TWINT)));                           /* Wait until TWI finish its
current job (Write operation) */
status = TWSR & 0xF8;                                           /* Read TWI status
register with masking lower three bits */
if (status != 0x18 )                                            /* Check weather SLA+W
transmitted & ack received or not? */
{
I2C_Stop();                                                             /* If not then
generate stop condition */
continue;                                                               /* continue
with start loop again */
```

```
}
break;                                                                  /* If
yes then break loop */
}
}

uint8_t I2C_Write(char data)                                            /* I2C write
function */
{
uint8_t status;                                                         /*
Declare variable */
TWDR = data;                                                            /* Copy
data in TWI data register */
TWCR = (1<<TWEN)|(1<<TWINT);                                    /* Enable TWI and
clear interrupt flag */
while (!(TWCR & (1<<TWINT)));                                   /* Wait until TWI
finish its current job (Write operation) */
status = TWSR & 0xF8;                                                   /* Read TWI
status register with masking lower three bits */
if (status == 0x28)                                                     /*
Check weather data transmitted & ack received or not? */
return 0;
        /* If yes then return 0 to indicate ack received */
if (status == 0x30)                                                     /*
Check weather data transmitted & nack received or not? */
return 1;
        /* If yes then return 1 to indicate nack received */
else
return 2;
        /* Else return 2 to indicate data transmission failed */
}

char I2C_Read_Ack()                                                     /* I2C
read ack function */
{
TWCR=(1<<TWEN)|(1<<TWINT)|(1<<TWEA);                    /* Enable TWI, generation of
ack and clear interrupt flag */
while (!(TWCR & (1<<TWINT)));                                   /* Wait until TWI
finish its current job (read operation) */
return TWDR;                                                            /*
Return received data */
}

char I2C_Read_Nack()                                                    /* I2C
read nack function */
{
TWCR=(1<<TWEN)|(1<<TWINT);                                              /* Enable TWI
and clear interrupt flag */
while (!(TWCR & (1<<TWINT)));                                   /* Wait until TWI
finish its current job (read operation) */
return TWDR;                                                            /*
Return received data */
}

/*
* I2C_Master_H_file.h
* http://www.electronicwings.com
*
```

```
*/


#ifndef I2C_MASTER_H_FILE_H_                                    /* Define library H file if not
defined */
#define I2C_MASTER_H_FILE_H_

#define F_CPU 16000000UL                                                    /* Define CPU clock
Frequency e.g. here its 8MHz */
#include <avr/io.h>                                                         /* Include AVR std.
library file */
#include <util/delay.h>                                                     /* Include delay
header file */
#include <math.h>                                                           /* Include math
function */
#define SCL_CLK 100000L                                                     /* Define SCL clock
frequency */
#define BITRATE(TWSR)  ((F_CPU/SCL_CLK)-16)/(2) /* Define bit rate */

void I2C_Init();                                                            /* I2C initialize
function */
uint8_t  I2C_Start(char);                                           /* I2C start function */
uint8_t  I2C_Repeated_Start(char);                              /* I2C repeated start function */
void I2C_Stop();                                                            /* I2C stop function
*/
void I2C_Start_Wait(char);                                          /* I2C start wait function */
uint8_t  I2C_Write(char);                                           /* I2C write function */
char I2C_Read_Ack();                                                /* I2C read ack function */
char I2C_Read_Nack();                                               /* I2C read nack function */


#endif                                                                          /*
I2C_MASTER_H_FILE_H_ */
```

3. DEVELOPED MODIFIED CODE OF TASK 2/A from TASK 1/A

Insert only the modified sections here

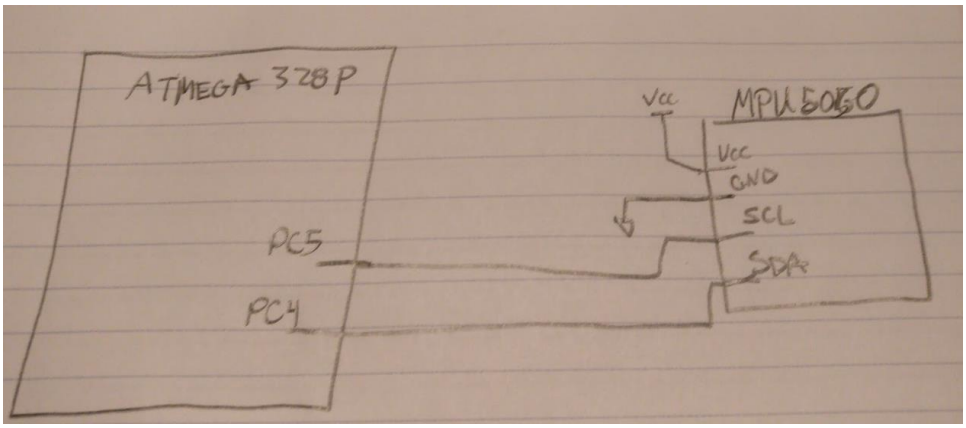
```
/*
* DA6.c
*
* Created: 12/11/2019 6:29:29 PM
* Author : Dilbert
*/

#ifndef F_CPU
#define F_CPU 16000000UL
#endif

#include <avr/io.h>
#include <util/delay.h>
#include <math.h>
#include <stdlib.h>                                                                     /*
Include standard library file */
#include <stdio.h>                                                                      /*
Include standard library file */
```

```
#include <inttypes.h>
#include "MPU6050_def.h"                                        /* Include MPU6050
register define file */
#include "i2c_master.h"                                         /* Include I2C Master
header file */
#include "uart.h"                                             /* Include USART header file
*/

#define MPU6050_WRITE 0xD0
#define MPU6050_READ 0xD1

float Acc_x,Acc_y,Acc_z,Gyro_x,Gyro_y,Gyro_z;

void init_uart(uint16_t baudrate){

uint16_t UBRR_val = (F_CPU/16)/(baudrate-1);

UBRR0H = UBRR_val >> 8;
UBRR0L = UBRR_val;

UCSR0B |= (1<<TXEN0) | (1<<RXEN0) | (1<<RXCIE0); // UART TX (Transmit - senden) einschalten
UCSR0C |= (1<<USBS0) | (3<<UCSZ00); //Modus Asynchron 8N1 (8 Datenbits, No Parity, 1 Stopbit)
}

void uart_putc(unsigned char c){

while(!(UCSR0A & (1<<UDRE0))); // wait until sending is possible
UDR0 = c; // output character saved in c
}

void uart_puts(char *s){
while(*s){
uart_putc(*s);
s++;
}
}

void init_MPU6050()/* Gyro initialization function */
{
_delay_ms(150);/* Power up time >100ms */
I2C_Start_Wait(0xD0);/* Start with device write address */
I2C_Write(SMPLRT_DIV);/* Write to sample rate register */
I2C_Write(0x07);/* 1KHz sample rate */
I2C_Stop();
I2C_Start_Wait(0xD0);
I2C_Write(PWR_MGMT_1);/* Write to power management register */
I2C_Write(0x01);/* X axis gyroscope reference frequency */
I2C_Stop();
I2C_Start_Wait(0xD0);
I2C_Write(CONFIG);/* Write to Configuration register */
I2C_Write(0x00);/* Fs = 8KHz */
I2C_Stop();
I2C_Start_Wait(0xD0);
I2C_Write(GYRO_CONFIG);/* Write to Gyro configuration register */
I2C_Write(0x18);/* Full scale range +/-2000 degree/C */
I2C_Stop();
I2C_Start_Wait(0xD0);
```

```
I2C_Write(INT_ENABLE);/* Write to interrupt enable register */
I2C_Write(0x01);
I2C_Stop();
}

void MPU_Start_Loc(){
I2C_Start_Wait(0xD0);/* I2C start with device write address */
I2C_Write(ACCEL_XOUT_H);/* Write start location address from where to read */
I2C_Repeated_Start(0xD1);/* I2C start with device read address */
}

void Read_RawValue(void){
_delay_ms(10);
MPU_Start_Loc();/* Read Gyro values */
Acc_x=(((int)I2C_Read_Ack()<<8)|(int)I2C_Read_Ack());//.. Read other registers
Acc_y=(((int)I2C_Read_Ack()<<8)|(int)I2C_Read_Ack());
Acc_z=(((int)I2C_Read_Ack()<<8)|(int)I2C_Read_Ack());
Gyro_x=(((int)I2C_Read_Ack()<<8)|(int)I2C_Read_Ack());
Gyro_y=(((int)I2C_Read_Ack()<<8)|(int)I2C_Read_Ack());
Gyro_z=(((int)I2C_Read_Ack()<<8)|(int)I2C_Read_Nack());
I2C_Stop();
}



int main(void){
char buffer[20], float_[10];
float Xa,Ya,Za,Xg,Yg,Zg;

I2C_Init();
init_MPU6050();
init_uart(9600);
_delay_ms(150);
while(1){
_delay_ms(150);
Read_RawValue();
Xa = Acc_x/16384.0;                                                    /* Divide raw value by
sensitivity scale factor to get real values */
Ya = Acc_y/16384.0;
Za = Acc_z/16384.0;
Xg = Gyro_x/16.4;
Yg = Gyro_y/16.4;
Zg = Gyro_z/16.4;

dtostrf( Xa, 3, 2, float_ );                                 /* Take values in buffer to send all
parameters over USART */
sprintf(buffer,"%s, Xa \n ",float_);
USART_SendString(buffer);

dtostrf( Ya, 3, 2, float_ );                                 /* Take values in buffer to send all
parameters over USART */
sprintf(buffer,"%s, Ya \n",float_);
USART_SendString(buffer);

dtostrf( Za, 3, 2, float_ );                                 /* Take values in buffer to send all
parameters over USART */
sprintf(buffer,"%s, Za \n",float_);
USART_SendString(buffer);
```

```
dtostrf( Xg, 3, 2, float_ );                    /* Take values in buffer to send all
parameters over USART */
sprintf(buffer,"%s, Xg \n",float_);
USART_SendString(buffer);

dtostrf( Yg, 3, 2, float_ );                    /* Take values in buffer to send all
parameters over USART */
sprintf(buffer,"%s, Yg \n",float_);
USART_SendString(buffer);

dtostrf( Zg, 3, 2, float_ );                    /* Take values in buffer to send all
parameters over USART */
sprintf(buffer,"%s, Zg \n",float_);
USART_SendString(buffer);

_delay_ms(1000);
}

return 0;
}
```

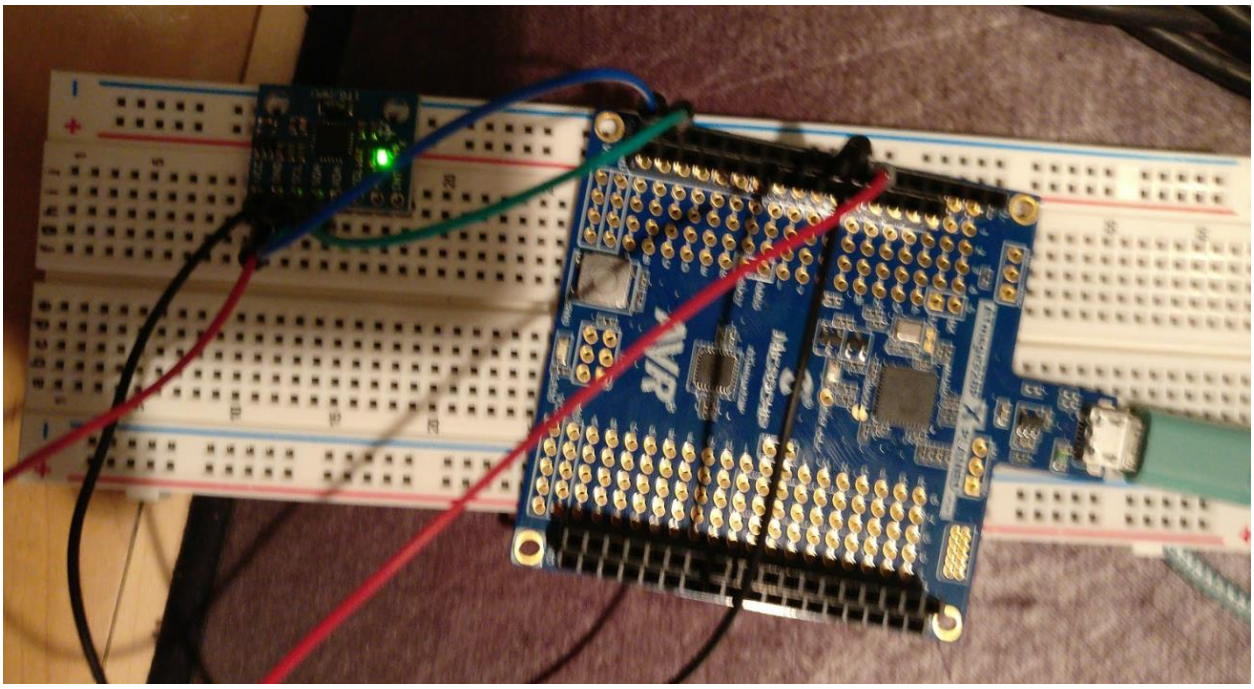
4. SCHEMATICS



5. SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)

```
 -0.11, Ya
1.11, Za
-335.61, Xg
1.04, Yg
-1.40, Zg
-0.05, Xa
 -0.03, Ya
1.13, Za
-335.61, Xg
1.83, Yg
-0.85, Zg
-0.06, Xa
 -0.02, Ya
1.11, Za
-335.61, Xg
1.89, Yg
-1.46, Zg
-0.05, Xa
 -0.02, Ya
1.11, Za
-337.56, Xg
1.77, Yg
-1.40, Zg
```

6. SCREENSHOT OF EACH DEMO (BOARD SETUP)



7. VIDEO LINKS OF EACH DEMO

https://youtu.be/dgFClPIDLtw

8. GITHUB LINK OF THIS DA

https://github.com/Dil-bert/Alabaster/tree/master/DesignAssignments/DA6/DA6

Student Academic Misconduct Policy
http://studentconduct.unlv.edu/misconduct/policy.html

"*This assignment submission is my own, original work*".
NAME OF THE STUDENT