

http://192.168.0.100

admin password: TVETadmin user password: TVETuser

I/O Mapping:

S1 --> IN1 S2 --> IN2 S3 --> IN3 S4 --> IN4 S5 --> IN5 S6 --> IN6

OUTPUT CONTROL

Switch Inputs 1 & 2 to various positions and observe Output 1's counter behaviour. - Inputs 1 & 2 are required to energize Output1.

Up/Down counters are used to display values on the TDE Display.

Wire the Input Actuators (S1-6) to the corresponding numbers Demonstration of the available OR gates on a LOGO!

of the LOGO! inputs (IN1-6).

Complete the 2-input OR Gate Truth Table and identify the control bit

Q: what bit is controlling the output?)

NOR Gate:

- Inputs 3 & 4 are required to energize Output2.

Switch Inputs 3 & 4 to various positions and observe Output 2's counter behaviour. Complete the NOR Gate Truth Table and identify the control bit

Q: what bit is controlling the output?

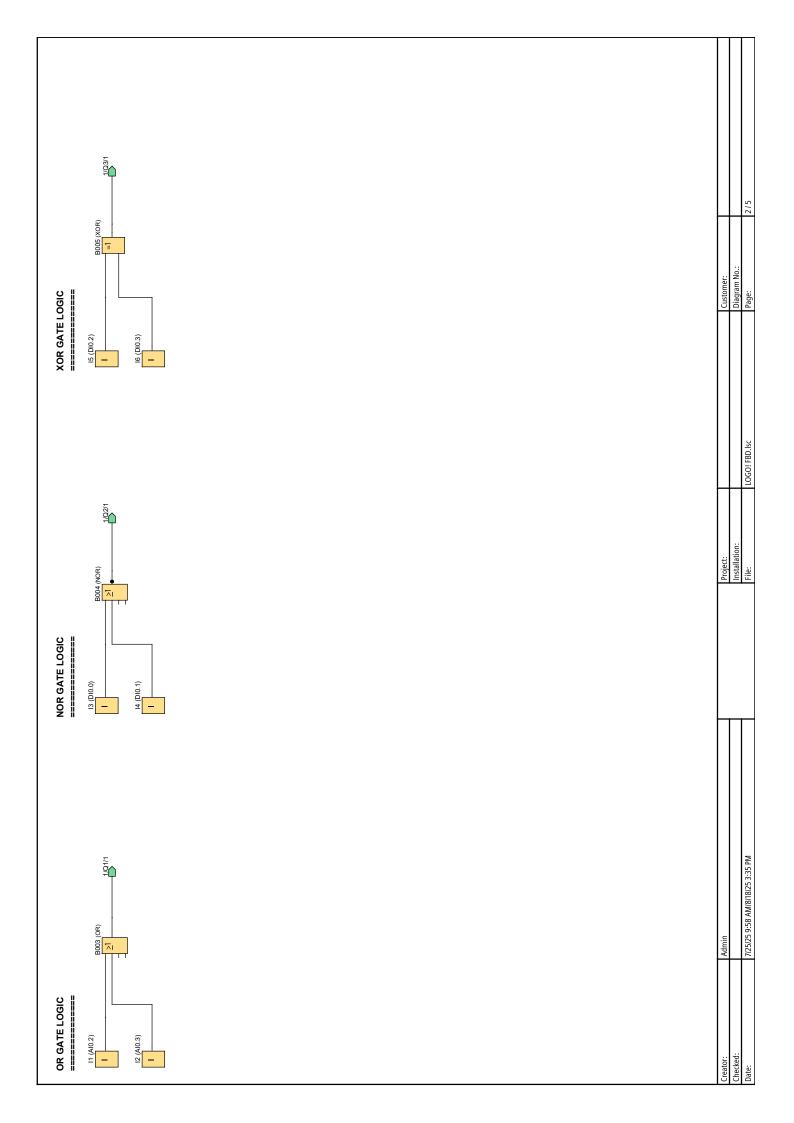
- Inputs 6 & 7 are required to energize Output3.

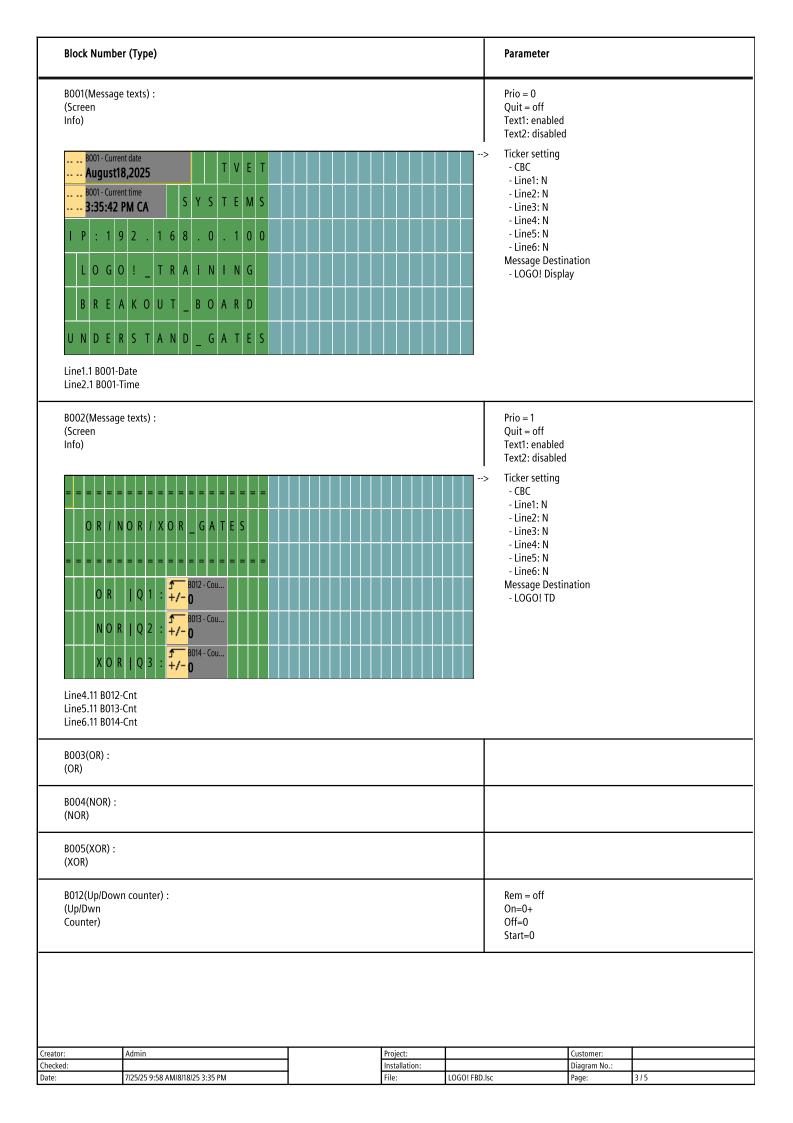
Switch Inputs 6 & 7 to various positions and observe Output 3's counter behaviour. Complete the XOR Gate Truth Table and identify the control bit

Q: what bit is controlling the output?)

| B012 (Up/Dwn | E | Start=0 | | | | | |
|--------------|---------------------------|-----------------------------|-------------------------------|------------|------------|------------|------------|
| | B013 (Up/Dwn cnd fraging) | Per | Start=0 | | | | |
| | | B014 (Up/Dwn Cal 1 Counter) | Rem = off On=0+ Off=0 Start=0 | | | | |
| Q (DQ0.0) | Q2 (DQ0.1) | Q3 (DQ0.2) | Q4 (DQ0.3) | as (ba1.0) | a6 (Da1.1) | Q7 (DQ1.2) | Q8 (DQ1.3) |
| | | | Disable unused outputs | | | | |
| 2/B003 | 2/B004 | 2/B005 | Disable unu |] | | | |

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| Block Number (Type) | | | Paramete | r | |
|--|---|----------|--|-----------|-----|
| B013(Up/Down counter) : (Up/Dwn Counter) | | | Rem = off On=0+ Off=0 Start=0 | | |
| B014(Up/Down counter) : (Up/Dwn Counter) | | | Rem = off On=0+ Off=0 Start=0 | | |
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| Date: | 7/25/25 9:58 AM/8/18/25 3:35 PM | File: | LOGO! FBD.lsc | Page: | 515 |