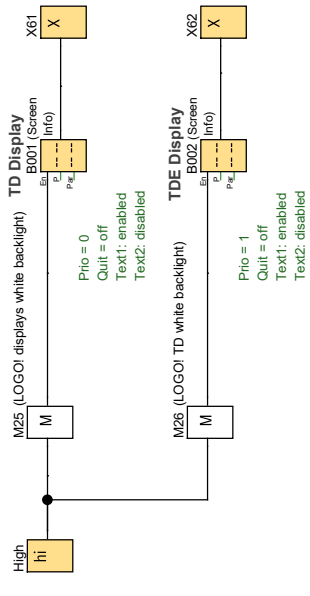
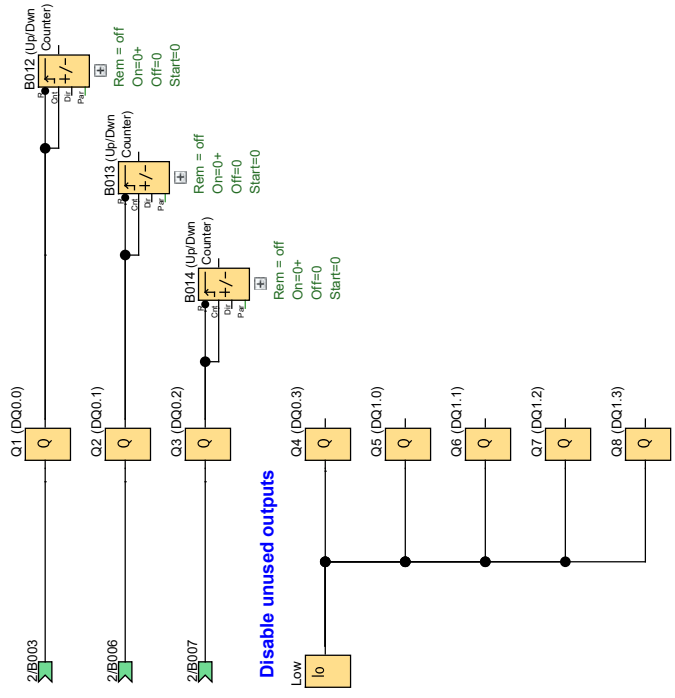


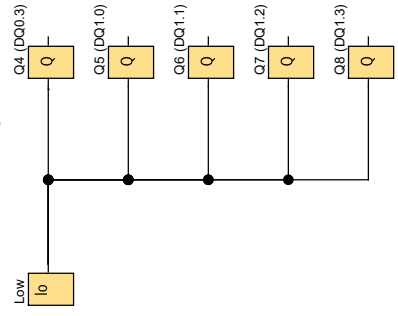
SCREEN CONTROL



OUTPUT CONTROL



Disable unused outputs



Web Info:
http://192.168.0.100
user password: TVETuser
admin password: TVETadmin

I/O Mapping:
S1 ---> IN1
S2 ---> IN2
S3 ---> IN3
S4 ---> IN4
S5 ---> IN5

Project Summary:
Demonstration of the available OR gates on a LOGO!
Wire the Input Actuators (S1-5) to the corresponding numbers of the LOGO! inputs (IN1-5).
Up/Down counters are used to display values on the TDE Display.

NOT Gate:
- Inputs 1 is required to energize Output1.
Switch Input 1 ON & OFF and observe Output 1's counter behaviour.
Note how the NOT Gate inverts the input signal.

AND Gate with NOT Gate:
- Inputs 2 & 3 are required to energize Output2.
Switch Inputs 2 & 3 to various positions and observe Output 2's counter behaviour.
Q: How can the same result be achieved without the NOT Gate?

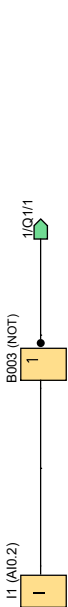
OR Gate with NOT Gate:
- Inputs 4 & 5 are required to energize Output3.
Switch Inputs 4 & 5 to various positions and observe Output 3's counter behaviour.
Q: How can the same result be achieved without the NOT Gate?

Creator:	Admin	Project:		Customer:	
Checked:		Installation:		Diagram No.:	
Date:	7/25/25 9:58 AM/8/18/25 4:23 PM	File:	LOGO! FBD.lsc	Page:	1/5

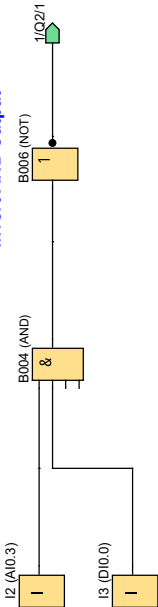
NOT GATE LOGIC

=====

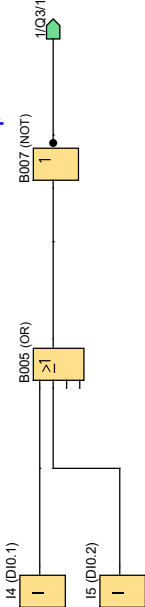
Invert single input



Invert AND output



Invert OR output



Creator:	Admin	Project:		Customer:	
Checked:		Installation:		Diagram No.:	
Date:	7/25/25 9:58 AM/8/18/25 4:23 PM	File:	LOGOI FBD.lsc	Page:	2 / 5

Connection	Label
I1	AI0.2
I2	AI0.3
I3	DI0.0
I4	DI0.1
I5	DI0.2
M25	LOGO! displays white backlight
M26	LOGO! TD white backlight
Q1	DQ0.0
Q2	DQ0.1
Q3	DQ0.2
Q4	DQ0.3
Q5	DQ1.0
Q6	DQ1.1
Q7	DQ1.2
Q8	DQ1.3
X61	
X62	