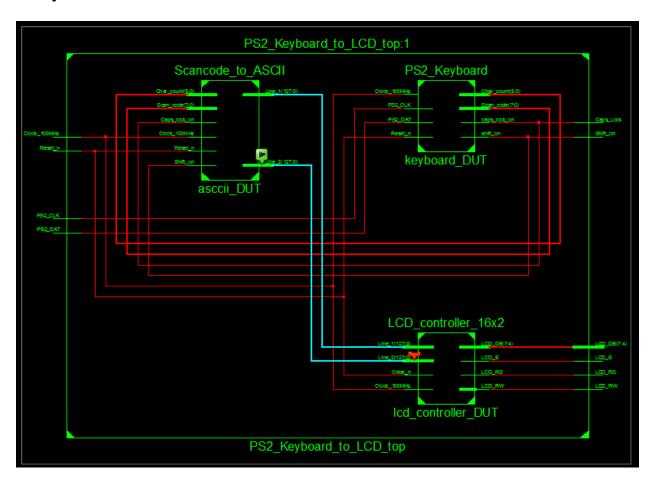
1. Simulation

2. Synthesis



3. Implementation

