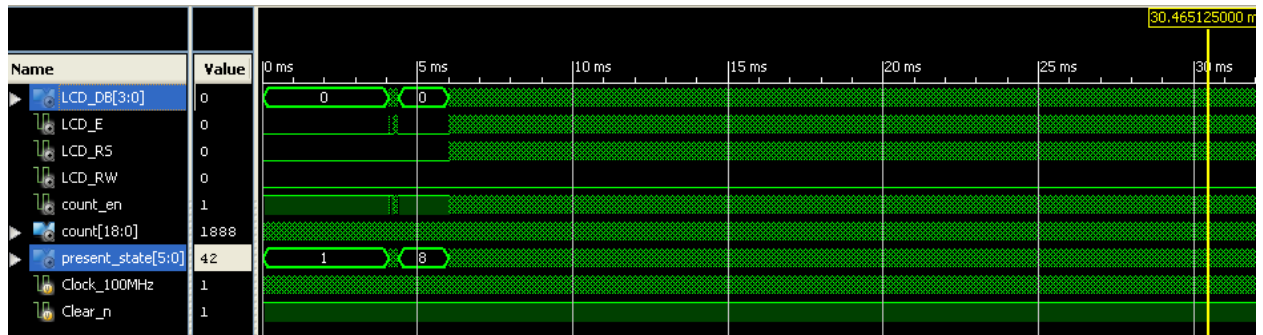
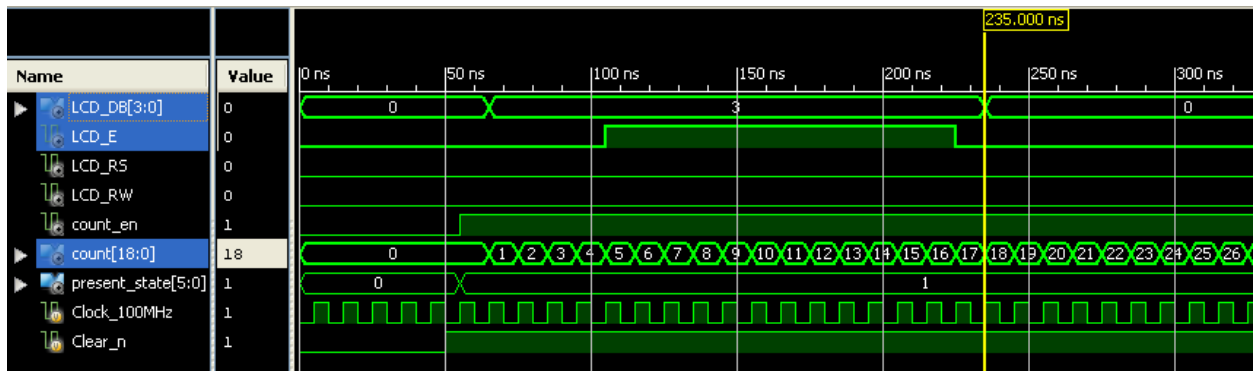


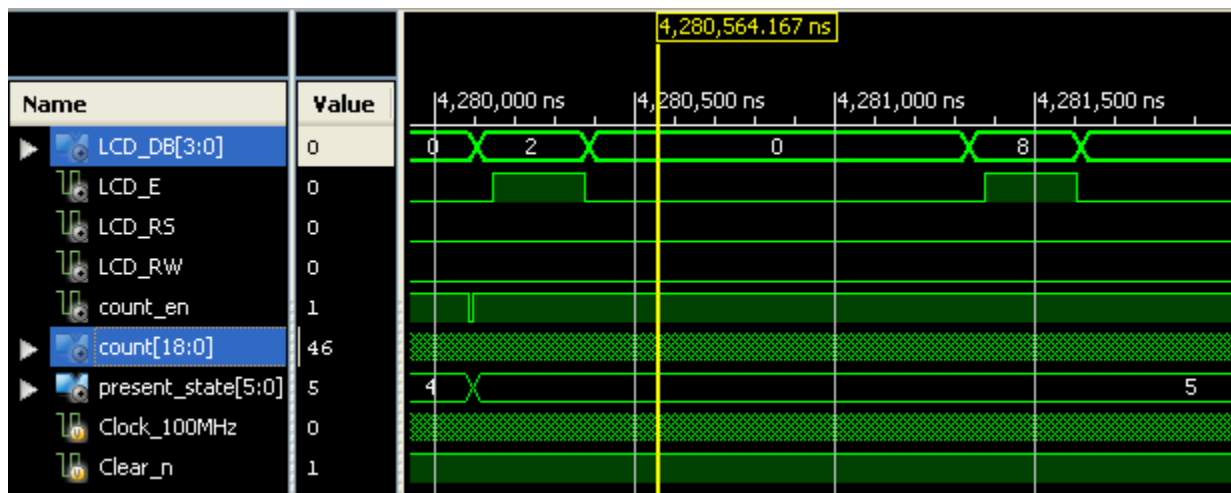
1. Simulation



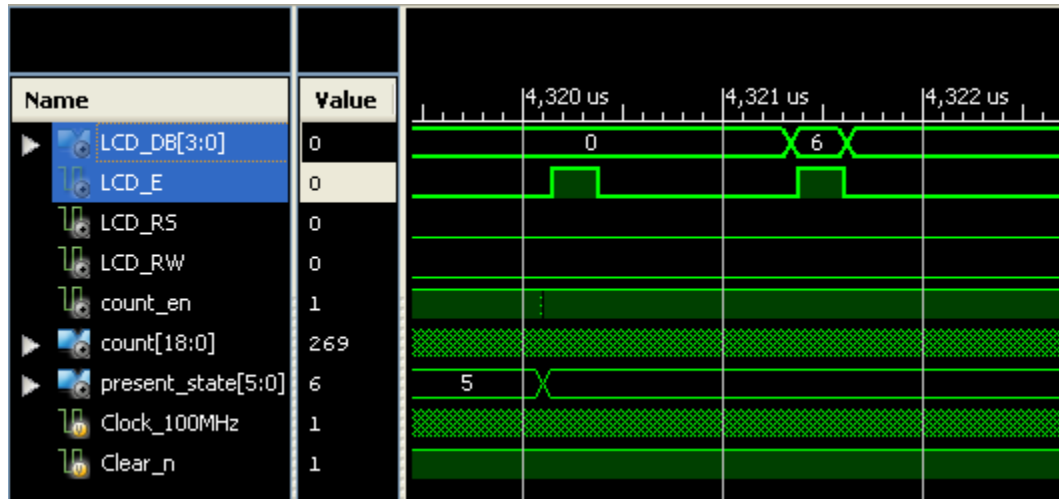
1.1. Power Init_1



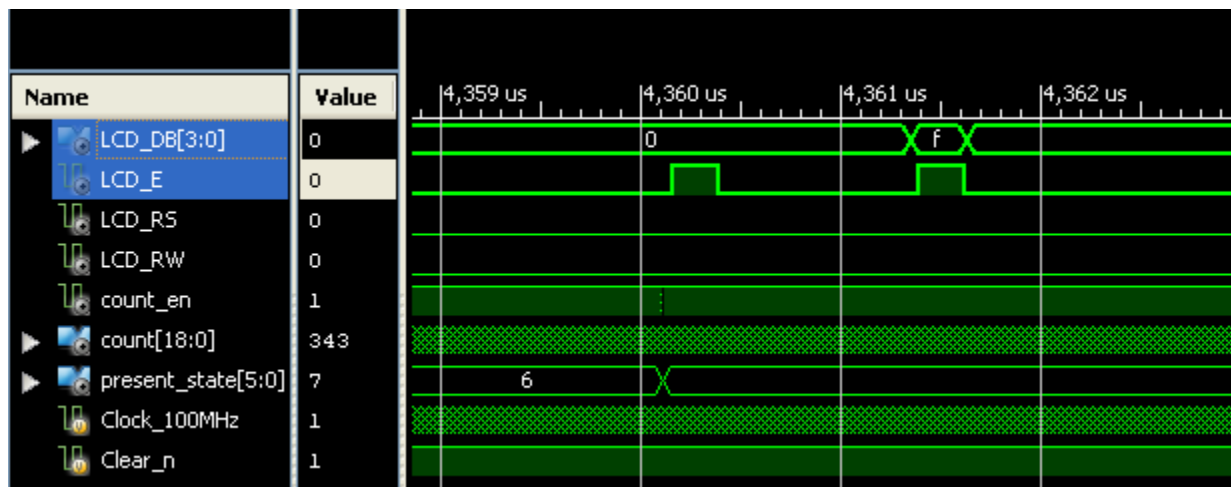
1.2. FUNCTION_SET



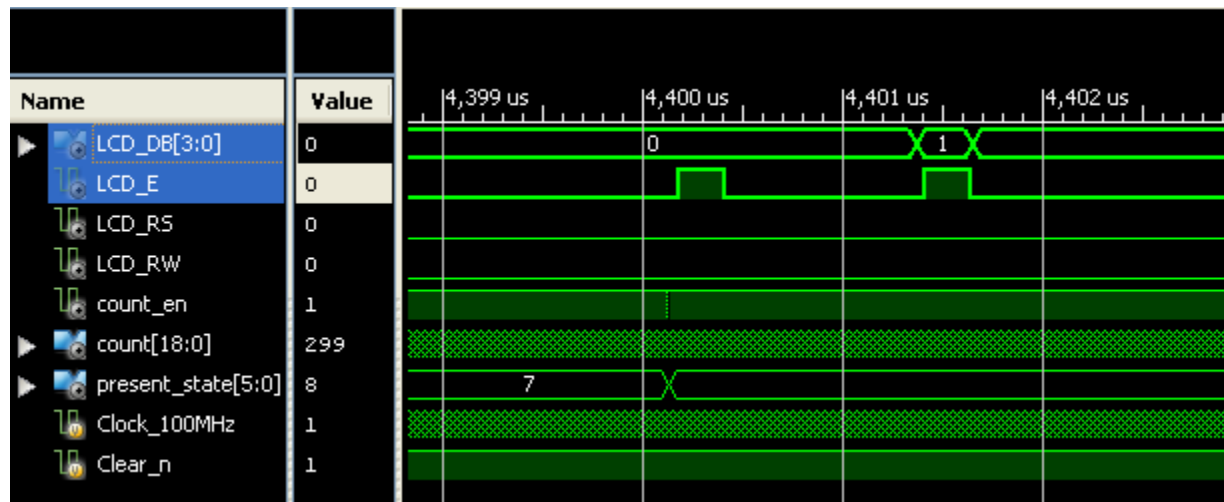
1.3. ENTRY_MODE_SET



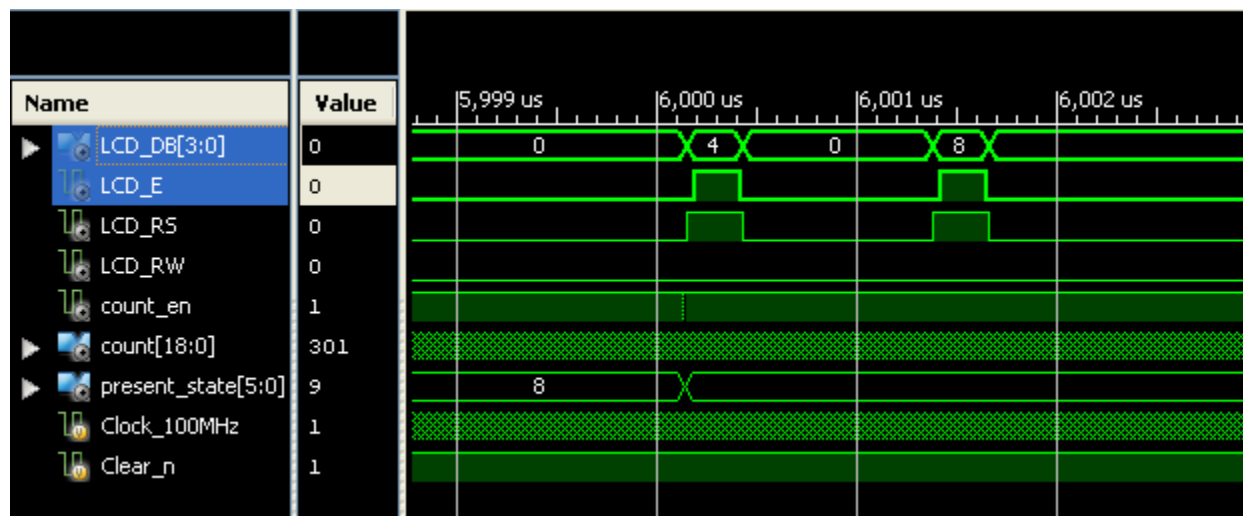
1.4. DISPLAY_ON_OFF



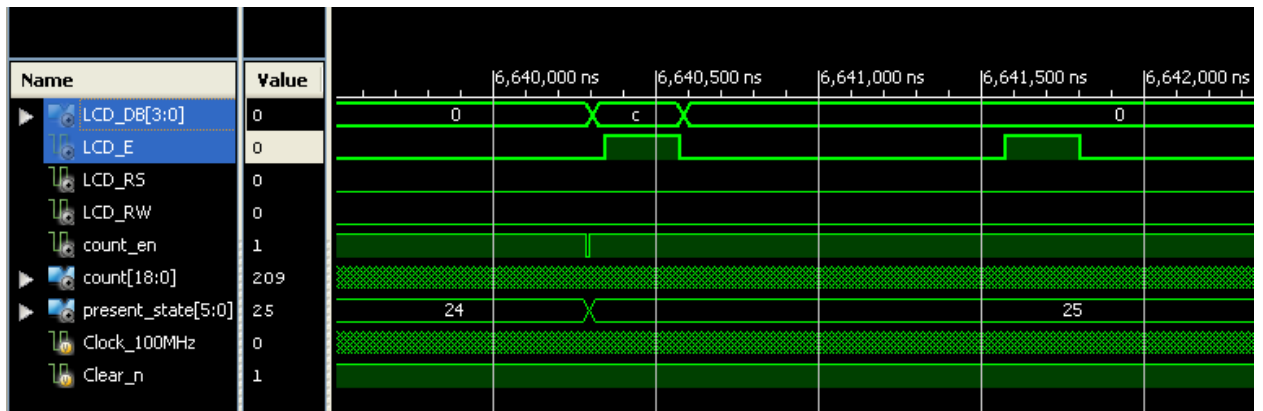
1.5. CLEAR_DISPLAY



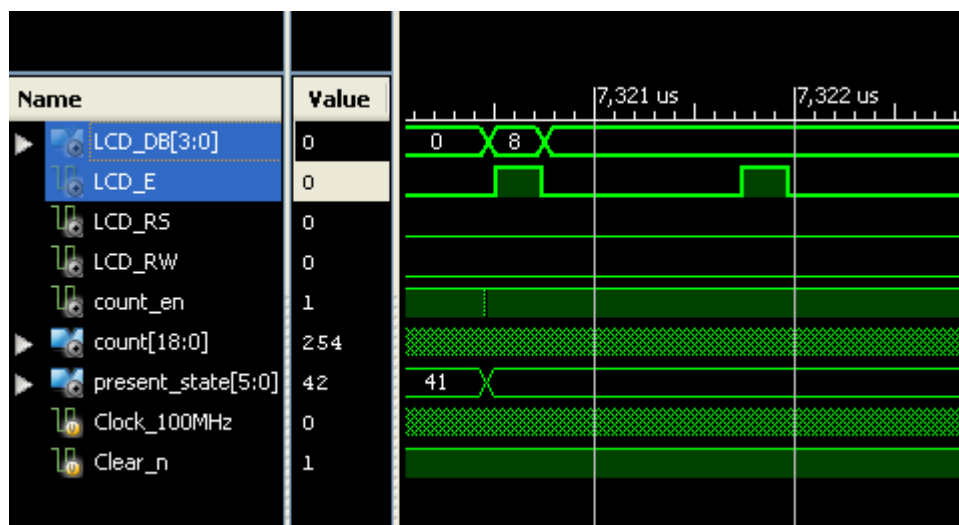
1.6. Char_16x1



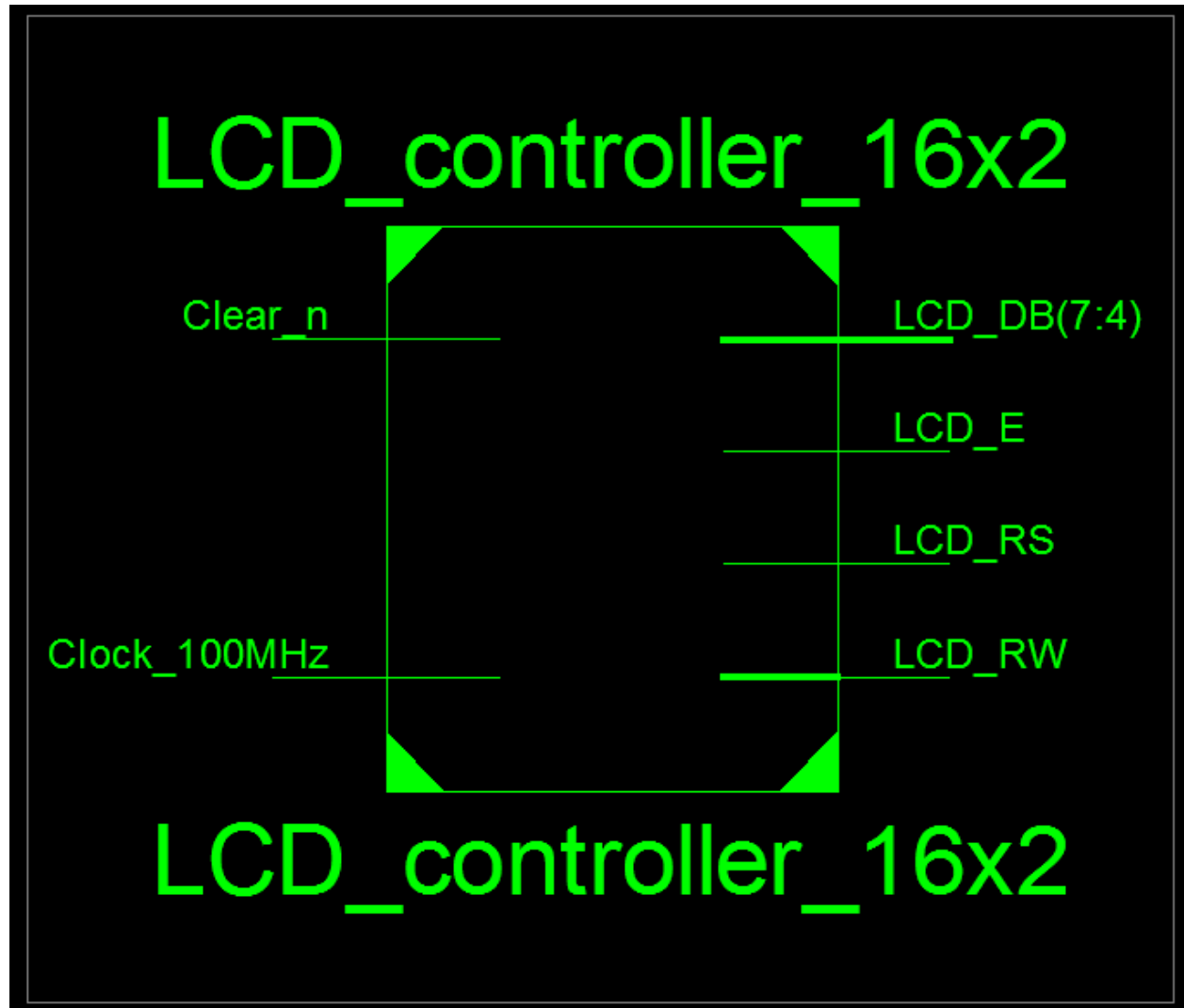
1.7. DD_RAM_ADDR_40

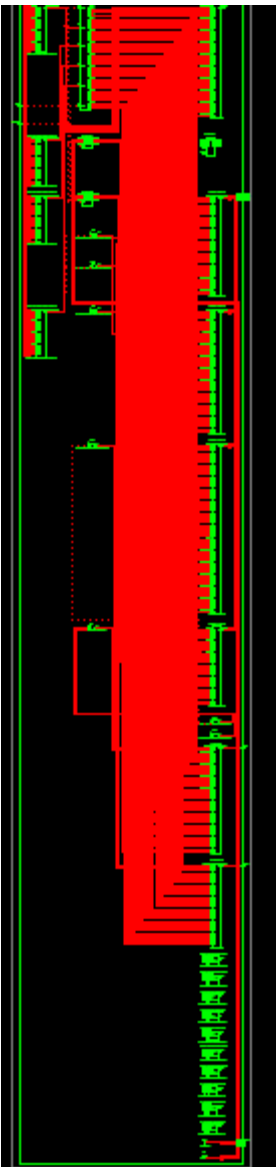


1.8. DD_RAM_ADDR_00



2. Synthesis





3. Implementation

