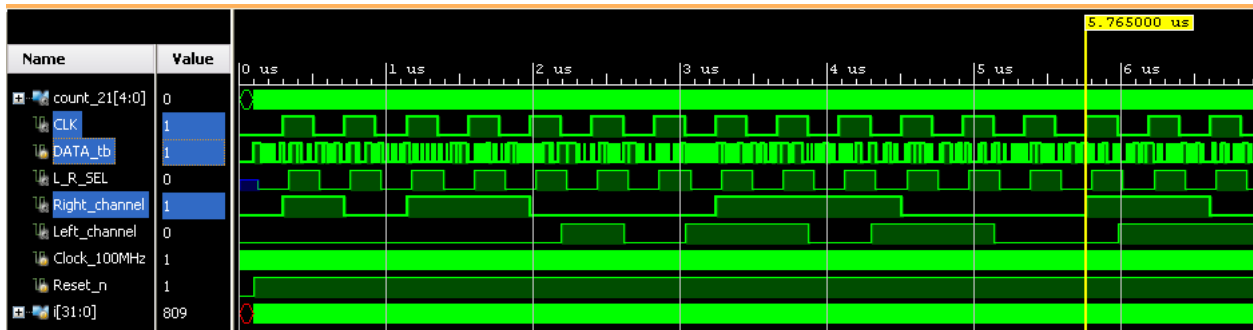
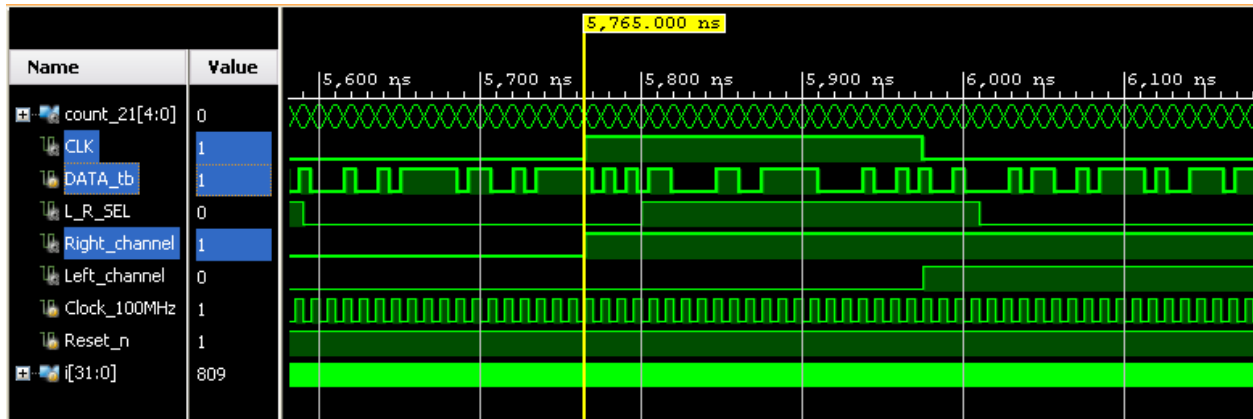


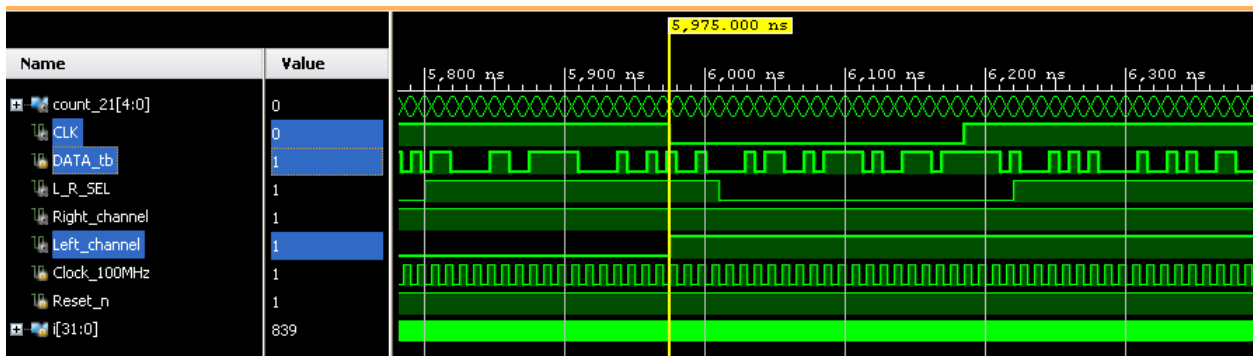
1. Simulation



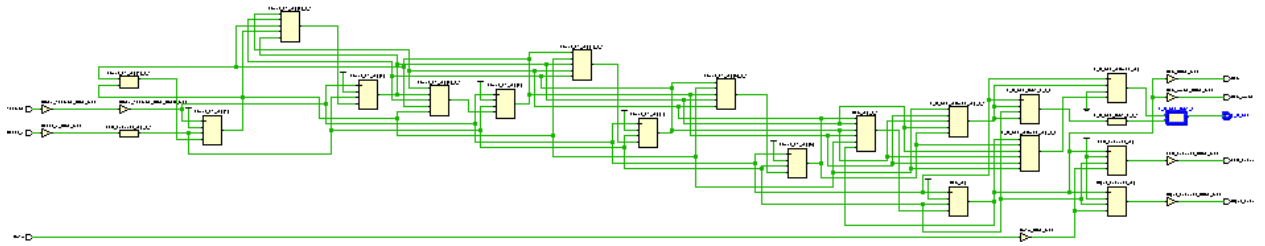
Right channel



Left channel



2. Synthesis



3. Implementation