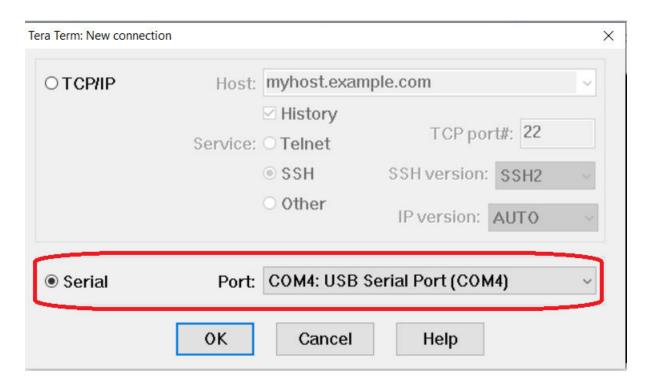
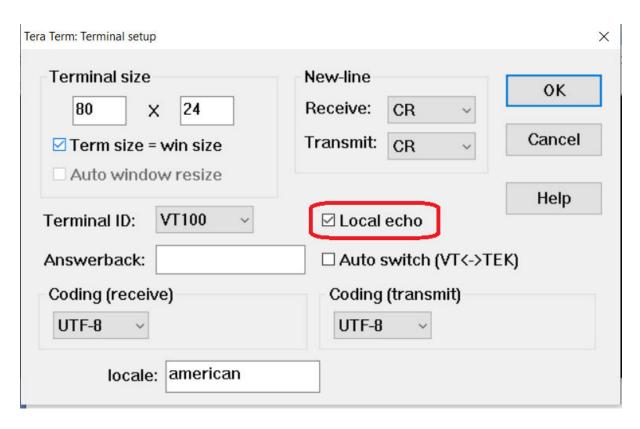
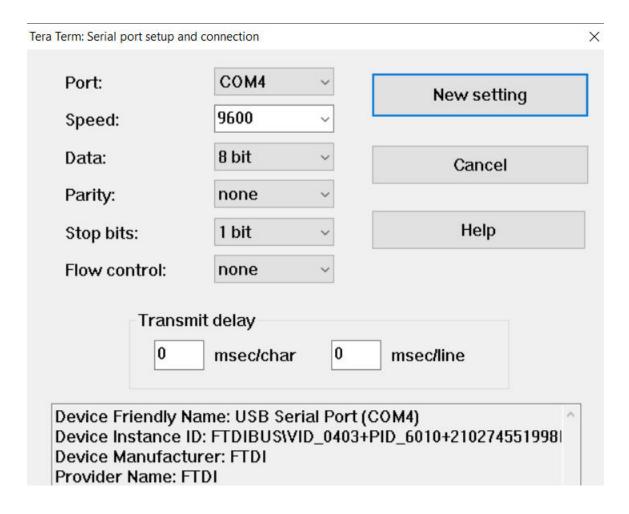
#### 1. Open Tera Term



## 1.1. Tera Term $\rightarrow$ Setup $\rightarrow$ Terminal

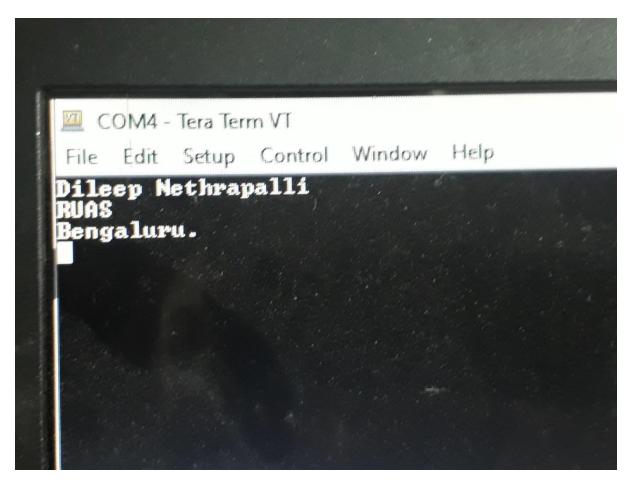


## 1.2 . Tera Term → Setup → Serial Port



# 2. Implementation

# 2.1. FPGA to Computer



### 2.2. Computer to FPGA

