Assignment#3 CS207 Fall 2023

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PROBLEM 1. Analyze the sequential circuit with two JK flip-flops A and B with two inputs x and y and one output z. The input equation and output equation is given.

SOLUTION. The state equation for the circuit is given by

$$A(t+1) = J_A(t)A' + K'_A(t)A = A'Bx + A'B'y' + A(B'xy')'$$

$$= A'Bx + A'B'y' + A(B+x'+y)$$

$$= A'Bx + A'B'y' + AB + Ax' + Ay$$

$$= \Sigma(0, 2, 6, 7, 8, 9, 10, 12, 13, 14, 15)$$

$$B(t+1) = J_B(t)B' + K'_B(t)B = A'B'x + B(A+xy')'$$

= $A'B'x + A'Bx'y$
= $\Sigma(2, 3, 4, 5, 7)$

and the output equation is given by

$$z(t) = Ax'y' + Bx'y' = \Sigma(4, 8, 12)$$

So the state table is

	sent ate	Inj	out		ext ate	Output		JKFF	Inpu	t
A	В	x	y	A	B	z	J_A	K_A	J_B	K_B
0	0	0	0	1	0	0	1	0	0	0
0	0	0	1	0	0	0	0	0	0	0
0	0	1	0	1	1	0	1	1	1	1
0	0	1	1	0	1	0	0	0	1	0
0	1	0	0	0	1	1	0	0	0	0
0	1	0	1	0	1	0	0	0	0	0
0	1	1	0	1	0	0	1	0	1	1
0	1	1	1	1	1	0	1	0	1	0
1	0	0	0	1	0	1	1	0	0	1
1	0	0	1	1	0	0	0	0	0	1
1	0	1	0	1	0	0	1	1	0	1
1	0	1	1	0	0	0	0	0	0	1
1	1	0	0	1	0	1	0	0	0	1
1	1	0	1	1	0	0	0	0	0	1
1	1	1	0	1	0	0	1	0	0	1
1	1	1	1	1	0	0	1	0	0	1

and the state diagram and timing diagram is

Figure 1: State Diagram 01/010/0 00 11 11/0 00/101/0 11/0*X*1/0 10/0 11/000/001 10 10/0 00/100/101/001/010/0

Figure 2: Timing Diagram

CLK

A #

B #

x #

y #

z #

PROBLEM 2. Analyze the sequential circuit with two TFFs A and B.

SOLUTION. From the diagram, we can derive the input equations

$$T_A = A + B$$

$$T_B = A' + B$$

and there's no output equation. So the state equation would be

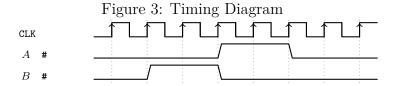
$$A(t+1) = T_A \oplus Q_A = (A+B) \oplus A$$

$$B(t+1) = T_B \oplus Q_B = (A'+B) \oplus B$$

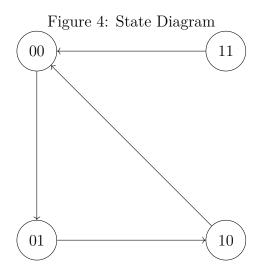
and the state table is

Table 2: State Table							
Pres Sta		Ne	${ m ext}$				
A	В	\overline{A}	В				
0	0	0	1				
0	1	1	0				
1	0	0	0				
1	1	0	0				

the fourth state is useless. And the timing diagram and state diagram is



PROBLEM 3. For the block diagram, find the state table and state diagram.



SOLUTION. a) The input functions are

$$J_1 = X$$

$$K_1 = (Q'_2 X)'$$

$$J_2 = X$$

$$K_2 = (Q_1 X)'$$

and the state equation is

$$Q_1(t+1) = J_1Q'_1 + K'_1Q_1 = Q'_1X + Q_1Q'_2X$$

$$Q_2(t+1) = J_2Q'_2 + K'_2Q_2 = Q'_2X + Q_1Q_2X$$

$$Q'_2(t+1) = (X'+Q_2)(Q'_1 + Q'_2 + X')$$

$$= X' + Q_2(Q'_1 + Q'_2) = X' + Q'_1Q_2$$

and the output function is

$$F = X \oplus Q_2(t+1)' = X \oplus (X' + Q_1'Q_2)$$

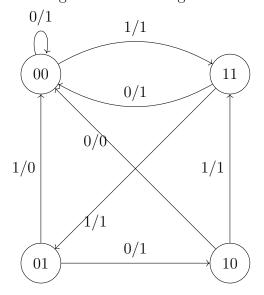
so the state table is

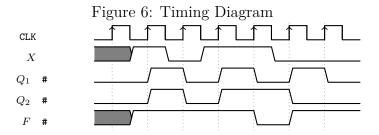
b) It's a Mealy Machine since the output F is determined by the input X and the state.

П	$\Gamma_0 L L$. 2.	State	Tab	10
	i a.ni	e 3:	State	Tan	1e

Present State		Input	Next State		Output		JKFF	Inpu	t
$Q_1(t)$	$Q_2(t)$	X	$Q_1(t+1)$	$Q_2(t+1)$	F	J_1	K_1	J_1	K_1
0	0	0	0	0	1	0	1	0	1
0	0	1	1	1	1	1	0	1	1
0	1	0	1	0	1	0	1	0	1
0	1	1	0	0	0	1	1	1	1
1	0	0	0	0	1	0	1	0	1
1	0	1	1	1	1	1	0	1	0
1	1	0	0	0	1	0	1	0	1
1	1	1	0	1	1	1	1	1	0

Figure 5: State Diagram





c) The timming diagram based on the state table is

PROBLEM 4. Obtain the simplified input equations for a sequential circuit with TFF and the state diagram is given.

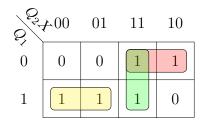
SOLUTION. From the state diagram, we can derive the state table so the

Table 4: State Table Next Present Input State State $Q_1(t)$ $Q_2(t)$ X $Q_1(t+1)$ $Q_2(t+1)$

state equations are

$$Q_1(t+1) = T_1 \oplus Q_1(t) = \Sigma(2,3,4,5,7)$$
$$Q_2(t+1) = T_2 \oplus Q_2(t) = \Sigma(0,2,4,7)$$

and the K-Maps for each TFF are



S. S.	⊁ 00	01	11	10	
0	1	0	0	1	
1	1	0	1	0	

So the simplified state equations are

$$Q_1(t+1) = Q_1'Q_2 + Q_2X + Q_1Q_2'$$

$$= Q_1 \oplus (Q_1 \oplus (Q_1'Q_2 + Q_2X + Q_1Q_2'))$$

$$= Q_1 \oplus (Q_1'Q_2 + Q_1Q_2X')$$

$$Q_2(t+1) = Q_2'X' + Q_1'X' + Q_1Q_2X$$

$$= Q_2 \oplus (Q_2 \oplus (Q_2'X' + Q_1'X' + Q_1Q_2X))$$

$$= Q_2 \oplus (Q_1Q_2X' + Q_1'Q_2X)$$

So the input equations are

$$T_1 = Q_1'Q_2 + Q_1Q_2X'$$
$$T_2 = Q_1Q_2X' + Q_1'Q_2X$$

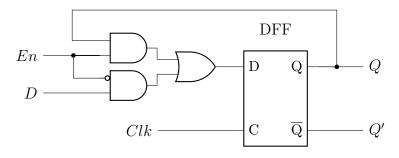
PROBLEM 5. Design a DFF with enable input whose function table is given.

SOLUTION. From the characteristic table, we can derive the input equation

\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \	> 00	01	11	10
0	0	1	1	0
1	0	0	1	1

$$Q(t) = Q(t+1)En' + DEn$$

so the circuit is



PROBLEM 6. Design a certain filp-flop according the following description.

SOLUTION. a) The characteristic table is

A	B	Q(t+1)	Q(t+1)'	
0	0	0	1	clear to 0
0	1	Q(t)	Q'(t)	no change
1	0	Q'(t)	Q(t)	complement
1	1	1	0	set to 1

b) From the characteristic table, we can derive the K-maps first

0/3/3	> 00	01	11	10
0	0	0	1	1
1	0	1	1	0

so the simplified equation is

$$Q(t+1) = AQ'(t) + BQ(t)$$

c) The excitation table is

Q(t)	Q(t+1)	A	B	Operation
0	0	0	X	no change
0	1	1	X	set to 1
1	0	X	0	clear to 1
1	1	X	1	no change

d) From the characteristic equation, we have

$$Q(t+1) = AQ'(t) + (B')'Q(t)$$

that is, we can substitude the inputs with JKFF's inputs

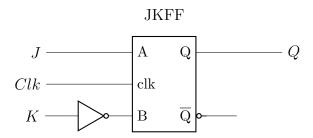
$$Q(t+1) = JQ'(t) + K'Q(t) \Rightarrow J = A \qquad K = B'$$

which is indicates the input equations of our flip-flop

$$A = J$$

$$B = K'$$

so the block diagram is

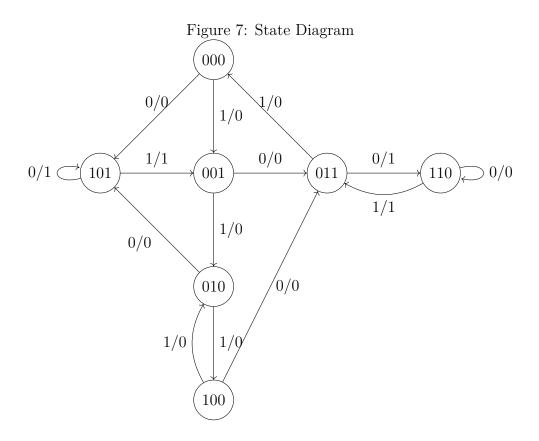


PROBLEM 7. For the following state table, simplify the state table and draw the state diagram. Then design the sequential circuit using JKFF.

SOLUTION. To simplify the state, we first notice that the state of h is the same as state d, so we can remove the state h and rewrite the transition to h into d. Then we assign codes to the states and obtain the state table

Present State	Next State		Out	put
ABC	x = 0	x = 1	x = 0	x = 1
000	101	001	0	0
001	011	010	0	0
010	101	100	0	0
011	110	000	1	0
100	011	010	0	0
101	101	010	1	1
110	110	011	0	1
111	X	X	X	X

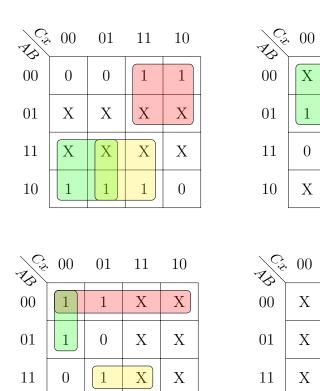
and the coresponding state diagram is



Next we obtain the state equations and output equation using the state table and K-map

A Ca	. 00	01	11	10
00	1	0	0	0
01	1	1	0	1
11	X	X	X	X
10	X	X	X	X

AS CS	. 00	01	11	10
00	X	X	X	X
01	X	X	X	X
11	0	1	X	X
10	1	1	1	0



Χ

X

1

0

10

Figure 8: Output K-Map

01

X

1

0

X

01

X

X

X

Χ

X

10

11

X

1

X

X

11

1

1

1

10

X

0

X

X

10

0

1

Χ

0

AS Ca	. 00	01	11	10
00	0	0	0	0
01	0	0	0	1
11	0	1	X	X
10	0	0	1	1

Table 6: State Table

	reser State		Input		Next State			J	IKFF	Inpu	t	
\overline{A}	B	C	\overline{x}	\overline{A}	B	C	J_A	K_A	J_B	K_B	J_C	K_C
0	0	0	0	1	0	1	1	X	0	X	1	X
0	0	0	1	0	0	1	0	X	0	X	1	X
0	0	1	0	0	1	1	0	X	1	X	X	0
0	0	1	1	0	1	0	0	X	1	X	X	1
0	1	0	0	1	0	1	1	X	X	1	1	X
0	1	0	1	1	0	0	1	X	X	1	0	X
0	1	1	0	1	1	0	1	X	X	0	X	1
0	1	1	1	0	0	0	0	X	X	1	X	1
1	0	0	0	0	1	1	X	1	1	X	1	X
1	0	0	1	0	1	0	X	1	1	X	0	X
1	0	1	0	1	0	1	X	0	0	X	X	0
1	0	1	1	0	1	0	X	1	1	X	X	1
1	1	0	0	1	1	0	X	0	X	0	0	X
1	1	0	1	0	1	1	X	1	X	0	1	X
1	1	1	X	X	X	X	X	X	X	X	X	X

so the input equations are

$$J_A = C'x' + BC' + BCx'$$

$$K_A = Ax + AB'C'$$

$$J_B = A'C + AC' + Ax$$

$$K_B = A'C'Cx$$

$$J_C = A'B' + A'C'x' + ABx + AB'x'$$
$$K_C = Cx + BC$$

and the output equation is

$$y = AC + ABx + BCx'$$

and the state equations are

$$A(t+1) = J_A A' + K'_A A$$

$$= A'(C'x' + BC' + BCx') + A(Ax + AB'C')'$$

$$= A'C'x' + A'BC' + A'BCx' + ABx' + ACx'$$

$$B(t+1) = J_B B' + K'_B B$$

$$= B'(A'C + AC' + Ax) + A(A'C'Cx)'$$

$$= A + A'B'C$$

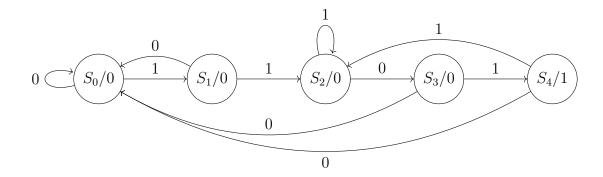
$$C(t+1) = J_C C' + K'_C C$$

$$= C'(A'B' + A'C'x' + ABx + AB'x') + C(Cx + BC)'$$

$$= A'B'C' + A'C'x' + ABC'x + AB'C'x' + BCx$$

PROBLEM 8. Design a sequence detector using DFF to recognize the occurrence of bits 1101 with Moore machine in overlapping mode.

SOLUTION. According to the description, we can draw the state diagram



and the state table is

Table 7: State Table

Present State		ext ate	Output
$\overline{\hspace{1cm}}$ ABC	x = 0	x = 1	\overline{y}
$(S_0) 000$	000	001	0
$(S_1) 001$	000	010	0
$(S_2) \ 010$	011	010	0
$(S_3) \ 011$	000	100	0
$(S_4) \ 100$	000	010	1
101	X	X	X
110	X	X	X
111	X	X	X

So we can obtain the input and state equations from the K-maps

$$A(t+1) = D_A(t) = BCx$$

 $B(t+1) = D_B(t) = BC' + Ax + B'Cx$
 $C(t+1) = D_C(t) = BC'x' + A'B'C'x$

and the output equation is

$$y = A$$

And the K-maps are

AP CS	. 00	01	11	10
00	0	0	0	0
01	0	0	1	0
11	X	X	X	X
10	0	0	X	X

AP CS	. 00	01	11	10
00	0	0	1	0
01	1	1	0	0
11	X	X	X	X
10	0	1	X	X

ZS CS	. 00	01	11	10
00	0	1	0	0
01	1	0	0	0
11	X	X	X	X
10	0	0	X	X

