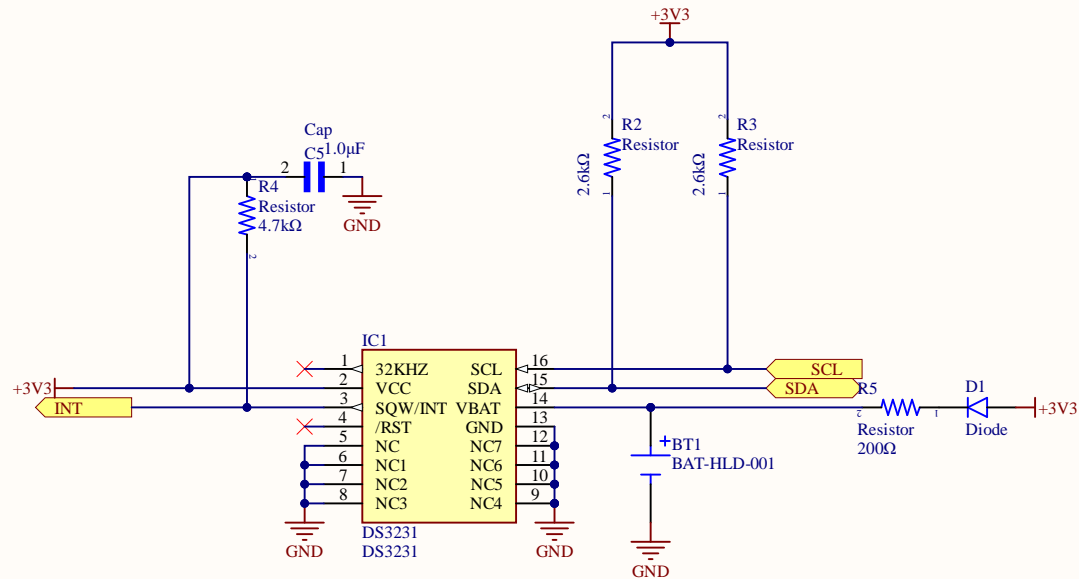
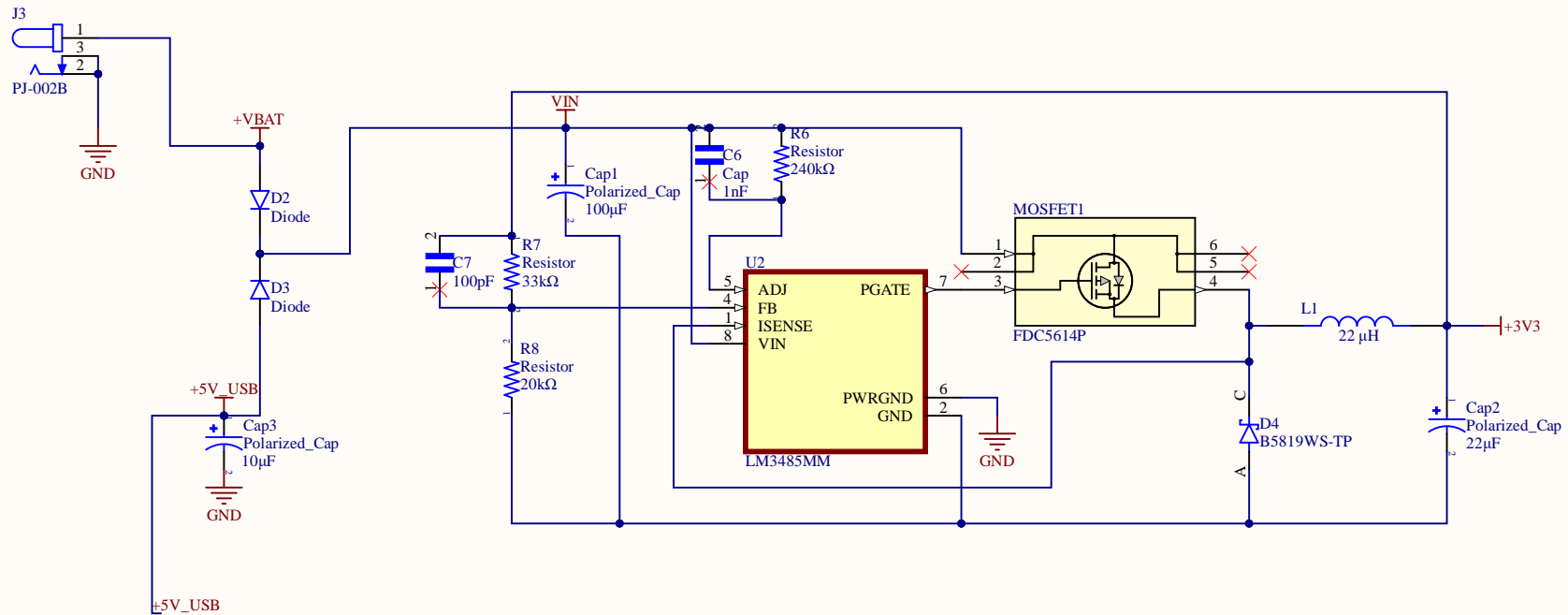


REVISION	DESCRIPTION	DATE	APPROVED

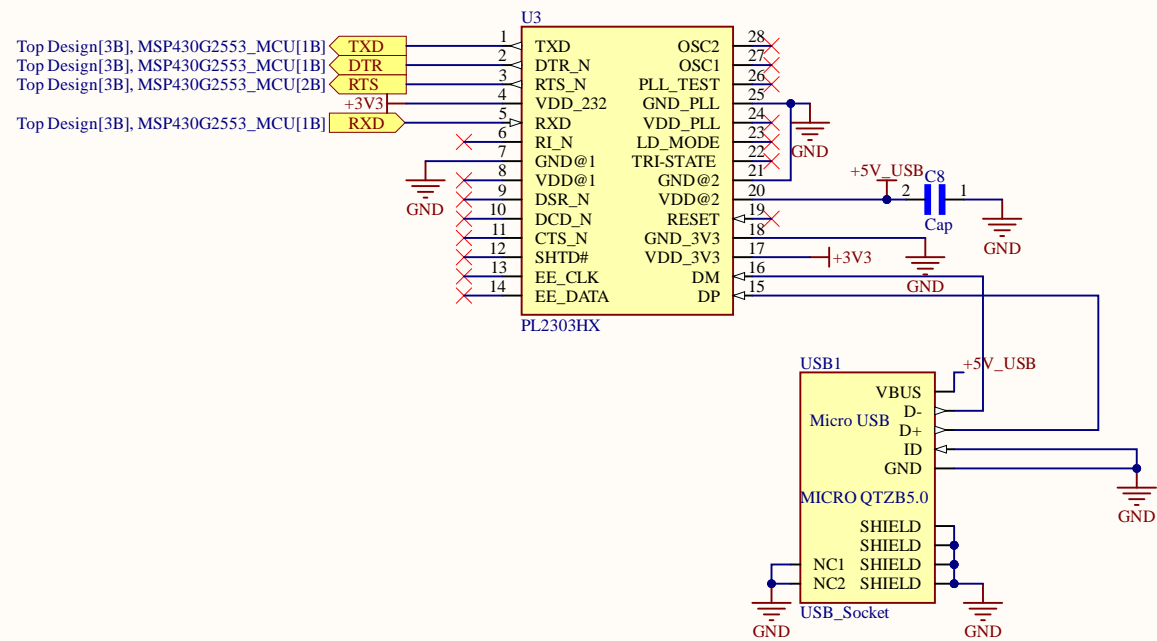
APPROVALS		DATE	PROJECT		Altium		
ENG: Intem Dilini			PROJECT REVISION:		DOCUMENT REVISION:		
DSN: Intem Dilini			DESIGN ITEM:				
CHK: Sir Pramitha			TITLE				
BOM:		REFERENCE DOCUMENTS				Top Design(low powe dev circuit)	
ASSY DWG:		SIZE	CAGE CODE	DWG NO.		REV	
FAB DWG:		A3					
PCB DWG:		SCALE:	FILE NAME	Top Design.SchDoc		SHEET 1 OF 5	



Title		
Size	Number	Revision
A4		
Date:	7/18/2023	Sheet of
File:	D:\SLT-Internship\...\DS3231_RTC.SchDocDrawn By:	



Title		
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Date: 7/18/2023	Sheet of	
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Title		
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