Diljodh Singh

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Education

Vellore Institute of Technology, (VIT) University   **Chennai, India**
Bachelor of Technology, Electronics and communication   *CGPA: 8.0*   *2015-2019*

Objective

Seeking a challenging career with a progressive organization that provides an opportunity to enhance my technical skills & capitalize on abilities in the field of information technology (IT).

Academic Projects

1.) Auto Intensity Controlled Street Light System(2017)**VIT, India**
- Steered efforts of a 3-member team to design an auto-intensity controlled street light system, using infrared sensors to sense the object arrival along with LDR (light dependent resistor) to detect the natural light intensity. .
- Used the PWM(pulse width modulation) concept to monitor the duty cycle of street lights.

2.) Obstacle Avoiding Robot using Arduino along with Ultrasonic Sensor and Servo Motor (2017) **VIT,India**
- Designed an obstacle avoiding robot by mounting ultrasonic sensor on the servo motor fan to have a full 180 degrees sight of the surroundings using the mechanism of servo motor
- Made the embedded prototype that was highly efficient in avoiding the obstacle with 100% efficiency.

3.) Home Automation System Using Bluetooth module and 8051 microcontroller (2018) **VIT, India**
- Worked with a 3-member team to design home automation system to power the home appliances using mobile phone via bluetooth module and 8051 microcontroller.
- Designed an embedded prototype using relay switches to start the high voltage appliances.

4.) FPGA Implementation of Industrial Automation(2019) VIT,India
- Steered efforts of a three member team to design an automated industrial system using FPGA Spartan 3 kit caliberated by the PIR sensor to detect the movement of the objects in the production vicinity along with LM 35 sensor to meaure the temperature.
- Used the GSM module to send the readings to the mobile phones of the concerened authorities.

Professional Experience

Bharat Sanchar Nigam Limited(BSNL)   **Gurdaspur, Punjab, India**
Intern   June 2017
- Came across various call set up techniques such as MLLN(Managed Leased Line Network)and MDF.
- Learned about the function of exchange such as inchannel signaling , common channel signaling.
- Gathered knowledge about the features of intranet , wifi, wimax .

Activities and Interests

Deputy head, Electronics Department -- Shaurya Racing: Official team for Formula Student India, **VIT   (2017 & 2018)**
- Selected from among 1000 teams to compete in FSI (Formula Student India) (2017 and 2018).
- Led 27-member team to design and manufacture a Formula-One vehicle type for the competition
- Led 5-member team to design gear shifter using solenoids and designed the data acquisition system to optimize the performance of the car
- Ranked 9th in the designing event of the competition with overall rank 12th .

Skills
- Hardware troubleshooting
- Programming (C/Java)
- Microsoft Office (MS Word, Excel, Powerpoint,etc)