Dynamic Elias-Fano Representation

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A **dynamic ordered set** S is a data structure representing n objects and supporting the following operations:

- Insert(x) inserts x in S
- Delete(x) deletes x from S
- Search(x) checks whether x belongs to S
- Minimum() returns the minimum element of S
- Maximum() returns the maximum element of S
- Predecessor(x) returns max{ $y \in S : y < x$ }
- Successor(x) returns min{ $y \in S : y \ge x$ }

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Challenge

How to **optimally** solve the **integer** dynamic ordered set problem in **compressed space**?

Integer Data Structures

- van Emde Boas Trees
- X/Y-Fast Tries
- Fusion Trees
- Exponential Search Trees
- •

- EF(S(n,u)) = $n \log(u/n) + 2n$ bits to encode an ordered integer sequence S
- O(1) **Access**
- O(1 + $\log(u/n)$) Predecessor

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- + time
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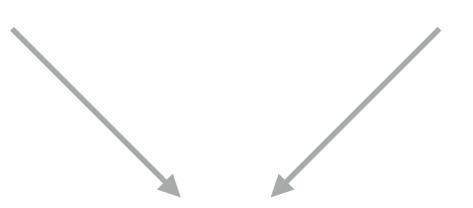




Elias-Fano Encoding

- $\mathsf{EF}(S(n,u)) = n \log(u/n) + 2n \, \mathsf{bits} \, \mathsf{to}$
 - encode an ordered integer sequence S
- O(1) Access
- $O(1 + \log(u/n))$ **Predecessor**

- + time
- space
- + dynamic



- + time
- + space
- static

Can we grab the best from both?

Extend the *static* Elias-Fano representation of S as to support

- 1. Predecessor
- 2. Access/Insert/Delete

in **optimal time** and using $n \log(u/n) + 2n$ bits

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in **optimal time** and using $n \log(u/n) + 2n$ bits +

sublinear redundancy

o(n) bits

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Lower bounds

- 1. [Patrascu and Thorup, STC 2007]
- Optimal space/time trade-off
- m bits, where $a = \log(m/n) \log w$

$$\Theta\Big(\min\Big\{\log_w n,\log\frac{w-\log n}{a},\frac{\log\frac{w}{a}}{\log(\frac{a}{\log n}\log\frac{w}{a})},\frac{\log\frac{w}{a}}{\log(\log\frac{w}{a}/\log\frac{\log n}{a})}\Big\}\Big)$$

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Dynamic List Representation Problem:

- Access/Insert/Delete in Ω(log n / loglog n)
 amortized time
- $w \le \log^{\gamma} n$ for some γ

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For Elias-Fano, $a = \log(\log(u/n) + 2)$ bits: the second branch becomes $O(\log\log n)$ 2. [Fredman and Saks, STC 1989]

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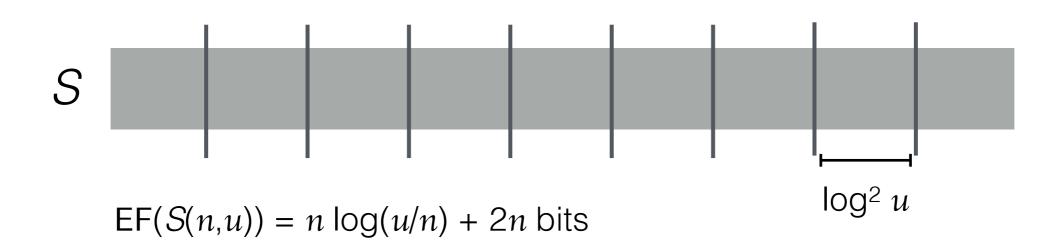
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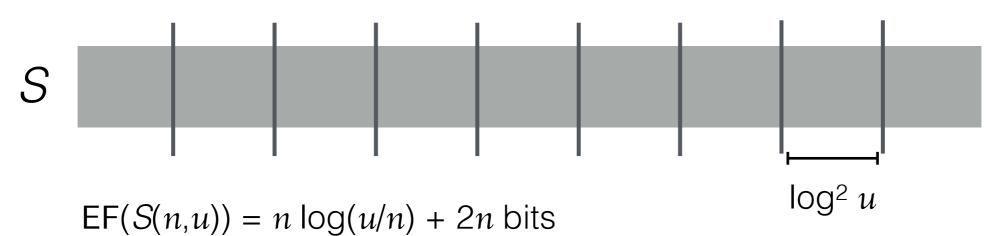
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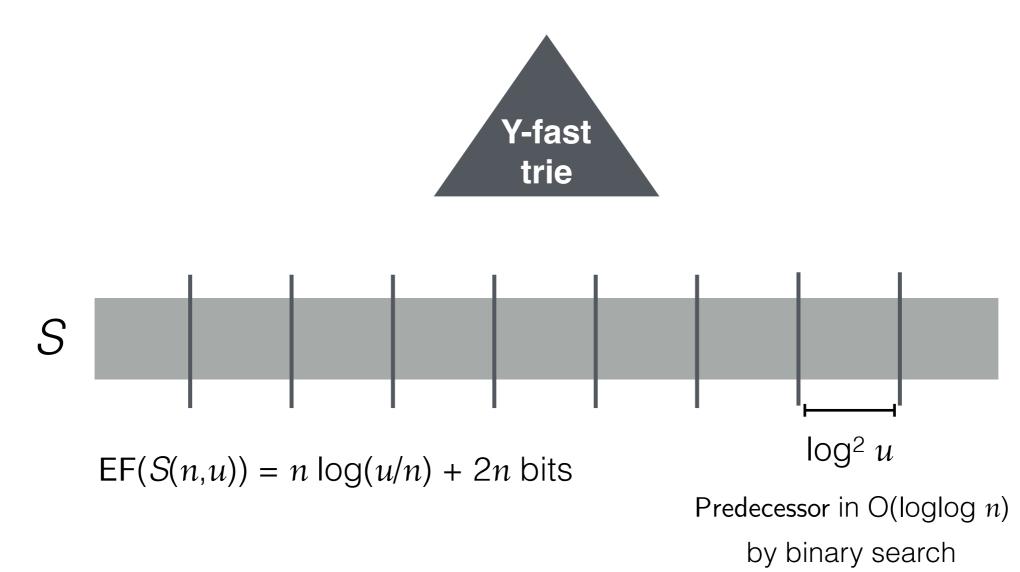


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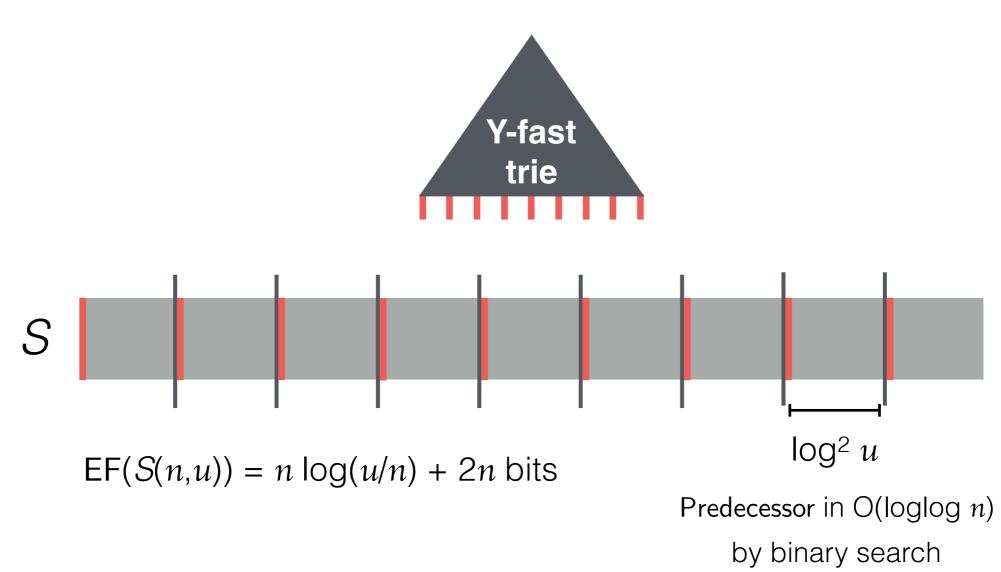


Predecessor in O(loglog *n*) by binary search

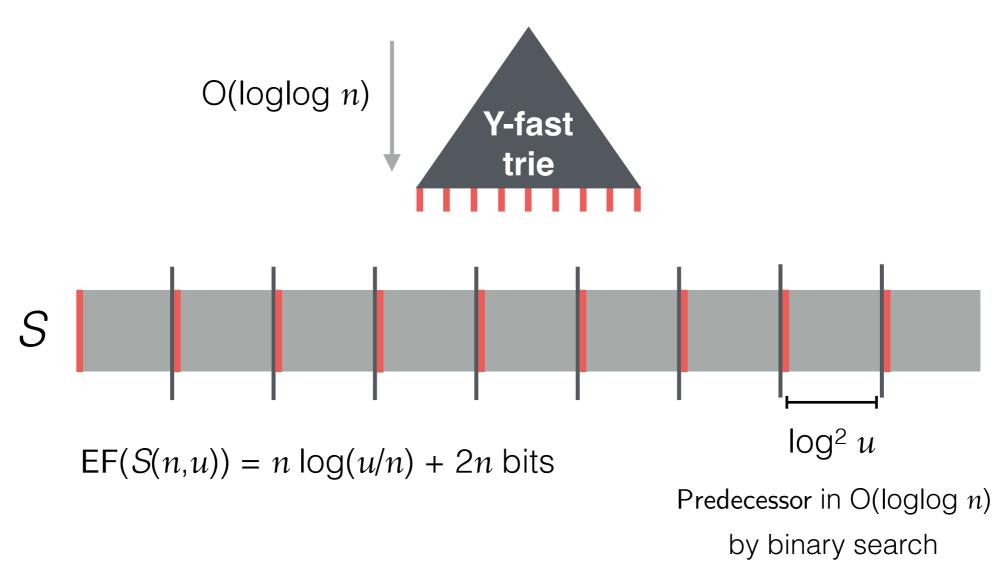
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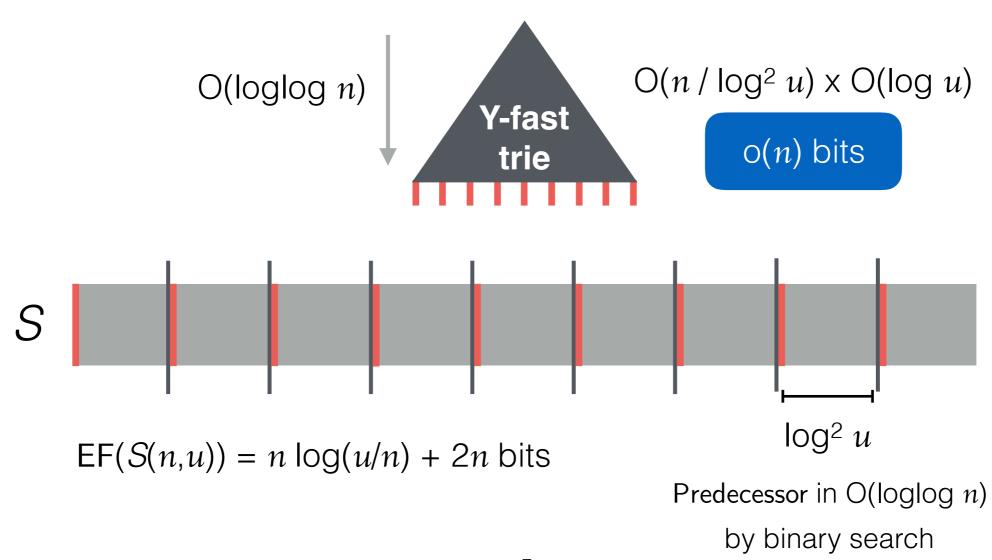
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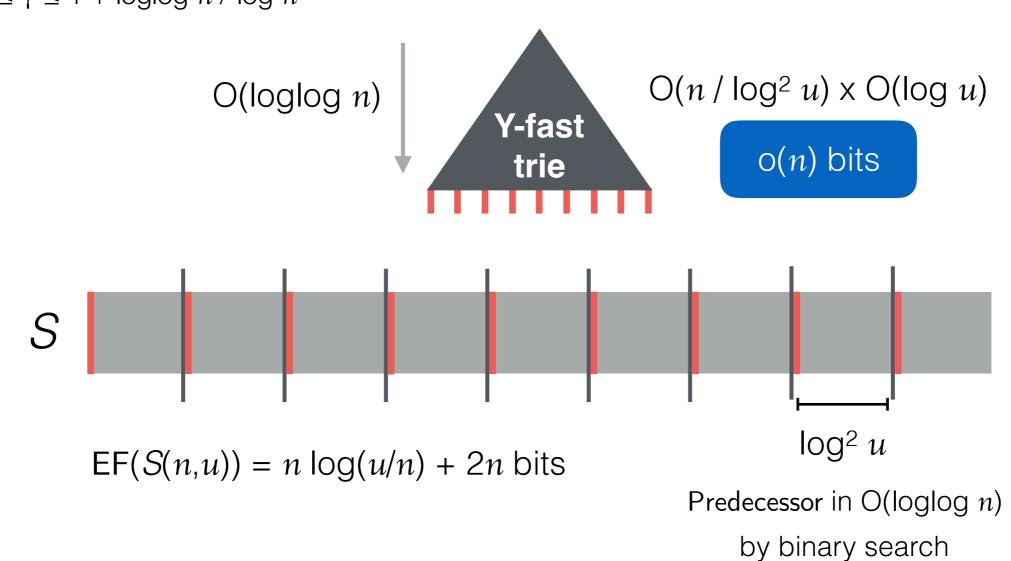
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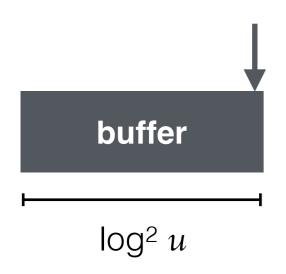
- EF(S(n,u)) + o(n) bits
- O(1) Access
- O(min{1+log(u/n), loglog n}) Predecessor for the tiny range $1 \le \gamma \le 1 + \log\log n / \log n$



For
$$u = n^{\gamma}$$
, $\gamma = \Theta(1)$:

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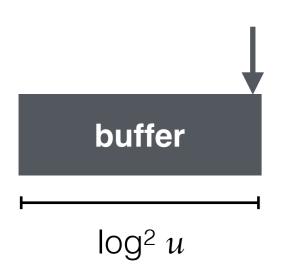
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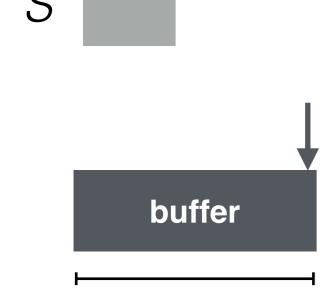
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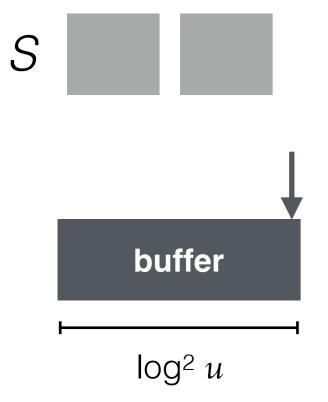
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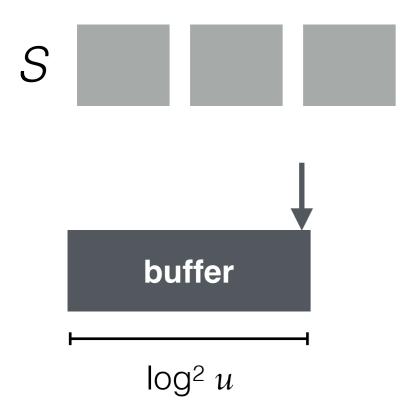


 $log^2 u$

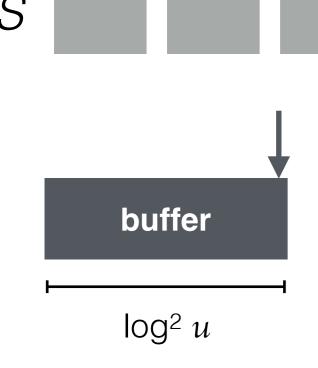
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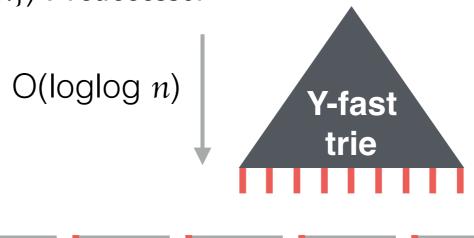
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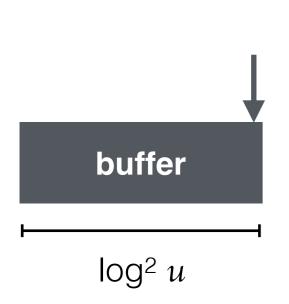


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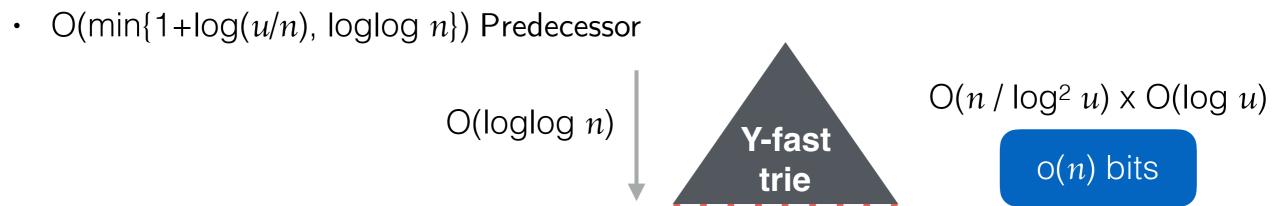




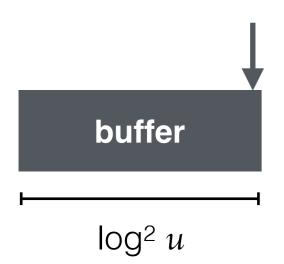
Results - Extensible Elias-Fano

For $u = n^{\gamma}$, $\gamma = \Theta(1)$:

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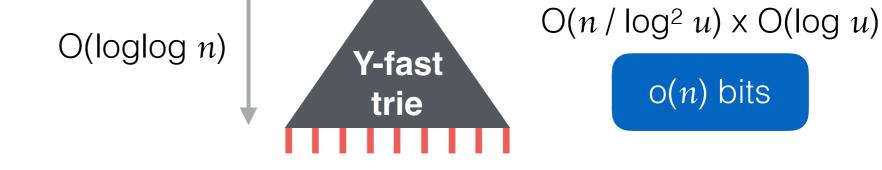


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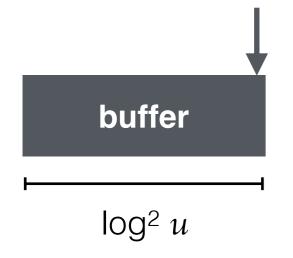
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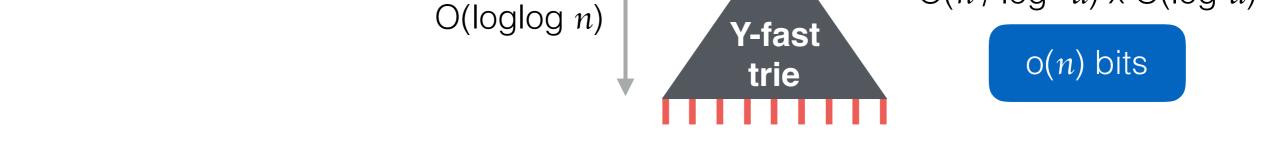
 $EF(S(n,u)) = n \log(u/n) + 2n \text{ bits}$

The encoding of the blocks takes $\leq EF(S(n,u))$ bits

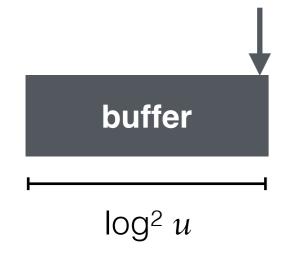
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- O(min{1+log(u/n), loglog n}) Predecessor $O(n / \log^2 u) \times O(\log u)$







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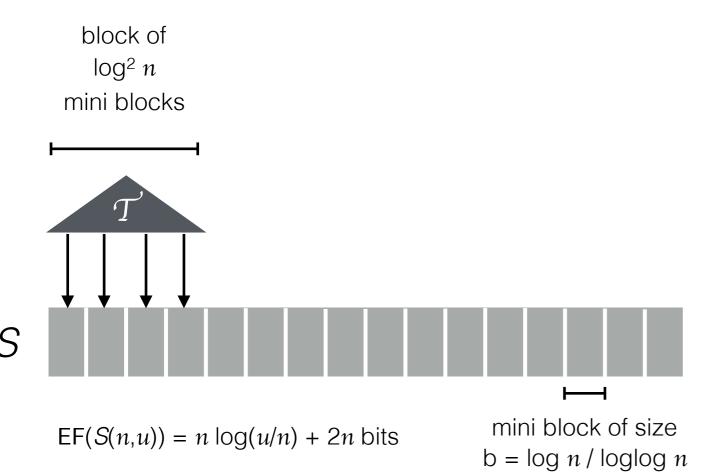


 $\mathsf{EF}(S(n,u)) = n \log(u/n) + 2n \, \mathsf{bits}$

mini block of size b = log n / loglog n

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block of $\log^2 n$ mini blocks

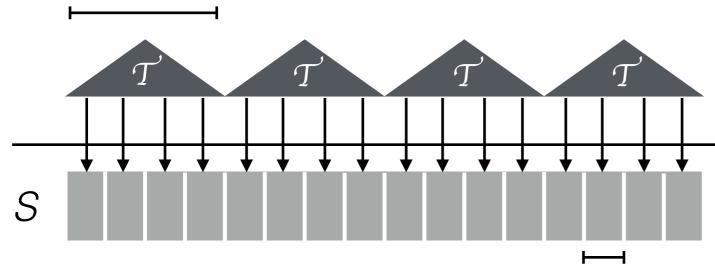
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 \mathcal{T} is a k-ary tree of constant height:

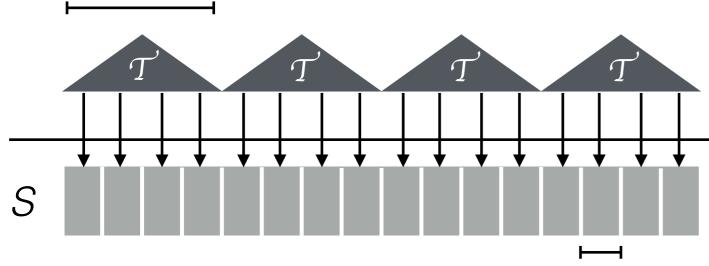
- O(loglog n) time
- $O(\log^2 n \log \log n)$ bits

lower level

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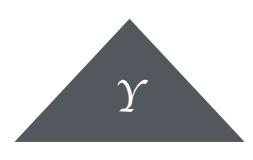
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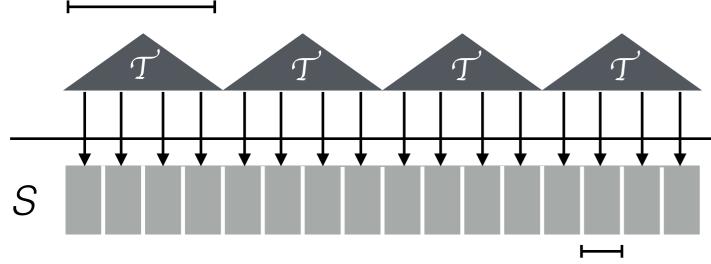
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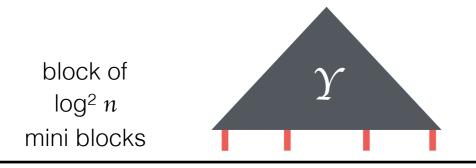
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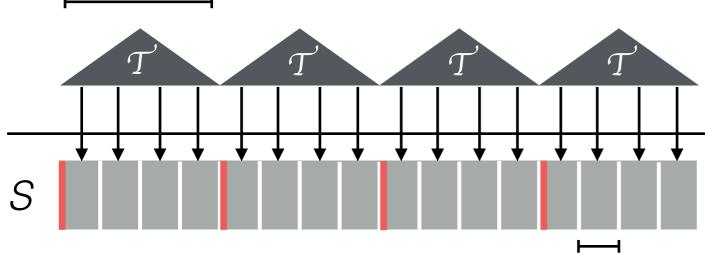
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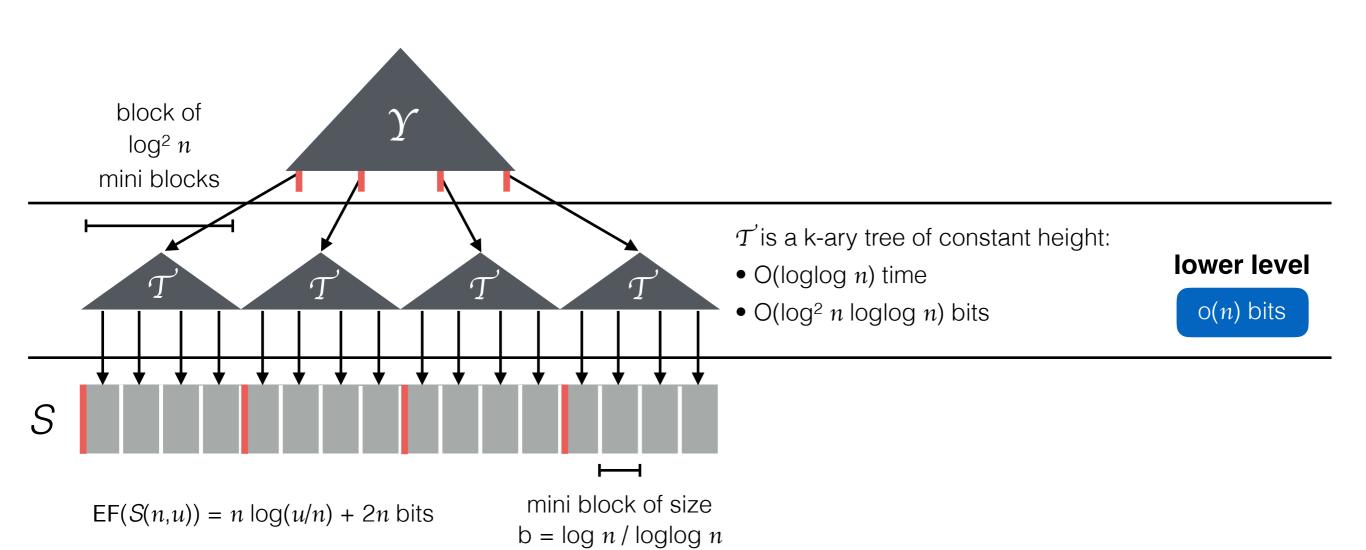
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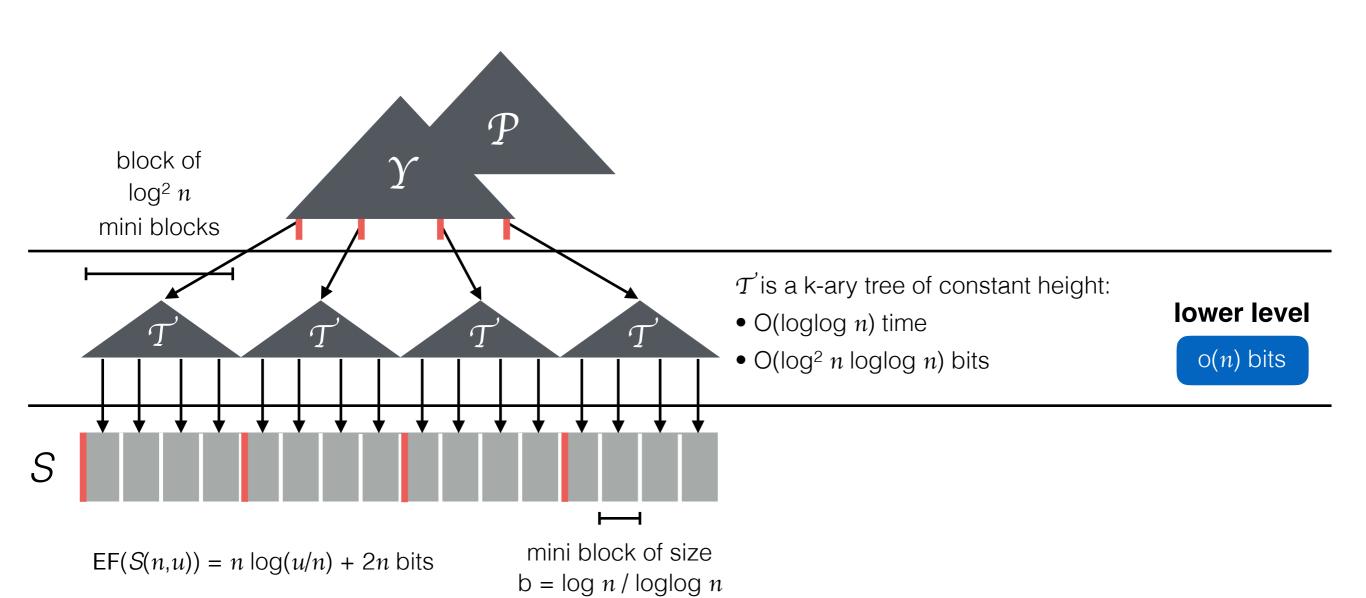
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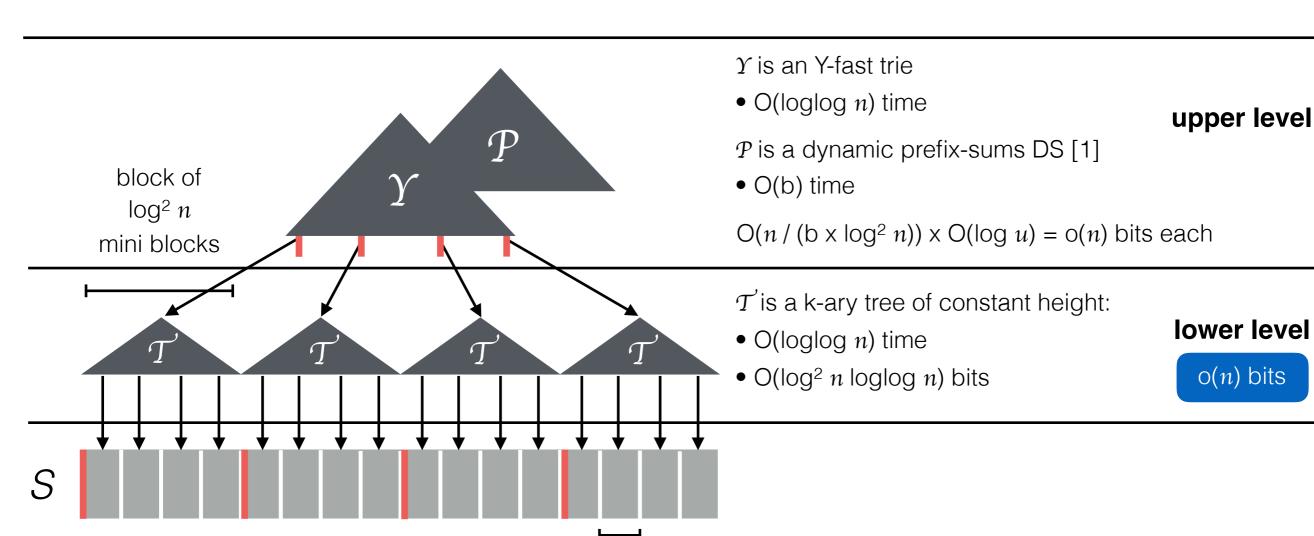
For $u = n^{\gamma}$, $\gamma = \Theta(1)$:

- EF(S(n,u)) + o(n) bits
- $O(\log n / \log \log n)$ Access
- O($\log n / \log \log n$) Insert/Delete (amortized)
- O(min{1+log(u/n), loglog n}) Predecessor



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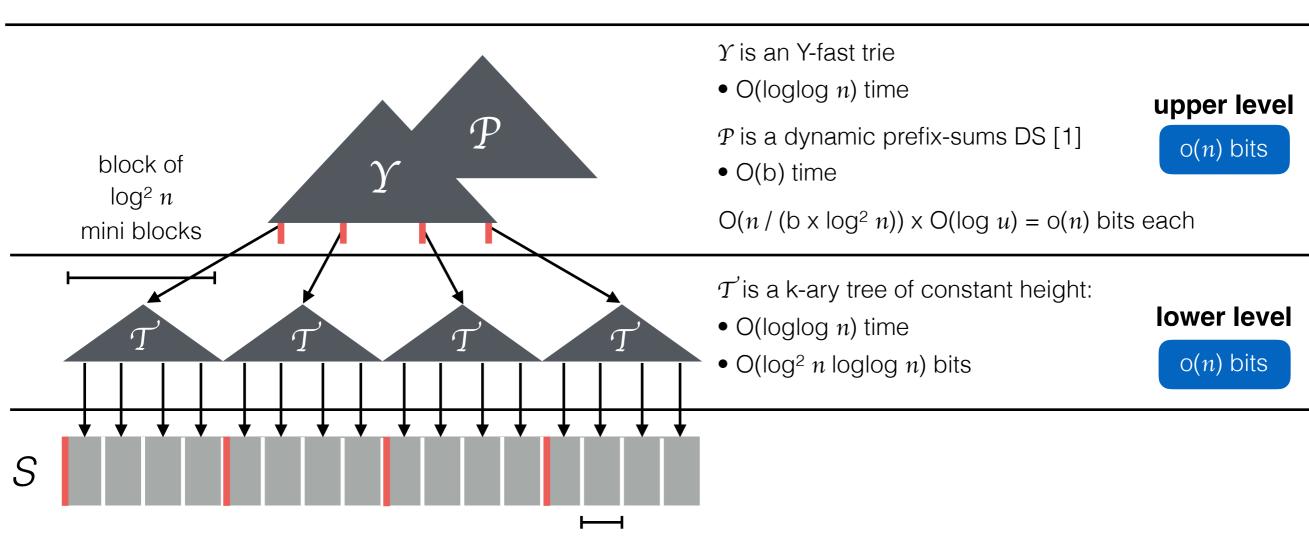


 $\mathsf{EF}(S(n,u)) = n \log(u/n) + 2n \, \mathsf{bits}$

mini block of size $b = \log n / \log \log n$

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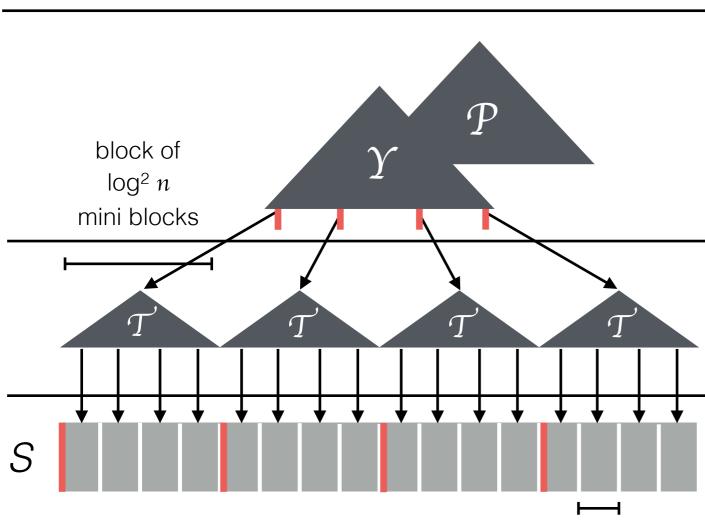


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 Υ is an Y-fast trie

O(loglog n) time

 \mathcal{P} is a dynamic prefix-sums DS [1]

O(b) time

 $O(n / (b \times log^2 n)) \times O(log u) = o(n)$ bits each

 \mathcal{T} is a k-ary tree of constant height:

- O(loglog *n*) time
- $O(\log^2 n \log \log n)$ bits

lower level

upper level

o(n) bits

o(n) bits

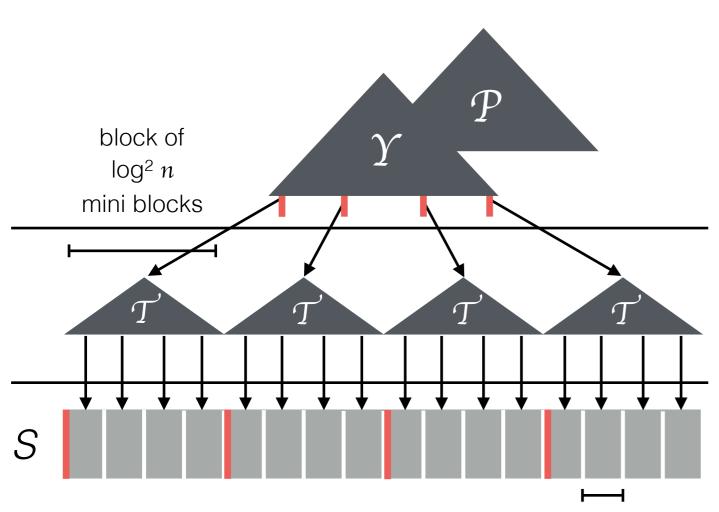
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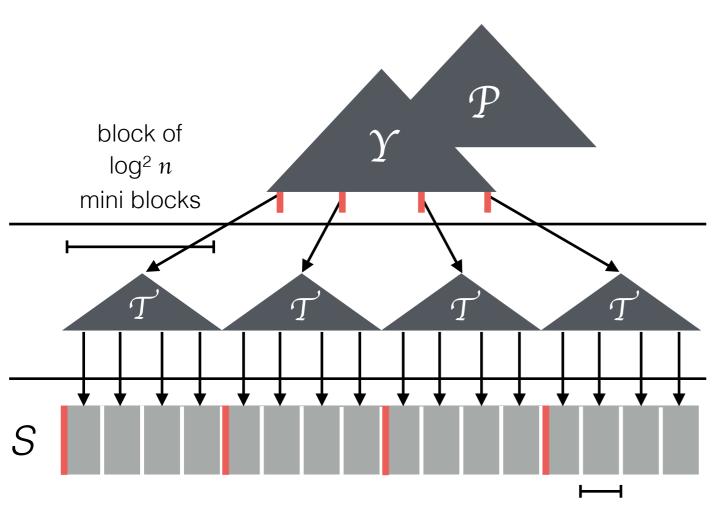
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Memory management for the mini blocks:

Corollary 3 from [3]: random Access in O(1).

Theorem 6 from [2]: address and allocate the high part of a mini block in O(1).

The overall redundancy is o(n) bits.

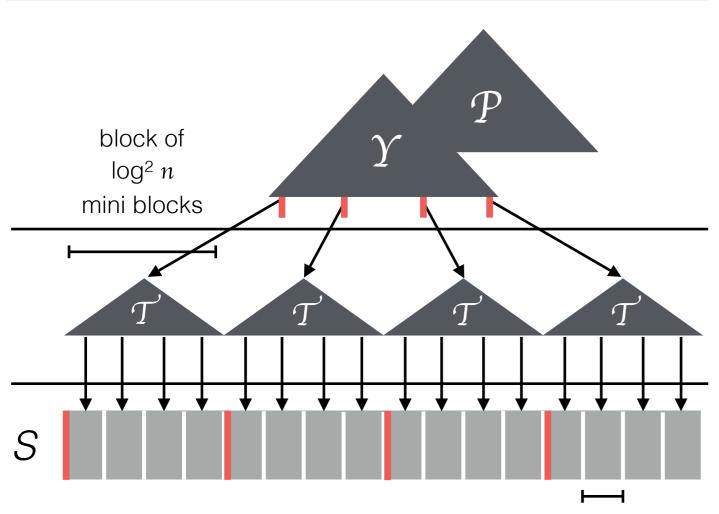
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Memory management for the mini blocks:

b
$$\log(u/b)$$
 + 2b bits

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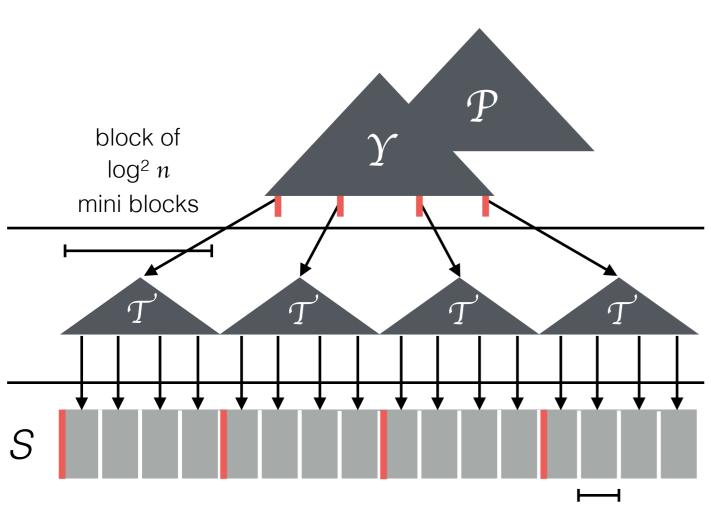
[1] Philip Bille, Patrick Hagge Cording, Inge Li Gørtz, Frederik Rye Skjoldjensen, Hjalte Wedel Vildhøj, and Søren Vind. *Dynamic relative compression. CoRR*, abs/1504.07851, 2015.

[2] J. Jansson, K. Sadakane, and Wing-Kin Sung. *CRAM: Compressed random access memory.* ICALP 2012.

[3] R. Raman, V. Raman, and S. Srinivasa Rao. *Succinct dynamic data structures*. WADS 2001.

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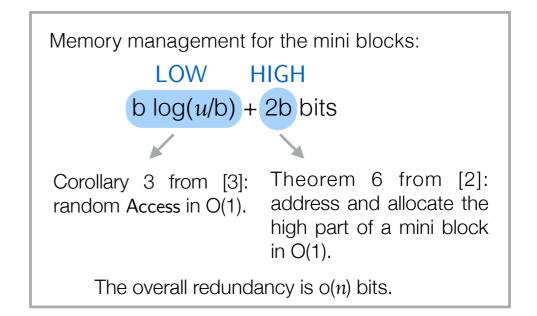
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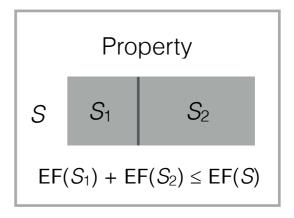


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Thanks for your attention, time, patience!

Any questions?