



Dilon Brahmabhatt

Summary

Diploma in Electronics and Communication Engineering, with hands-on experience working as a Circuit Inspection Engineer. Presently engaged in a B.Tech course in Information and Communication Technology. Good command over programming languages- C, C++, Java, Python, and MySQL for database management. Well acquainted with CPU Design Concepts. Seeking a challenging role where I can apply my skills and contribute to a successful organization.

Education

SSC 10th , Ambe School, Vadodara

10th Standard, 2020 with 61% marks.

diploma, MS University, Vadodara

September 2020 — May 2023

completed diploma in ECE with 76.1 percentage

B Tech, pandit deendayal energy university, gandhinagar

September 2023 — May 2026

B.Tech in Information and Communication Technology. Eager to learn and grow in a challenging and dynamic environment.

Internships

Circuit Inspection Engineer at SYNCHRONIC ELECTRONICS, Vadodara

June 2023 — August 2025

Analyses HMI circuit part by part and find default electronic components on the basis of output Voltage, frequency and current. Learn basic concepts of circuit behavior.

Project

CPU Design

March 2024 — April 2024

Designed and implemented a CPU model based on the 8085. The design included an ALU for processing, a common bus for data transfer, and a control unit for instruction execution.

Details

1102 , Bluebell, Vicenza

Highdeck

Vadodara, 390012

India

8320137058

dilon151103@gmail.com

Date / Place of birth

15/11/2003

Vadodara

Nationality

Indian

Skills

Proficient in programming languages such as C, C++, Java, Python, and database management with MySQL.

Critical thinking and problem solving

Hobbies

Enjoy reading articles on football and cricket. Fond of solving general mathematics puzzles.

Languages

English

Hindi

Gujarati