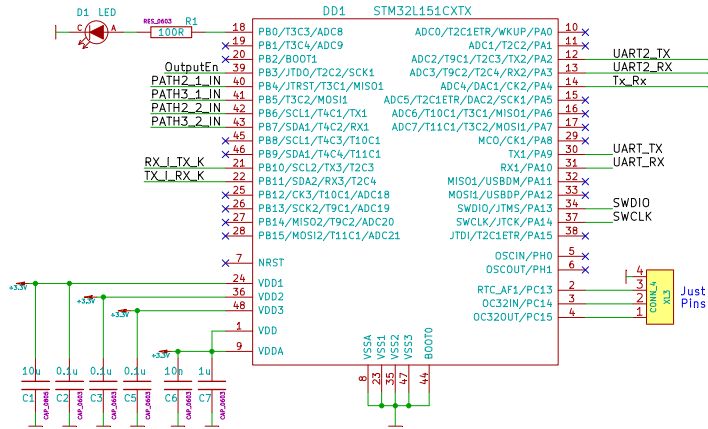
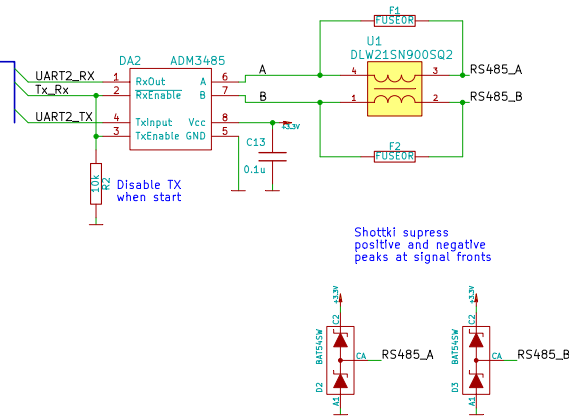


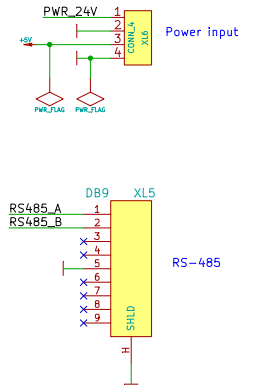
CPU



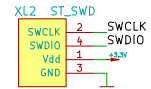
RS-485 interface



Power & Signal Connectors



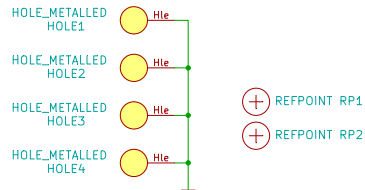
Programming



Debug port

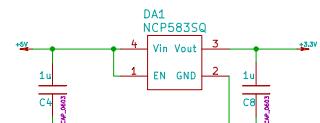


Holes & ref points



Power

3.3V 150mA power regulator



Output buffers

Disabled by default

