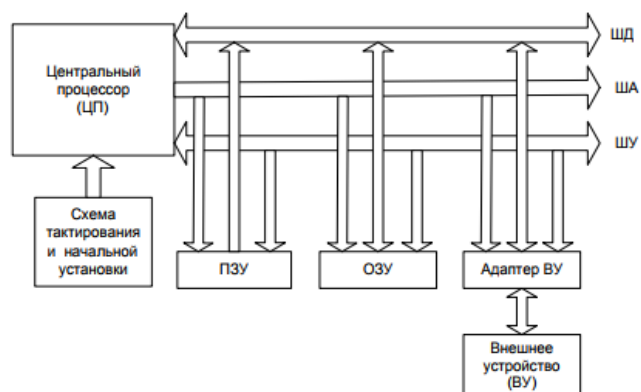
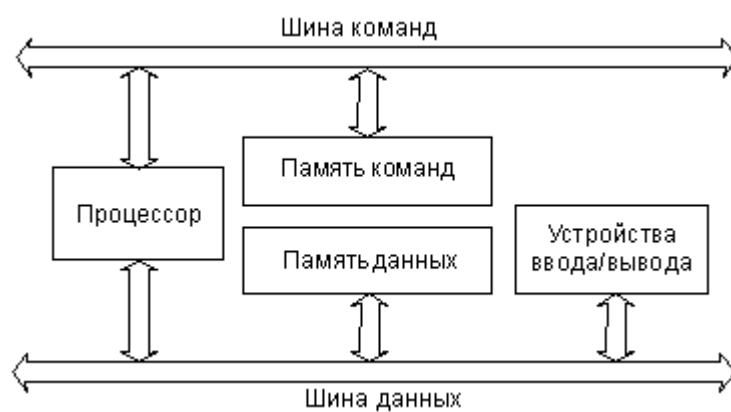
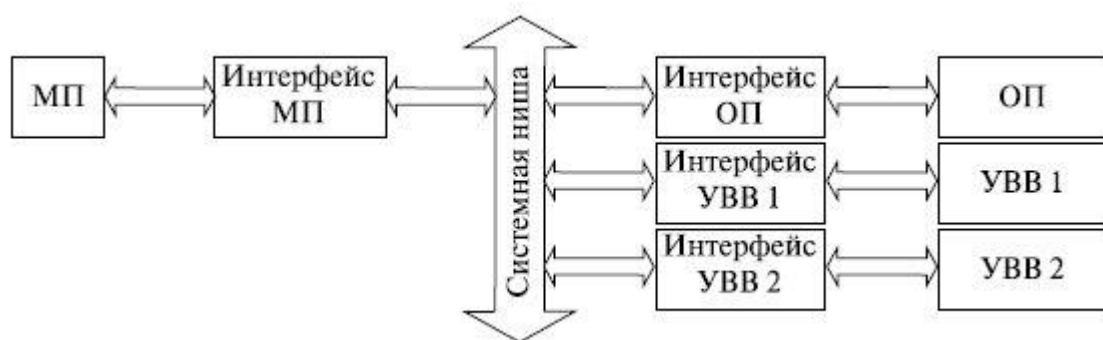


Тема 2.7 Магистрально-модульный принцип построения микропроцессорных систем

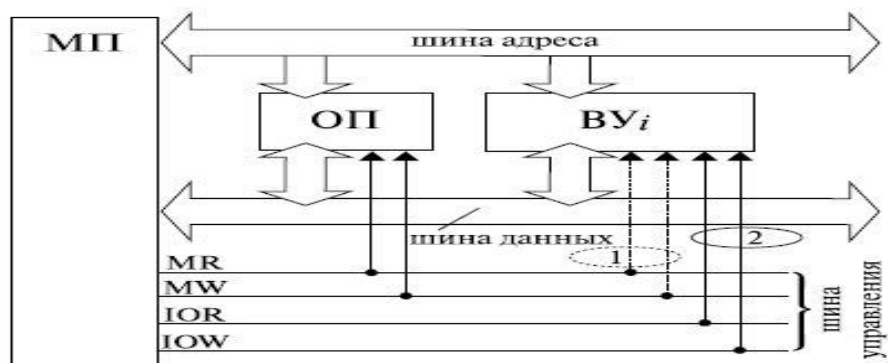






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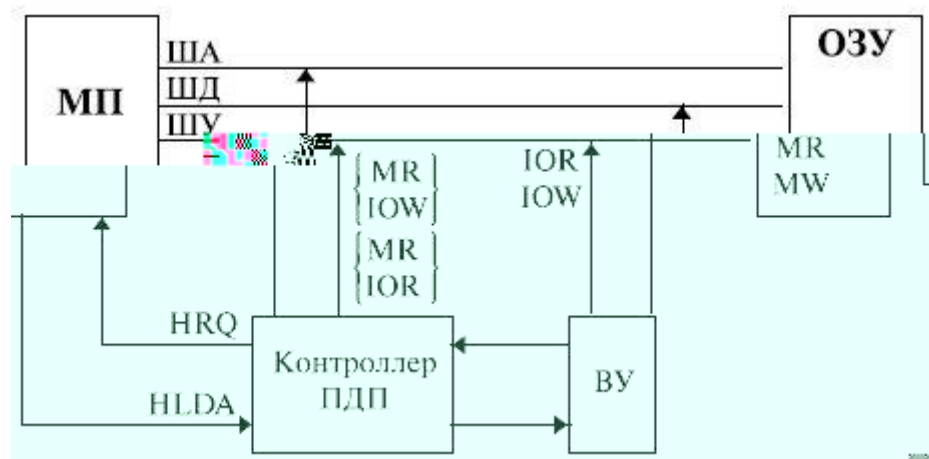
(burst



- IOR/IOW

(Direct Memory Access - DMA).

- 1.
- 2.
- 3.
- 4.
- 5.
- 6.
- 7.



1. DRQ
2. HRQ).
3. HLDA

4.

DACK).

-

5.

6.

IOW,

IOR, MW

7.

8.

-8.