	RA00291	Date: 03.04.2016
COBHAM	Use on DSBR4008 System DSBR011 Test Procedure	Doc. p/n: RA00291
		Rev.: A

S/N: 18012209

Frequency band U/L: MHz; D/L: 806-824 MHz

RF to RF: Connect a 6dB attenuator from IFin to IFout

Default DSA settings: UL: 0.0, 0.0, 0.0

DL: 0.0, 6.0, 8.0

N	Test		Min.	Max.	D/L	U/L	Remarks
3.1	RFB Current at 6.5VDC (A)		2.0	2.5	NA	NA	2.3A typical *the DDF current is 3.1A
3.2	RF to IF Gain (dB), PIN -60dBm		43.0	47.0	NA	NA	Full band (IF center = 120MHz) Write the values on top of RFB
	RF to IF Flatness (dBp-p)		-	1.5	NA	NA	Full band (IF center = 120MHz)
3.3	RF to RF Gain (dB), PIN -50dBm		UL 0.0	UL 0.0	58.9	None	Full band Write the values on top of RFB
			DL 55.0	DL 59.0			
	RF to RF Flatness (dBp-p)		-	3.5	1.7	None	
3.4	RF Output Return Loss		-12dB	-	Pass	Pass	Will be removed in the future.
3.5	DSA's PIN = -70 dBm	DSA1	Pass / Fail		Pass	None	For each DSA set these values: 0.5, 1, 2, 4, 8, 16, 31 Fill Pass / Fail
		DSA2	Pass / Fail		Pass	None	
		DSA3	Pass / Fail		Pass	None	
3.6	DL:Inter-mod. at POUT = 0 dBm UP:Inter-mod. at POUT = 0 dBm		-	-55.0	Pass	None	2 CW, Spacing of 0.6MHz at center freq.
3.7	BIT Alarm – Set the IF value to 1.2GHz		-	-	Pass	None	Pass / Fail; alarm on/off
3.8	ALCIN Validation (dBm)		-10.0	-20.0	104	None	Starting value is 255. Power in during test is 0 Bm. Set the value to 0, see the power reduced fill Pass / Fail return the value to 255 For ATE test: set value to 120.
3.9	ALCOUT Validation (dBm)		-30.0	-40.0	-45.1	None	Starting value is 255. Power in during test is 0 Bm. Set the value to 0, see the power reduced fill Pass / Fail return the value to 255 For ATE test: set value to 120. Insert 2.7v to pin 46 and 45 and verify the power drops by 20db min.

Tested by : Dima   Signature: ICT 46 pass   Date test: 20180213 12:33:17