

S/N: 18011362

Frequency band U/L: 1920-1980 MHz; D/L: 2110-2170 MHz

RF to RF: Connect a 6dB attenuator from IFin to IFout

Default DSA settings: UL: 0.0, 9.0, 13.5

DL: 0.0, 10.0, 5.5

N	Test	Min.	Max.	D/L	U/L	Remarks
3.1	RFB Current at 6.5VDC (A)	2.0	2.5	NA	NA	2.3A typical *the DDF current is 3.1A
3.2	RF to IF Gain (dB), PIN -60dBm	43.0	47.0	NA	NA	Full band (IF center = 120MHz) Write the values on top of RFB
	RF to IF Flatness (dBp-p)	-	1.5	NA	NA	Full band (IF center = 120MHz)
3.3	RF to RF Gain (dB), PIN -50dBm	UL 41.0	UL 45.0	46.6	40.5	Full band Write the values on top of RFB
		DL 41.0	DL 45.0			
	RF to RF Flatness (dBp-p)	-	3.5	2.8	1.9	
3.4	RF Output Return Loss	-12dB	-	Pass	Pass	Will be removed in the future.
3.5	DSA's PIN = -60 dBm	DSA1	Pass / Fail	Pass	Pass	For each DSA set these values: 0.5, 1, 2, 4, 8, 16, 31 Fill Pass / Fail
		DSA2	Pass / Fail	Pass	Pass	
		DSA3	Pass / Fail	Pass	Pass	
3.6	DL:Inter-mod. at POUT = -7 dBm UP:Inter-mod. at POUT = -7 dBm	-	-55.0	Pass	Pass	2 CW, Spacing of 0.6MHz at center freq.
3.7	BIT Alarm – Set the IF value to 1.2GHz	-	-	Pass	Pass	Pass / Fail; alarm on/off
3.8	ALCIN Validation (dBm)	-10.0	-20.0	110	130	Starting value is 255. Power in during test is -35 Bm. Set the value to 0, see the power reduced fill Pass / Fail return the value to 255 For ATE test: set value to 120.
3.9	ALCOUT Validation (dBm)	-30.0	-40.0	-28.8	-37.2	Starting value is 255. Power in during test is -35 Bm. Set the value to 0, see the power reduced fill Pass / Fail return the value to 255 For ATE test: set value to 120. Insert 2.7v to pin 46 and 45 and verify the power drops by 20db min.

Tested by : Dima

Signature: ICT 46 pass

Date test: 20180204 12:17:45