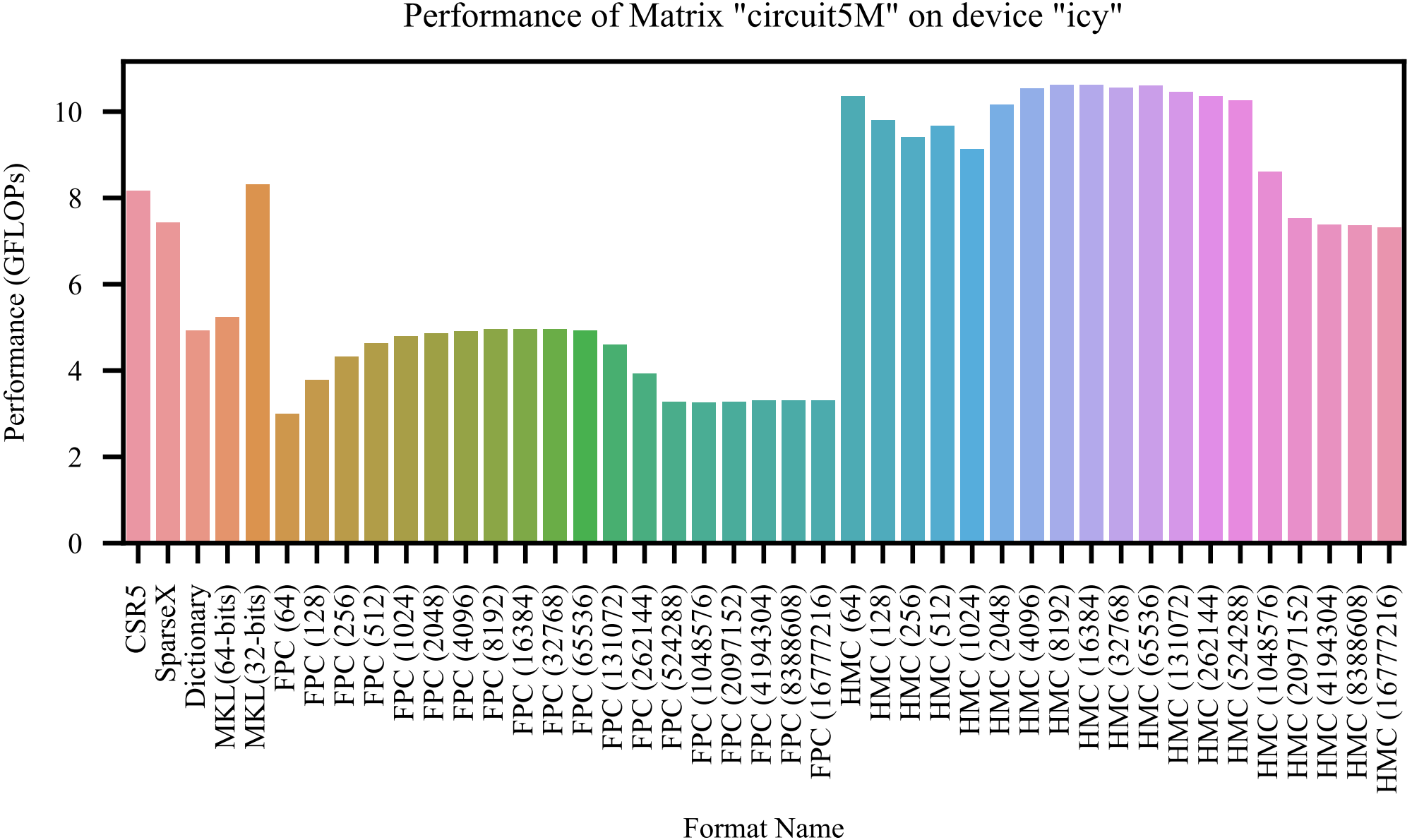
Performance of Matrix "circuit5M" on device "icy"
Performance (GFLOPs)
0
2
4
6
8
10
CSR5
SparseX
Dictionary
MKL(64-bits)
MKL(32-bits)
FPC (64)
FPC (128)
FPC (256)
FPC (512)
FPC (1024)
FPC (2048)
FPC (4096)
FPC (8192)
FPC (16384)
FPC (32768)
FPC (65536)
FPC (131072)
FPC (262144)
FPC (524288)
FPC (1048576)
FPC (2097152)
FPC (4194304)
FPC (8388608)
FPC (16777216)
HMC (64)
HMC (128)
HMC (256)
HMC (512)
HMC (1024)
HMC (2048)
HMC (4096)
HMC (8192)
HMC (16384)
HMC (32768)
HMC (65536)
HMC (131072)
HMC (262144)
HMC (524288)
HMC (1048576)
HMC (2097152)
HMC (4194304)
HMC (8388608)
HMC (16777216)
Format Name