Transistor Sweep

User: Spachis_Leandros   Device: Dev3c5-PVSK-1.4M-29-02-2024   Area: 1.00 cm^2

"2024-07-29_11.35.20_Transistor Sweep_Dev3c5-PVSK-1.4M-29-02-2024.txt"

▷ Start   Ready

User name	Cell name	Cell area	Temperature
Spachis_Leandros	Dev3c5-PVSK-1.4M-29-	1 cm²	25.0001 °C

00:00:00	100 %	7/29/2024 11:35:30 AM
Remaining (est.)	Sweep Progress	Estimated Completion Time

Device Select
SMU + FGEN

SMU Configuration

Probe Config   Fast = Gate

Fast Probes
Current Gain   1,000
Max current   0A

Slow Probes
Voltage Range   Normal (6V)
Current Range   10 mA
Current Limit   10m A
Autorange   ☐

Measurement Type
Sweep gate, measure channel

Ramp Configuration

Gate
V_start (V)   0
V_end (V)   0
V_step (V)   0

Channel
V_start (V)   0
V_end (V)   2
V_step (V)   100m

Delay Time (s)   0
Scan Rate (mV/s)   200
Include reverse   ☐
Revert   ☐

LED Ring
0.000

Graphs

Gate Voltage 1.553mV   Gate Current 207.611nA

○ Linear ○ Logarithmic

Channel Voltage 2.000V   Channel Current -3.750nA

○ Linear ○ Logarithmic

☑ Vgate: 0.00 V

Ready