Transistor Sweep

User: Spachis_Leandros   Device: Dev3c4-PVSK-1.4M-29-02-2024   Area: 1.00 cm^2

"2024-07-29_12.26.50_Transistor Sweep_Dev3c4-PVSK-1.4M-29-02-2024.txt"

			User name	Cell name	Cell area	Temperature
▷ Start		Ready	Spachis_Leandros	Dev3c4-PVSK-1.4M-29-	1 cm²	25.0004 °C

00:00:01	100 %	7/29/2024 12:27:30 PM
Remaining (est.)	Sweep Progress	Estimated Completion Time

Device Select
SMU + FGEN

SMU Configuration

Probe Config   Fast = Gate

Fast Probes
Current Gain   100
Max current   100mA

Slow Probes
Voltage Range   Normal (6V)
Current Range   100 µA
Current Limit   1m A
Autorange   ☑

Measurement Type
Sweep gate, measure channel

Ramp Configuration

Gate		Channel	
V_start (V)	0	V_start (V)	-1
V_end (V)	0	V_end (V)	1
V_step (V)	0	V_step (V)	25m

Delay Time (s) 5   Scan Rate (mV/s) 100

Include reverse ☑

Revert ☐

LED Ring
0.000

Graphs

Gate Voltage 1.490mV   Gate Current 1.088uA

● Linear ○ Logarithmic

Channel Voltage -999.997mV   Channel Current -4.545nA

● Linear ○ Logarithmic

☑ Vgate: 0.00 V

Ready