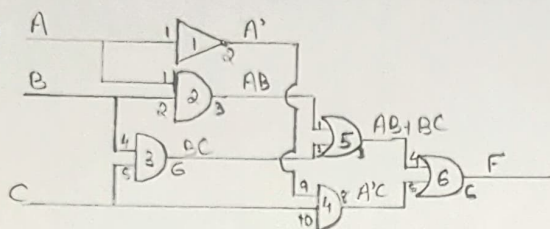


## II. > Pre-Lab ->

$$F = AB + A'C + BC$$



b)

Inputs			Outputs				
A	B	C	A'	AB	A'C	BC	$F = AB + A'C + BC$
0	0	0	1	0	0	0	0
0	0	1	1	0	1	0	1
0	1	0	1	0	0	0	0
0	1	1	1	0	1	1	1
1	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0
1	1	0	0	1	0	0	1
1	1	1	0	1	0	1	1

$$c) F = AB + A'C + BC$$

$$= AB + A'C + BC(A' + A)$$

$$= AB + A'C + A'BC + ABC$$

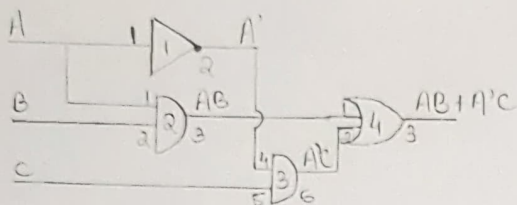
$$= ABC + C + A'C(1 + B)$$

$$= AB + A'C$$

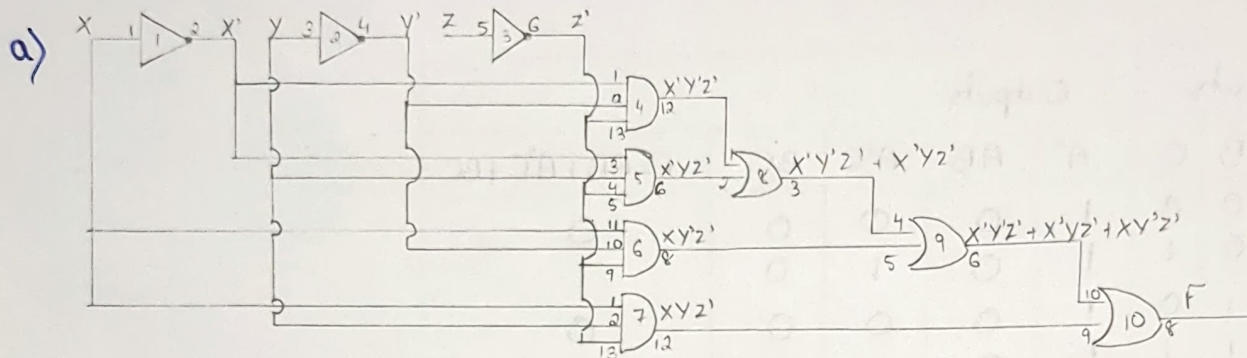
Inputs			Outputs			
A	B	C	A'	AB	A'C	$AB + A'C$
0	0	0	1	0	0	0
0	0	1	1	0	1	1
0	1	0	1	0	0	0
0	1	1	1	0	1	1
1	0	0	0	0	0	0
1	0	1	0	0	0	0
1	1	0	0	1	0	1
1	1	1	0	1	0	1

$\therefore$  ~~Since~~  $AB + A'C$  has the same output as  $F$ .

d)  $F = AB + A'C$



2) i)  $F = X'Y'Z' + X'YZ' + XY'Z' + XYZ'$



Inputs			Outputs							
X	Y	Z	X'	Y'	Z'	X'Y'Z'	X'YZ'	XY'Z'	XYZ'	F
0	0	0	1	1	1	1	0	0	0	1
0	0	1	1	1	0	0	0	0	0	0
0	1	0	1	0	1	0	1	0	0	1
0	1	1	1	0	0	0	0	0	0	0
1	0	0	0	1	1	0	0	1	0	1
1	0	1	0	1	0	0	0	0	0	0
1	1	0	0	0	1	0	0	0	1	1
1	1	1	0	0	0	0	0	0	0	0

b)  $F = X'Y'Z' + X'YZ' + XY'Z' + XYZ'$

$= X'Z'(Y' + Y) + XZ'(Y + Y')$

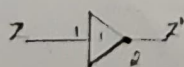
$= X'Z' + XZ'$

$= Z'(X + X')$

$= Z'$

c) X \_\_\_\_\_

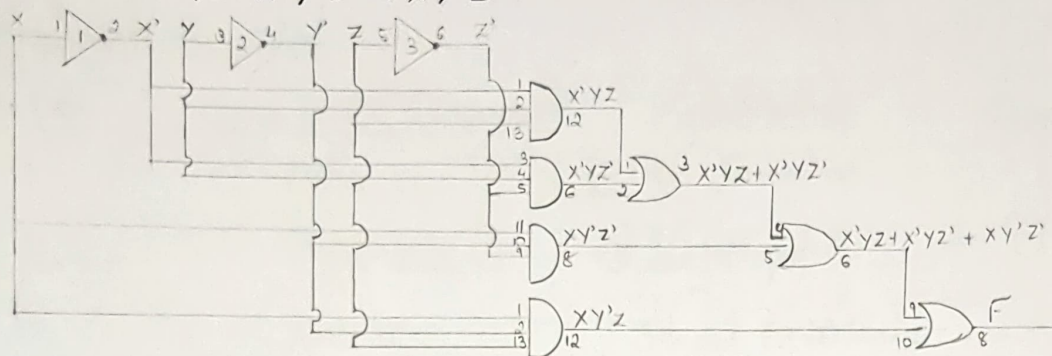
Y \_\_\_\_\_



Inputs			Outputs	
X	Y	Z	Z'	
0	0	0	1	Same as F
0	0	1	0	
0	1	0	1	
0	1	1	0	
1	0	0	1	
1	0	1	0	
1	1	0	1	
1	1	1	0	



$$F = X'YZ + X'YZ' + XY'Z' + XY'Z$$

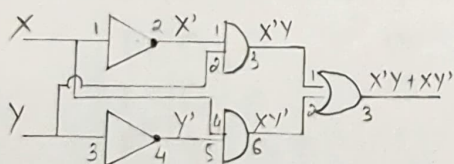


Inputs			Outputs							
X	Y	Z	X'	Y'	Z'	X'YZ	X'YZ'	XY'Z'	XY'Z	F
0	0	0	1	1	1	0	0	0	0	0
0	0	1	1	1	0	0	0	0	0	0
0	1	0	1	0	1	0	1	0	0	1
0	1	1	1	0	0	1	0	0	0	1
1	0	0	0	1	1	0	0	1	0	1
1	0	1	0	1	0	0	0	0	1	1
1	1	0	0	0	1	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0	0

$$b) F = X'YZ + X'YZ' + XY'Z' + XY'Z$$

$$= X'Y(Z + Z') + XY'(Z' + Z)$$

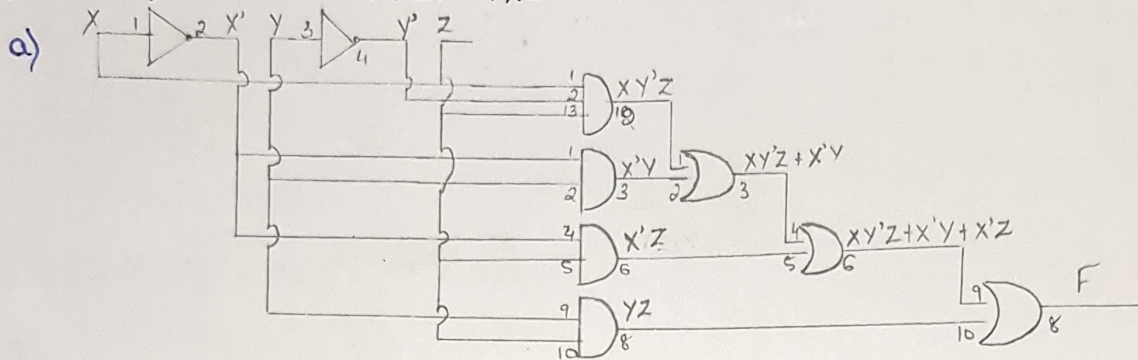
$$= X'Y + XY'$$



Inputs		Outputs				
X	Y	X'	Y'	X'Y	XY'	X'Y + XY'
0	0	1	1	0	0	0
0	0	1	1	0	0	0
0	1	1	0	0	1	1
0	1	1	0	0	1	1
1	0	0	1	1	0	1
1	0	0	1	1	0	1
1	1	0	0	0	0	0
1	1	0	0	0	0	0

$\therefore X'Y + XY'$  has the same output as F.

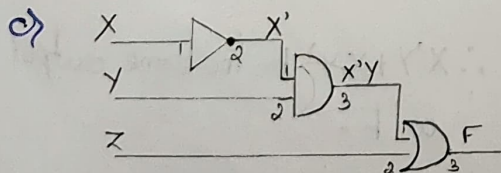
$$3) F(X, Y, Z) = XY'Z + X'Y + X'Z + YZ$$



Inputs			Outputs						
X	Y	Z	X'	Y'	$XY'Z$	$X'Y$	$YZ$	$X'Z$	F
0	0	0	1	1	0	0	0	0	0
0	0	1	1	1	0	0	0	1	1
0	1	0	1	0	0	1	0	0	1
0	1	1	1	0	0	1	1	0	1
1	0	0	0	1	0	0	0	0	0
1	0	1	0	1	0	0	0	0	0
1	1	0	0	0	0	1	0	0	1
1	1	1	0	0	0	1	1	0	1

b)

$$\begin{aligned}
 F(X, Y, Z) &= XY'Z + X'Y + X'Z + YZ \\
 &= Z(XY' + Y) + X'(Y + Z) \\
 &= Z(X + Y)(Y' + Y) + X'(Y + Z) \\
 &= Z(X + Y) + X'(Y + Z) \\
 &= ZX + ZY + X'Y + X'Z \\
 &= Z(X' + X) + X'Y + ZY \\
 &= Z(Y + 1) + X'Y \\
 &= Z + X'Y
 \end{aligned}$$



Inputs			Outputs		
X	Y	Z	X'	$X'Y$	$Z + X'Y$
0	0	0	1	0	0
0	0	1	1	0	1
0	1	0	1	1	1
0	1	1	1	1	1
1	0	0	0	0	0
1	0	1	0	0	1
1	1	0	0	0	0
1	1	1	0	0	1



# LAB →

## Components Required →

Sl. No.	Name of the Component	Specification	Quantity
1	7404 IC	Hex Inverter	1
2	7408 IC	Quad 2 input AND Gates	1
3	7411 IC	3 input AND Gate	2
4	7432 IC	Quad 2 input OR Gate	1
5	Universal Trainer Kit	—	1
6	Connecting Wires	23 B WG	As required

## Observation →

1)  $F = AB + A'C + BC$

Inputs			Theoretical Output		Practical Output in both the cases
A	B	C	$AB + A'C + BC$	$AB + A'C$	
0	0	0	0	0	0
0	0	1	1	1	1
0	1	0	0	0	0
0	1	1	1	1	1
1	0	0	0	0	0
1	0	1	0	0	0
1	1	0	1	1	1
1	1	1	1	1	1

2) a)  $F = X'Y'Z' + X'YZ' + XY'Z' + XYZ'$

Inputs			Theoretical Output		Practical Output in both the cases
X	Y	Z	F	Z'	
0	0	0	1	1	1
0	0	1	0	0	0
0	1	0	1	1	1
0	1	1	0	0	0
1	0	0	1	1	1
1	0	1	0	0	0
1	1	0	1	1	1
1	1	1	0	0	0

$$b) F = X'YZ + X'YZ' + XY'Z + XY'Z'$$

Inputs			Theoretical Output		Practical Output
X	Y	Z	F	$X'Y + XY'$	in both the cases
0	0	0	0	0	0
0	0	1	0	0	0
0	1	0	1	1	1
0	1	1	1	1	1
1	0	0	1	1	1
1	0	1	1	1	1
1	1	0	0	0	0
1	1	1	0	0	0

$$3) F(X, Y, Z) = XY'Z + X'Y + YZ + X'Z$$

Inputs			Theoretical Output		Practical Output
X	Y	Z	F	$Z + X'Y$	in both the cases
0	0	0	0	0	0
0	0	1	1	1	1
0	1	0	1	1	1
0	1	1	1	1	1
1	0	0	0	0	0
1	0	1	1	1	1
1	1	0	0	0	0
1	1	1	1	1	1

### Conclusion →

This experiment Implementing Boolean functions using logic gates. Some basics of logic gates (AND, OR, NOT) in sum of product representation of equations and their implementation using logic gates and 7411 IC, 3 input AND gate is also used.



#### IV) Post Lab →

Ans

- 1) Using Boolean law, we can minimize a function which helps us to reduce the number of literals and construct the circuit using minimum number of gates.
- 2) We can represent the Boolean function using 2 forms i.e.
  - i) Standard form
  - ii) Canonical form

$$3) ABCF + ACEF + ACDF \\ = ACF (B+E+D)$$

