NATIONAL INSTITUTE OF TECHNOLOGY
TIRUCHIRAPPALLI

P DINESH

Male, Indian, 20 years
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PERSONAL DETAILS

- Father's Name : P RAMESH BABU
- Date of Birth : 14th Aug 1998
- Languages Known : English, Telugu, Tamil

EDUCATION

B.Tech **Electrical and Electronics Engineering** **CGPA : 8.95**

Semester	I Dec 15	II May 16	III Dec 16	IV May 17	V Dec 17	VI May 18
GPA	8.6	8.86	9.38	9.08	8.75	9.04

Class XII (State)	2015	Sri Chaitanya Junior College, Tirupati	97.5%
Class X (State)	2013	Chaitanya High School, Tirupati	97%

ACADEMIC ACHIEVEMENTS AND CO-CURRICULAR ACTIVITIES

- "FPGA based SoC Estimator and Constant Current Charging/Discharging Controller for Lead Acid Battery", **20th IEEE International Conference on Industrial Technology 2019**, Melbourne, Australia. (Communicated)
- **Finalist** of **Circutrix** event in **Pragyan'17**, an International Techno-Management festival of **NIT Trichy**.
- **Finalist** of **Embedtronix** event in **Probe'17**, the Annual Technical Symposium of the Electronics and Communication Engineering Department, **NIT Trichy**.
- Volunteered for a Workshop on **Hybrid Electrical Systems** during **Currents'17,** the Annual Technical Symposium of Electrical and Electronics Engineering Department, **NIT Trichy**.
- Participated in **Genesis'15** workshop organized by RMI (Robotics and Machine Intelligence), a robotic and technical research club of NIT Trichy.

PROJECT WORK/ TRAINING

- **Implementation of LEON3 Processor in FPGA** **(May '18 – July '18)**
  (Guide: Shri P Balasubramanian, Scientist-F, RIC DRDO, Chennai)
  The work involved setting up the software environment for LEON3 processor and implementing the same in Virtex-6 ML605 Evaluation kit with debug support unit enabled for benchmarking various design configurations.

- **FPGA based SoC Estimator and Constant Current Charging/Discharging Controller for Lead Acid Battery** **(May '17 – Dec '17)**
  (Guide: Asso. Prof. Dr. S. Moorthi, NIT Trichy)
  The Project involved FPGA based constant current charging and discharging controller using power converters and Extended Kalman Filtering is used to estimate the State of Charge of Battery. Verilog HDL and Embedded C are used to code the FPGA in this project.

- **E-Yantra 2016** **(Dec '16 – Mar '17)**
  E-Yantra is the Annual Robotics Competition of IIT Bombay, which involved building of two robots using ATMEGA 2560 Microcontrollers with wireless communication between them to traverse the arena and play the set of given sequential audio notes in shortest time.

- **In-Plant Training** **(Dec '16)**
  Underwent two weeks of industrial training at **Bharat Heavy Electricals Limited, Hyderabad**. The training involved a study on manufacturing of various parts of turbo alternators and their assembly.

SOFTWARE SKILL SET

- Languages : C, Embedded C, Verilog HDL, VHDL
- Operating System : Windows, Linux.
- Packages : MATLAB, Altium Designer Suite, ISE Design Suite, MS Office.

EXTRA CURRICULAR ACTIVITIES

- **Coordinator**, Public Relations and Hospitality Team of **Currents'16**, the Annual Technical Symposium of Electrical and Electronics Engineering Department, NIT Trichy
- **Organizer,** Workshops team of **Currents'17** and **Currents'18**, the Annual Technical Symposium of Electrical and Electronics Engineering Department, NIT Trichy.
- Part of the **NSS** team of NIT Trichy which organized events for school children from nearby localities. Volunteered in planning and scheduling various events.
- Attended a **Disaster Management Program** which involved strategic techniques that should be adopted during disastrous situations.