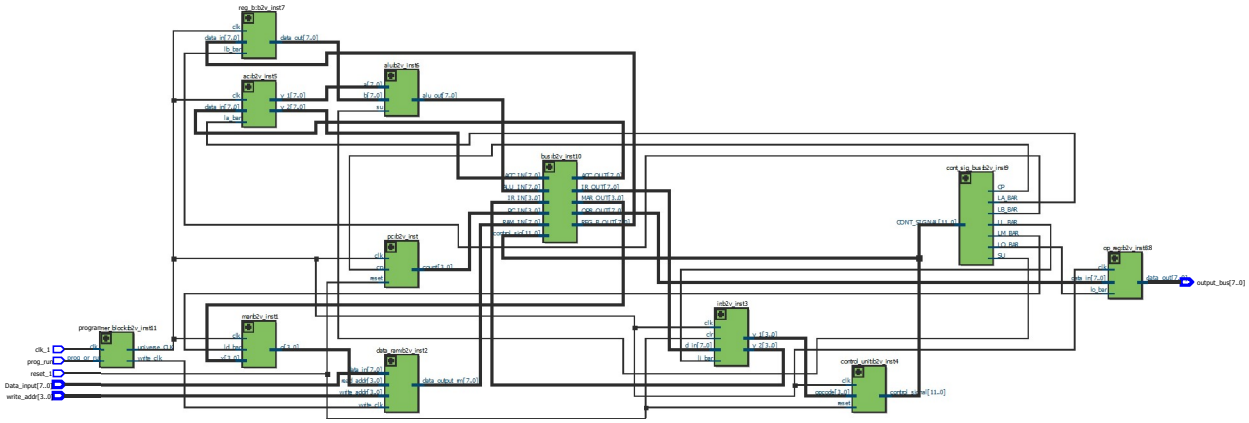
reg_bub2v_inst7
ac_bb2v_inst5
alub2v_inst6
bus_tb2v_inst10
cont_sig_bus_tb2v_inst9
pc_tb2v_inst
op_reg_tb2v_inst8
output_bus[7..0]
programer_block_tb2v_inst1
clk_1
prog_run
reset_1
Data_input[7..0]
write_addr[3..0]
mar_tb2v_inst4
data_ram_tb2v_inst2
ir_tb2v_inst3
control_unit_tb2v_inst4