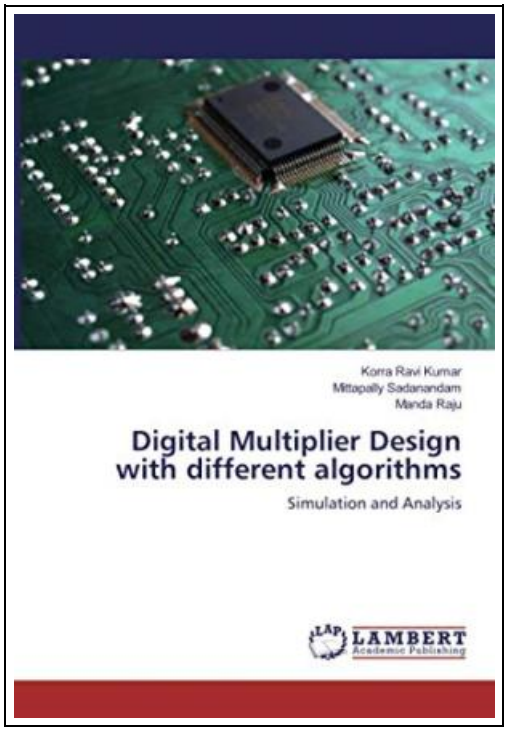
Digital Multiplier Design with different algorithms



Filesize: 1.48 MB

Reviews

An extremely wonderful publication with lucid and perfect reasons. It typically will not expense too much. You are going to like the way the blogger compose this publication.
(Prof. Maya Hand)

DIGITAL MULTIPLIER DESIGN WITH DIFFERENT ALGORITHMS

To read **Digital Multiplier Design with different algorithms** PDF, remember to click the button listed below and save the file or have accessibility to additional information that are in conjuction with DIGITAL MULTIPLIER DESIGN WITH DIFFERENT ALGORITHMS ebook.

LAP Lambert Academic Publishing Mai 2019, 2019. Taschenbuch. Condition: Neu. Neuware - Multipliers play an important role in today's digital signal processing and various other applications. In high performance systems such as microprocessor, DSP etc addition and multiplication of two binary numbers is fundamental and most often used arithmetic operations. Statics shows that more than 70% instructions in microprocessor and most of DSP algorithms perform addition and multiplication. So, these operations dominate the execution time. That's why; there is need of high speed multiplier. The demand of high speed processing has been increasing as a result of expanding computer and signal processing applications. Low power consumption is also an important issue in multiplier design. To reduce significant power consumption it is good to reduce the number of operation thereby reducing dynamic power which is a major part of total power consumption. So the need of high speed and low power multiplier has increased. Designer mainly concentrates on high speed and low power efficient circuit design. The objective of a good multiplier is to provide a physically packed together, high speed and low power consumption unit. 88 pp. Englisch.

Relevant eBooks