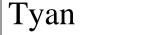
# SPEC® CINT2006 Result



SPECint®2006 =

Not Run

Tyan Thunder KKQS Pro (S4882)

SPECint\_base2006

0.00

				<del></del>
CPU2006 license #:	9999 Test sponsor:	Turbo Computers Test date:	Dec-9999 Hardware Availability:	Dec-9999 Software Availability: Dec-9999
	0 3.00 6.00 9.00 12.0 15.0 1	8.0 21.0 24.0 27.0 30.0 33.0 36.0 39.0	42.0 45.0 48.0 51.0 54.0 57.0 60.0 6	53.0 66.0 69.0 72.0 75.0 78.9 81.0 84.0 87.0 93.0
400 11 1				
400.perlbench	1			_
				* \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
401.bzip2	,			
401.021p2		26.8		<u> </u>
		20.0		
403.gcc				
			43.3	
420	c			
<b>429.</b> mc				A
			16.5	
445.gobmk				
		28.5		
456.hmmei				<u>/</u>
		/		74.6
458.sjeng	-		\ )/	
	3	30.6		
		30.0		
462.libquantum	1	//		
•				93.0
4641064 6	c	$\overline{}$		
464.h264re	Ĭ.			
		$\overline{}$	61.3	
471.omnetpp				
47 Homnetpp		25.0		
473.astar			_	
	*		$\mathcal{I}$	
		22.1	)/	
483.xalancbmk			//	
			47.4	
			47.4	
		$(() \land \land \land )$		

#### Hardware

CPU Name:

**CPU Characteristics:** 

CPU MHz: FPU:

CPU(s) enabled:

CPU(s) orderable:

Primary Cache:

Secondary Cache

L3 Cache:

Other Cache: (

Disk Subsystem:

Memory:

AMD Opteron 256

3000 Integrated

1 core, 1 chip, 1 core/chip

64 KB I + 64 KB D on chip per chip

1 MB 1 D on chip per chip

None

None

2 GB (2 1 GB DDR333 CL2.5)

**SATA** 

Other Hardware:

#### **Software**

Operating System: SUSE SLES9 (for AMD64)

Compiler: gcc, g++ & gfortran 4.2.0325 (for AMD64)

Auto Parallel: No File System: ext3 System State: runlevel 3

Base Pointers: 64-bit

Peak Pointers: Not Applicable

Other Software: None

#### **Errors**

'reportable' flag not set during run

- 471.omnetpp base did not have enough runs!
- 456.hmmer base did not have enough runs!
- 445.gobmk base did not have enough runs!
- 458.sjeng base did not have enough runs! 429.mcf base did not have enough runs!

Continued on next page

Standard Performance Evaluation Corporation info@spec.org http://www.spec.org/

Page 1

## SPEC CINT2006 Result Copyright ©2006 Standard Performance Evaluation Corporation

## Tyan

SPECint2006 =

Not Run

Tyan Thunder KKQS Pro (S4882)

SPECint base2006

0.00Dec-9999

CPU2006 license #:

9999 Test sponsor:

Turbo Computers Test date:

Dec-9999 Hardware Availability:

Dec-9999 Software Availability:

#### **Errors** (Continued)

473.astar base did not have enough runs!

483.xalancbmk base did not have enough runs!

400.perlbench base did not have enough runs!

464.h264ref base did not have enough runs!

462.libquantum base did not have enough runs!

401.bzip2 base did not have enough runs!

403.gcc base did not have enough runs!

Unknown flags were used! See

http://www.spec.org/auto/cpu2006/Docs/runspec.html#flagsurl for information about how to get rid of this error.

#### Results Table

Base					Peak						
Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
0.00	0.00										
<u>361</u>	<u>26.8</u>										
<u>186</u>	<u>43.3</u>			_							
<u>196</u>	<u>46.5</u>		$\wedge$								
<u>368</u>	<u>28.5</u>		$\overline{}$								
<u>125</u>	<u>74.6</u>				$\setminus \mathcal{I}$						
<u>396</u>	<u>30.6</u>										
<u>223</u>	<u>93.0</u>				7						
<u>361</u>	<u>61.3</u>			\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\							
<u>244</u>	<u>25.6</u>	1	)								
<u>318</u>	<u>22.1</u>	$\leq$ \									
<u>145</u>	47.4										
	0.00  361  186  196  368  125  396  223  361  244  318	0.00     0.00       361     26.8       186     43.3       196     46.5       368     28.5       125     74.6       396     30.6       223     93.0       361     61.3       244     25.6       318     22.1	Seconds         Ratio         Seconds           0.00         0.00         0.00           361         26.8         186           186         43.3         196           368         28.5         6           125         74.6         74.6           396         30.6         30.6           223         93.0         361         61.3           244         25.6         318         22.1	Seconds         Ratio         Seconds         Ratio           0.00         0.00         0.00         0.00           361         26.8         0.00 <t< td=""><td>Seconds         Ratio         Seconds         Ratio         Seconds           0.00         0.00         0.00         0.00           361         26.8         0.00         0.00           186         43.3         0.00         0.00           196         46.5         0.00         0.00         0.00           125         74.6         0.00</td></t<> <td>Seconds         Ratio         Seconds         Ratio         Seconds         Ratio           0.00</td> <td>Seconds         Ratio         Seconds         Ratio         Seconds           0.00</td> <td>Seconds         Ratio         Seconds         Ratio         Seconds         Ratio           0.00</td> <td>Seconds         Ratio         Seconds         Ratio         Seconds         Ratio         Seconds           0.00</td> <td>  Seconds   Ratio   Seconds   Se</td> <td>  Seconds   Ratio   Seconds  </td>	Seconds         Ratio         Seconds         Ratio         Seconds           0.00         0.00         0.00         0.00           361         26.8         0.00         0.00           186         43.3         0.00         0.00           196         46.5         0.00         0.00         0.00           125         74.6         0.00	Seconds         Ratio         Seconds         Ratio         Seconds         Ratio           0.00	Seconds         Ratio         Seconds         Ratio         Seconds           0.00	Seconds         Ratio         Seconds         Ratio         Seconds         Ratio           0.00	Seconds         Ratio         Seconds         Ratio         Seconds         Ratio         Seconds           0.00	Seconds   Ratio   Seconds   Se	Seconds   Ratio   Seconds

Results appear in the order hawhich they were run. Bold underlined text indicates a median measurement.

#### General Notes

PORTABILITY=-DSPEC\_CPU\_LP64 is applied to all benchmarks in base.

400.perlbehch: DSPEC CPU\_LINUX\_X64

462.libquantum: -DSPEC\_CPU\_LINUX
C base flags: -O3 /march=native
C++ base flags: -O3 -march=native -fpermissive -Wdeprecated -Wdeprecated-declarations Fortran base flags: -03 -march=native -std=legacy

### **Base Unknown Flags**

401.bzip2: "/usr/bin/gcc" (in CC) "/usr/bin/gcc" (in LD) "-03 -march=native" (in COPTIMIZE)

403.gcc: "/usr/bin/gcc" (in CC) "/usr/bin/gcc" (in LD) "-03 -march=native"(in COPTIMIZE)

Continued on next page

Standard Performance Evaluation Corporation info@spec.org http://www.spec.org/

## SPEC CINT2006 Result

Copyright ©2006 Standard Performance Evaluation Corporation

Tyan

SPECint2006 =

Not Run

Tyan Thunder KKQS Pro (S4882)

SPECint\_base2006

0.00

CPU2006 license #:

9999 Test sponsor:

Turbo Computers Test date:

Dec-9999 Hardware Availability:

Dec-9999 Software Availability:

### **Base Unknown Flags (Continued)**

429.mcf: "/usr/bin/gcc" (in CC) "/usr/bin/gcc" (in LD) "-03 -march=native" (in COPTIMIZE)

445.gobmk: "/usr/bin/gcc" (in CC) "/usr/bin/gcc" (in DD)
"-03 -march=native" (in COPTIMIZE)

456.hmmer: "/usr/bin/gcc" (in CC) "/usr/bin/gcc" (in ID)
"-03 -march=native" (in COPTIMIZE)

458.sjeng: "/usr/bin/gcc" (in CC) "/usr/bin/gcc" (in LD) "-03 -march=native" (in COPTIMIZE)

462.libquantum: "/usr/bin/gcc" (in CC)/"/usr/bin/gcc" (in LD)
"-03 -march=native" (n COPTIMIZE)

464.h264ref: "/usr/bin/gcc" (in CC) "/usr/bin/gcc" (in LD)
"-03 -march=native" (in COPTIMIZE)

471.omnetpp: "/usr/bin/gt+" (in CXX) "/usr/bin/g+" (in LD)
"-03 -march=native -fpermissive -Wdeprecated
-Wdeprecated-deplarations" (in CXXOPTIMIZE)

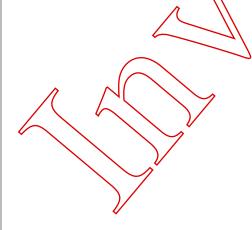
473.astar: "/usr/bin/g++" (in CXX) "/usr/bin/g++" (in LD)
"-03 -march=native -fpermissive -Wdeprecated
-Wdeprecated-declarations" (in CXXOPTIMIZE)

483.xalancbmk: "/usr/bin/g++" (in CXX) "/usr/bin/g++" (in LD)

"-include cstallib -include cstring" (in CXXPORTABILITY)

"-03 -march=native -fpermissive -Wdeprecated

-Wdeprecated-declarations" (in CXXOPTIMIZE)



SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.