Visition become Silt to indice many controlled to Controlled (1.4 think) and the Controlled to Controlled (1.4 think) and the Controlled (1.4 think) and th

D:\trabalho PCB grupo 9\TP1-laboratorio\Altium\Project Outputs for Codificador Morse\Design Rule Check - MAIN.drc

3650mil)(2180mil,3650mil) on Top Overlay (Top Overlay) to [Top Bolder] clearance [8.491mil]
Wislation between Silk To Solder Nesk Clearance Constraint: [8.491mil + 10mil) Between Pad 89-2(2500mil,3650mil) on Multi-Layer And Track (2420mil + 8650mil)(2460mil,3650mil) or Top Overlay) to [Top Bolder] clearance [8.491mil]

3660mil)(2460mil,3660mil) on Top Overlay [Top Overlay] to [Top Solder] clearance [8.491mil]
Rule Violations :31

3595mil, 3200mil) on Top Overlay Silk Text to Silk Clearance

Violation between Silk To Silk Clearance Constraint: (8.74mil 10mil) Between Text *2* (3645mil,3070mil) on Top Overlay And Track (3595mil,3000mil)

3575mm1,340mm1) on 100 Powerlay and wast to and clearance (0.6.4mm1) Violation between Silk To Silk Clearance Constraint (1.05mm1 < 10mm1) Between Text '5' (3285mm1,3170mm1) on Top Overlay And Track (3295mm1,3000mm1)

Violation between Silk To Silk Clearance Constraint: (2.063mil < 10mil) Between Text "6" (3285mil,3070mil) on Top Overlay And Track (3295mil,3000mil) on Top Overlay And Track (3295mil,3000mil) on Top Overlay Silk Tort to Silk Clearance [2.063mil]

Violation between Silk To Silk Clearance Constraint (6.07emil 10mil) Between Text 'CS_U_BOTAO3" (4140.104mil,1995.008mil) on Top Overlay And Track

4185mil,1970mil)(4635mil,1970mil) on Top Overlay Silk Text to Silk Clearance [6.074mil] Rule Violations:5

Pule Violations :0

Rule Violations :0

Violations Detected : 63 Waived Violations : 0