Maximum Number Finder

Sistemas Electrónicos de Computadores
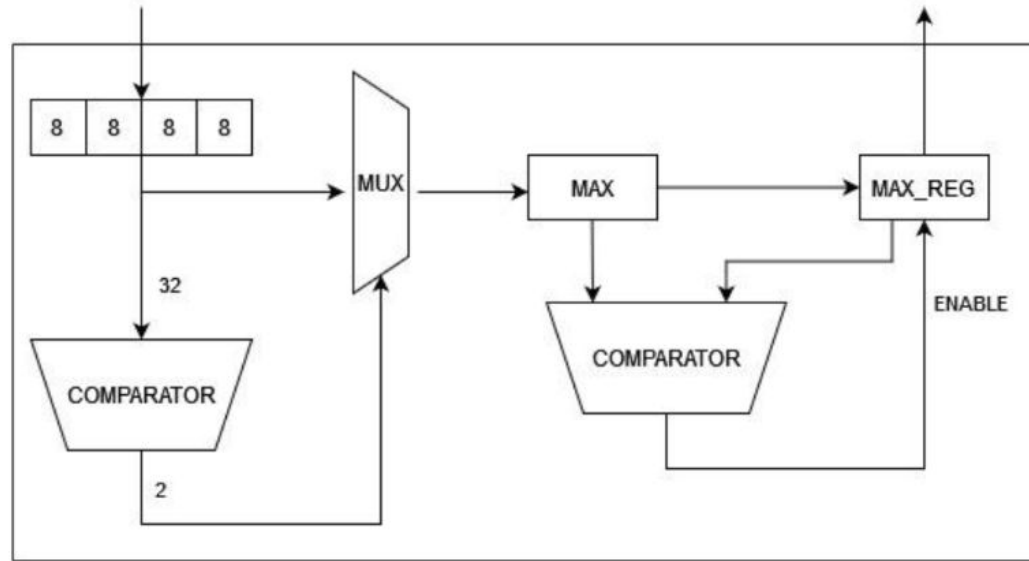
Dinis Cunha - 87875
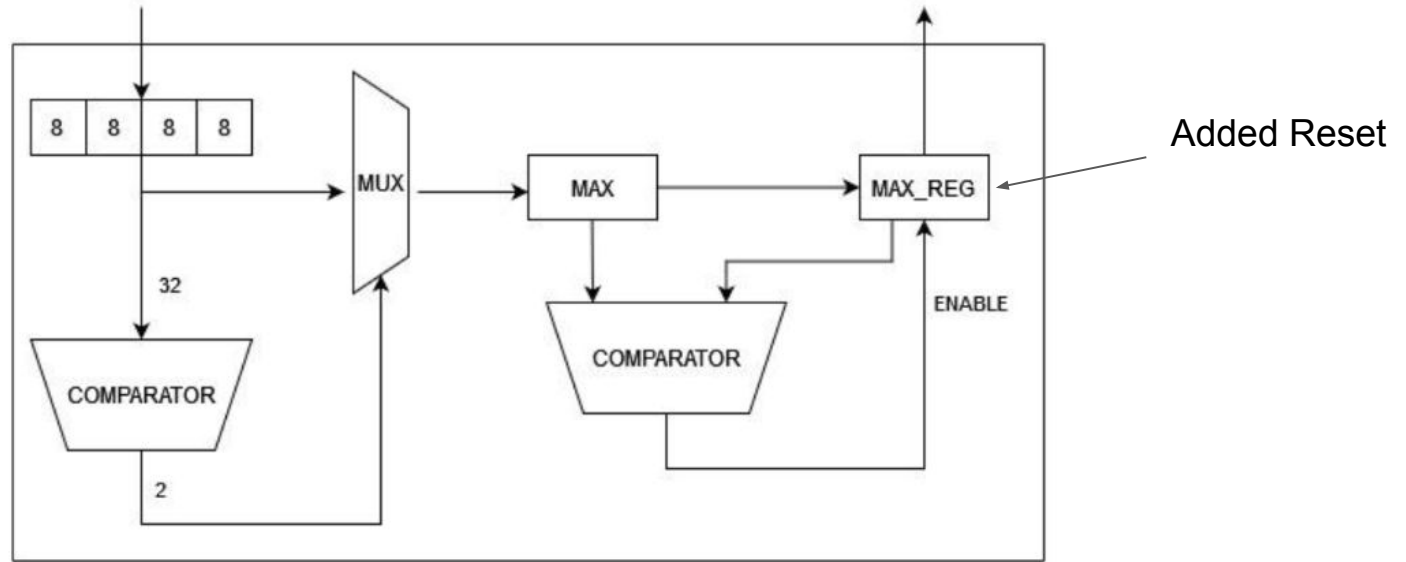Diogo Abreu - 94241

Hardware Acceleration

Expected Acceleration

- Four times acceleration in ideal conditions.

- Little variation with array size.

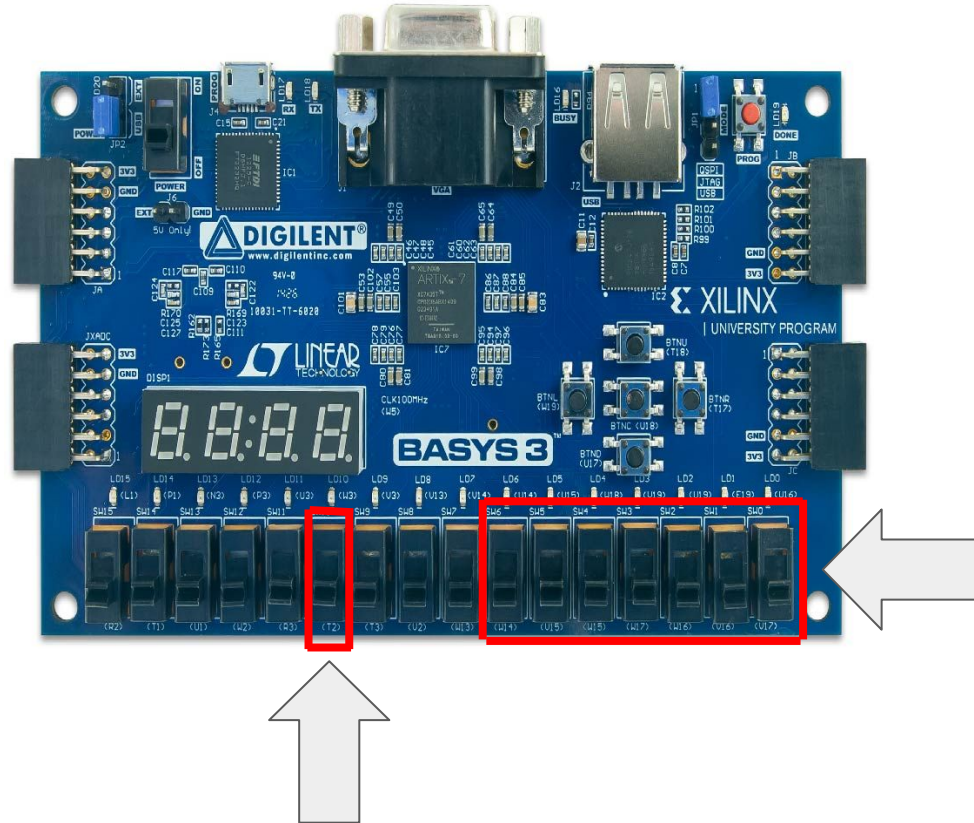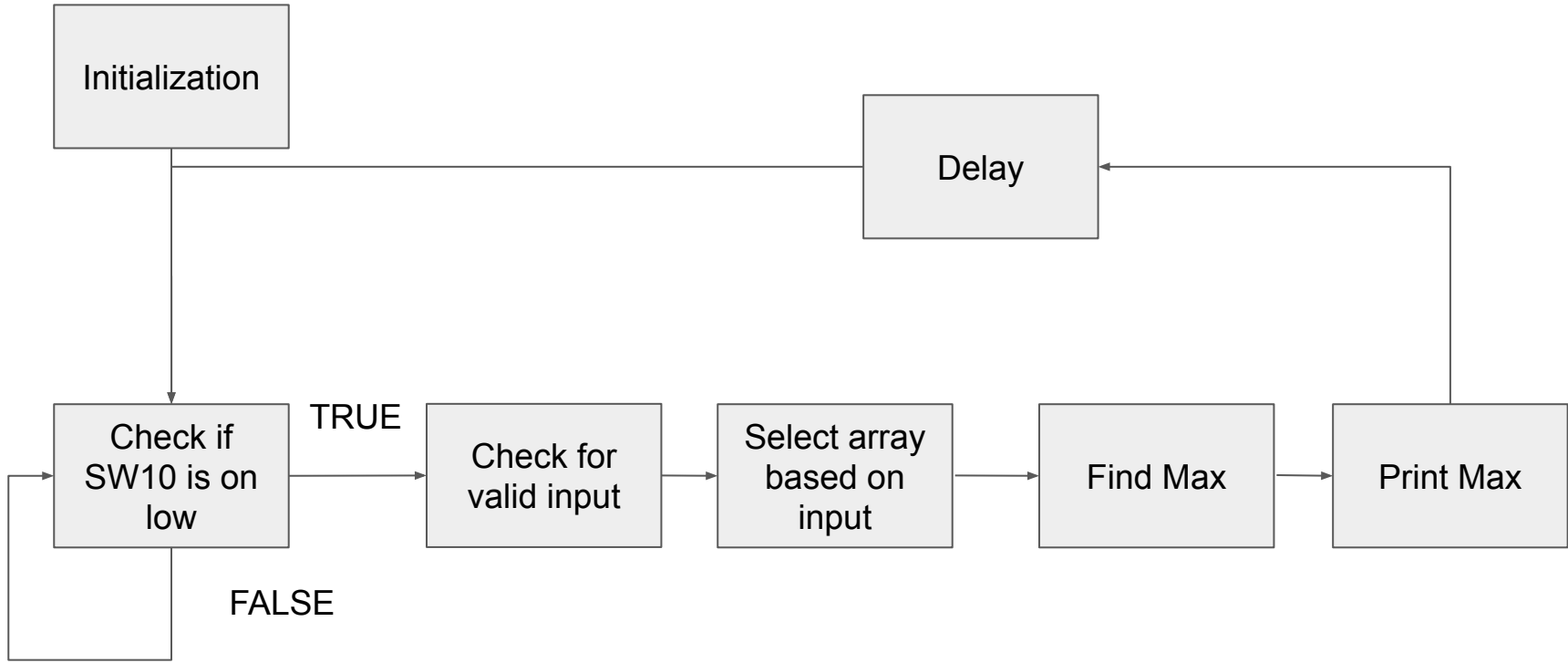- Write times reduce acceleration.

Designed Peripheral

Final Designed Peripheral

Added Reset

Interaction with the board

State Machine

Work for the Future

- Add run time comparison.

- Find a way to add or generate more arrays.

- Implement other types of hardware acceleration.