

Plano de Trabalho de Dissertação

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Universidade do Minho Escola de Engenharia

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Curso	Mestrado em Engenharia Eletrónica Industrial e Computadores	
Título da Dissertação (em Português)	Gerador de TFR acelerado em FPGA para aplicações baseadas em técnicas de CNN	
Título da Dissertação (em Inglês)	FPGA accelerated TFR generator for applications based on CNN techniques	

Enquadramento e Motivação (150 - 200 palavras)

A Convolutional Neural Network (CNN) is a Deep Learning (DL) method widely used for image and video classification [1], being an arising technology in recent years due to its powerful learning ability and the high accuracy achieved [2].

A Time-Frequency Representation (TFR) displays the time-frequency domain of a signal, i.e., the frequency content as a function of time, being particularly useful when the signal is non-stationary [3]. Therefore, a signal's TFR can be applied to a CNN model to detect and identify the patterns in a captured signal.

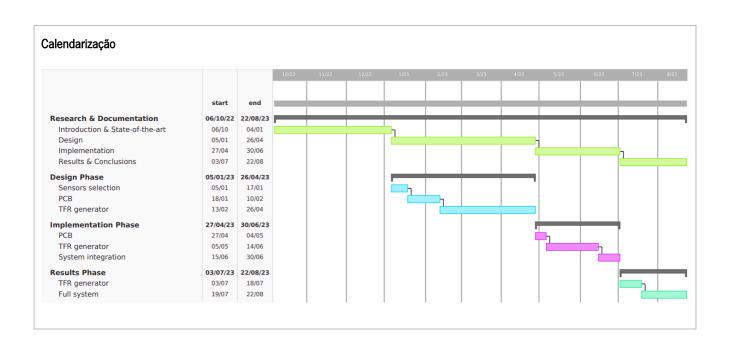
TFRs are utilized in the context of many applications. In the automotive industry, a microphone can be used to detect the alarm sirens of emergency vehicles trying to pass, alerting the drivers about their presence [4]. In the industry segment, TFR and CNN allow to diagnose machinery faults, easing the reparation of the fault and reducing costs [1].

However, the TFR generation algorithms are complex and time-consuming tasks, making it suitable for development in a FPGA platform. This dissertation proposes to design and implement a FPGA accelerated system capable of sampling an input signal, generating the respective TFR, and sending it to another system to be analyzed by a CNN model.

Objetivos e Resultados Esperados (150 - 200 palavras)

The objectives and expected results involved in the development of the system proposed by this dissertation are enumerated below:

- 1. Sample two signals: one from an accelerometer and one from a microphone. Knowing that the limits of human hearing [5] are roughly between 20 and 20 kHz, the minimum sampling frequency expected for the microphone is 40 kHz. The sampling frequency for the accelerometer may be lower because vibration signals have lower frequency components than sound signals.
- 2. Design and fabricate a PCB to accommodate and supply both sensors.
- 3. Design and implement a FPGA-based hardware accelerator for generation of TFRs, according to the state-of-the-art techniques.
- 4. Evaluate the FPGA resource utilization and investigate the potential for scaling the system to handle a greater number of sensors or higher data rates.
- 5. Evaluate the performance of the proposed system, compared to software based TFR generators and other state-of-the-art TFR generators, being expected a better performance with the proposed system.



Referências Bibliográficas (5 - 10 referências)

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- [2] Liu, H., Li, L., & Ma, J. (2016). Rolling Bearing Fault Diagnosis Based on STFT-Deep Learning and Sound Signals. *Shock and Vibration*, *2016*, 1–12. doi: 10.1155/2016/6127479
- [3] Hlawatsch, F., & Boudreaux-Bartels, G. F. (1992). Linear and quadratic time-frequency signal representations. *IEEE Signal Processing Magazine*, 21–67. doi: 10.1109/79.127284
- [4] Pramanick, D., Ansar, H., Kumar, H., Pranav, S., Tengshe, R., & Fatimah, B. (2021). Deep learning based urban sound classification and ambulance siren detector using spectrogram. *2021 12th International Conference on Computing Communication and Networking Technologies, ICCCNT 2021*. doi: 10.1109/ICCCNT51525.2021.9579778.
- [5] Purves, D., & Williams, S. M. (2001). *Neuroscience. 2nd edition.* Sinauer Associates 2001, ch. 12. Purves, D., & Williams, S. M. (2001). *Neuroscience. 2nd edition.* Sinauer Associates 2001. doi: ebk01:3450000000002013.



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Diretor do Ciclo de Estudos	Orientador (tal como previsto no ponto 3 do Artigo 169.º do RAUM. Neste caso, é obrigatório existir um Orientador pelo ponto 1 do Artigo 169.º do RAUM)

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