Total No	o. of Questions : 8] SEAT No. :	\neg
PB45	Total No. of Page	3
1 113	[6261]-44	.5 .0
	S.E.(Computer Engineering)	
	DIGITAL ELECTRONICS AND LOGIC DESIGN	
	(2019 Pattern) (Semester - III) (210245)	
Time: 2	2½ Hours] S	:70
	tions to the candidates:	
1)	Answers Q.1 or Q.2, Q.3 or Q.4, Q.5 or Q.6, Q.7 or Q.8.	
2) 3)	Neat diagrams must be draw wherever necessary. Assume Suitable data if necessary.	
3)	Assume Suttable data if necessary.	
01)	771	[7]
Q1) a)	What are the applications of Flip-Flop? Explain shift register.	[6]
b)	Design a sequence generator to generate the sequence.	[6]
0)		ĮΨJ
c)	Desgin the 2-bit synchronous UP counter using T - F/F.	[6]
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	OR O	
<i>Q2</i>) a)	Explain S-R Flip-Flop and J-K Flip Flop using NOR gate.	[6]
Q2) a)		
b)	Convert the following Flip Flops	[6]
,	i) D to T	
	ii) Jk to D	,
c)	Draw and explain 4 bit Ripple down Counter.	[6]
(12) -\\	Livelan ant the fallowing Dealers for the wards DVI	[7]
Q3) a)	Convert the following Flip Flops i) D to T ii) Jk to D Draw and explain 4 bit Ripple down Counter. Implement the following Boolem function using PAL. F1 = $\sum m(0,3,5,7,9,10,11,14,15)$ F2 = $\sum m(2,3,12,14)$	[0]
	$F1 = \sum m(0,3,5,7,9,10,11,14,15)$	
> "Y	$F2 = \sum m(2,3,12,14)$	

b) Draw block diagram of PLA device and explain in detail. [6]

c) What is ASM chart? Design ASM chart for 3-Bit gray code sequence with up-down conditions. [5]

OR

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ing 2-bit input.
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lue for TTL
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and shift registes.
[6]
Microprocessor.
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ocessor? [5]

Q8)	a)	Expl	lain in brief basic arithmetic operations using ALU IC 74	181. [6]
	b)	Writ	te a short note on	[6]
	0)	i)	te a short note on Address Bus Data Bus	اما
		ii)	Data Bus 💎	
		iii)	Control Bus	•
			20° 5°.	
	c)	Writ	te a short note on Memory organization of Microprocess	or. [5]
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