

1. Find the largest and smallest number in a given array of size N (8-bit numbers).
2. Find the smallest and smallest number in a given array of size N (8-bit numbers).
3. Arrange the elements of a given array of size N in ascending and ascending order (8-bit numbers).
4. Arrange the elements of an given array of size N in ascending and descending order (8-bit numbers).
5. Addition of two 16bit numbers using direct addressing mode.
6. Addition of two 16bit numbers using indirect addressing mode.
7. Addition of two 16bit numbers using index addressing mode.
8. Addition of two 16bit numbers using base index addressing mode.
9. Subtraction of two 16 bit numbers using direct addressing mode.
10. Multiplication of two 16 bit numbers using direct addressing mode.
11. Division of two 16 bit numbers using direct addressing mode.
12. AND two 16 bit numbers using direct addressing mode.
13. OR two 16 bit numbers using direct addressing mode.
14. NOT of a 16 bit number using direct addressing mode.
15. XOR of two 16 bit numbers using direct addressing mode.
16. Addition of two 32bit numbers using load/store addressing mode.
17. Subtraction of two 32bit numbers using load/store addressing mode.
18. Multiplication of two 32bit numbers using load/store addressing mode.
19. Find the Largest number from a given array of size N using ARM microprocessor
20. Find the smallest number from an array of size N using ARM microprocessor
21. Separate Even numbers and odds numbers in an array of size N using ARM microprocessor.
22. Perform the logical operations (AND, OR, XOR and NOT) on two 32bit numbers using load/store addressing mode.