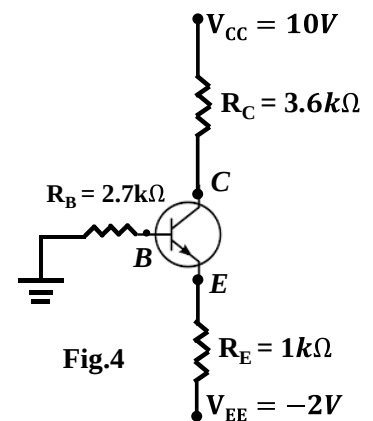
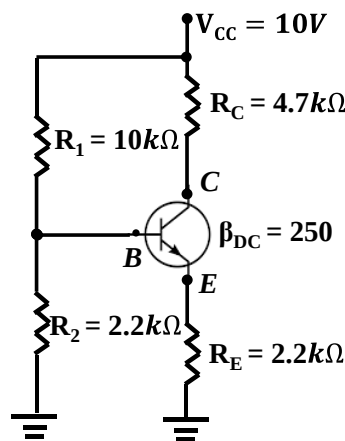
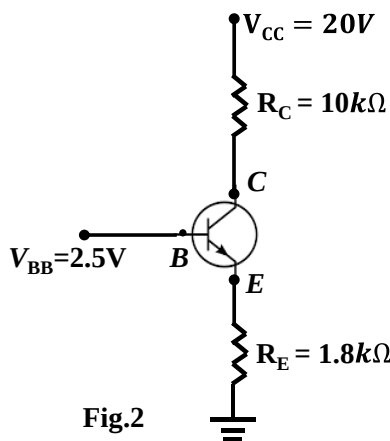
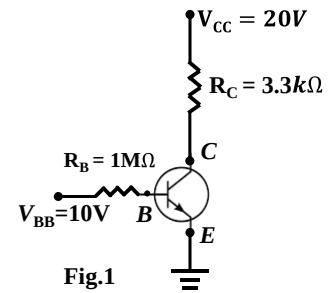


## Tutorial # 05

**Transistor Biasing:**

- For the given circuit in Fig.1,
  - Draw the load line,
  - Calculate  $I_{C(sat.)}$ ,
  - Calculate  $V_{CE(cutoff)}$ ,
  - What happens to the load line if (i)  $V_{CC}$  enhanced to 25V, (ii)  $R_C$  increased to  $4.7k\Omega$  and (iii)  $R_B$  reduced to  $500k\Omega$ . In each of these cases, other parameters of the circuit remain the same.
  - For  $\beta_{DC} = 200$ , determine  $V_C$ .
  - If  $\beta_{DC}$  varies between 25 to 300 then determine  $V_{C(max)}$  and  $V_{C(min)}$ .
  - Determine whether the transistor is saturated for each of these changes: (i)  $R_B=33k\Omega$ ,  $\beta_{DC} = 100$ . (ii)  $V_{BB}=5V$ ,  $\beta_{DC} = 200$ , (iii)  $R_C=10k\Omega$ ,  $\beta_{DC} = 50$  and (iv)  $V_{CC}=10V$ ,  $\beta_{DC} = 100$ .



- For the given circuit in Fig.2,
  - Determine  $V_C$  and  $V_E$ .
  - If  $R_E = 3.6k\Omega$  then determine  $V_{CE}$ .
  - If  $V_{CC} = 15V$  then determine  $V_C$ .
  - If the base supply voltage  $V_{BB}$  decreases by 10%, what happens to the base current, collector current, and collector voltage?
- For a given voltage-divider bias circuit in Fig.3
  - Is the voltage divider stiff?
  - Determine the operating point Q on the load line.
  - What will happen to this Q-point when  $\beta_{DC}$  varies between 50-300?
  - What will happen to this Q-point when  $R_E$  is doubled?
- For the circuit shown in Fig.3, determine the resistor values (i.e.,  $R_1$ ,  $R_2$ ,  $R_E$  and  $R_C$ ) to meet these specifications:  $V_{CC} = 10V$ ;  $V_{CE}$  @ midpoint,  $I_C = 10mA$  and  $\beta_{DC} = 100-300$ .
- Analyse the circuit in Fig.4 to locate the Q-point on the load-line.

6. Analyse the given emitter-feedback circuit in Fig.5. How does the operating point change when  $\beta_{DC}$  varies from 100 to 300?
7. Analyse the given collector-feedback circuit in Fig.6. How does the operating point change when  $\beta_{DC}$  varies from 100 to 300? Compare the results of Ques 5 and 6 and suggest which circuit gives better stability of Q-point?
8. Analyse the circuit in Fig.7 and locate the Q-point on the load-line.

