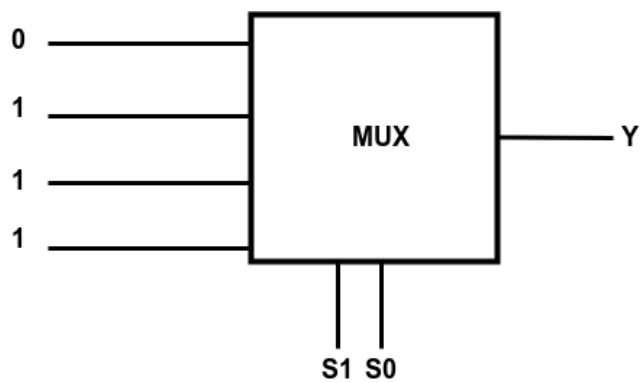|	
Started on	Saturday, 8 October 2022, 11:00 AM
State	Finished
Completed on	Saturday, 8 October 2022, 11:05 AM
Time taken	4 mins 58 secs
Marks	2.00/5.00
Grade	**4.00** out of 10.00 (**40**%)

Question **1**

Complete

Mark 1.00 out of 1.00

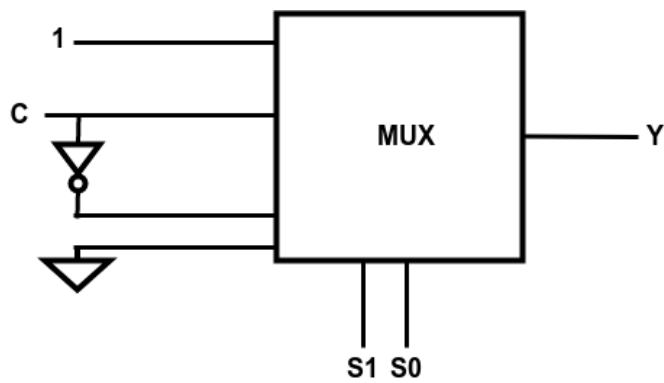The out Y of the circuit given below will perform the function of ?

○ a. X-OR Gate

○ b. X-NOR Gate

◉ c. OR Gate

○ d. AND Gate

What is the output of the circuit given below? If S1= A and S0= B

○ a. A'B + ABC + AB'C'

◉ b. AB + A'BC + AB'C'

○ c. A'B + A'BC + A'B'C'

○ d. A'B' + A'BC + AB'C'

Choose the right option for the circuit given below?
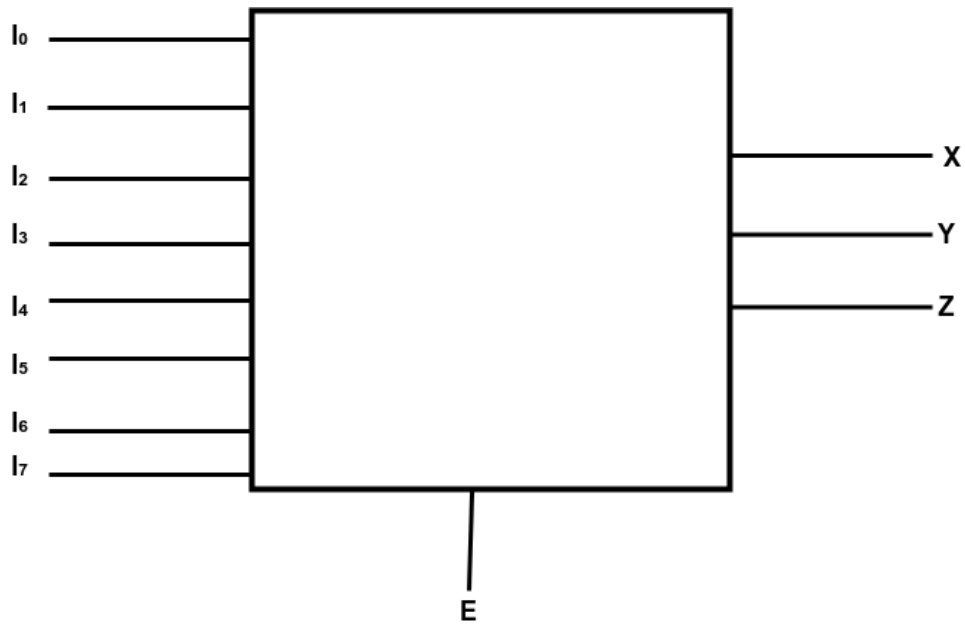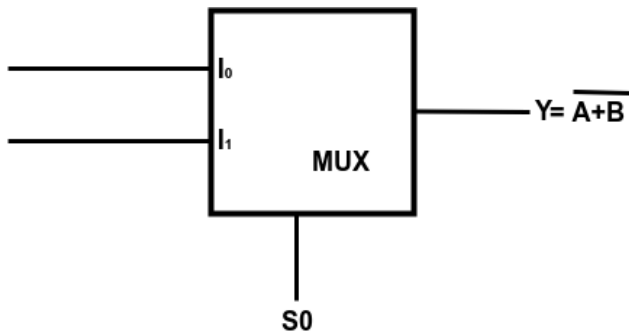

○ a. Octal to Hexadecimal Decoder

○ b. Decimal to BCD Decoder

◉ c. Octal to Binary Decoder

○ d. Decimal to Binary Decoder

For the circuit given below choose the correct option?

- a. S0 = A and I0 = I1= B'
- b. S0 = A, I0= B' and I1=0
- c. S0 = A, I0= B and I1=1
- d. S0 = A, I0= 0 and I1=1

Minimum numbers of NAND Gates requires to implements the X+XY'Z ?

- a. 1
- b. 3
- c. 0
- d. 2

Jump to...