

**Started on** Saturday, 12 November 2022, 11:00 AM

**State** Finished

**Completed on** Saturday, 12 November 2022, 11:08 AM

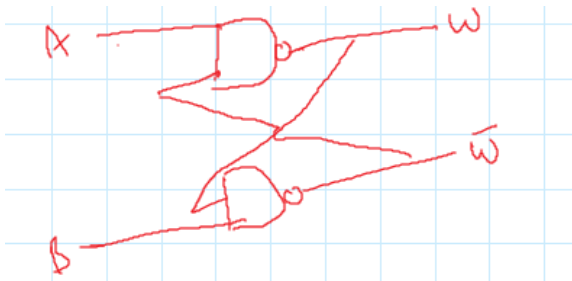
**Time taken** 7 mins 56 secs

Question **1**

Complete

Marked out of 1.00

For the circuit given below choose the correct option.



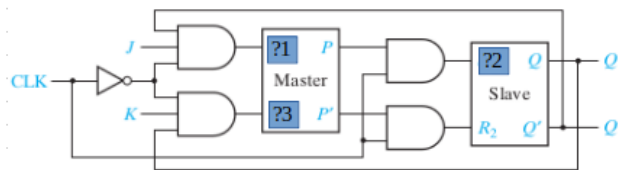
- ☐ a. All of the mentioned
- ☐ b.  $B = 0$  and  $W'$  is not reliable
- ☒ c.  $A = B = 0$  and  $W$  &  $W'$  not reliable
- ☐ d.  $A = 0$  and  $W$  is not reliable

Question 2

Complete

Marked out of 1.00

Find out the flip-flops.



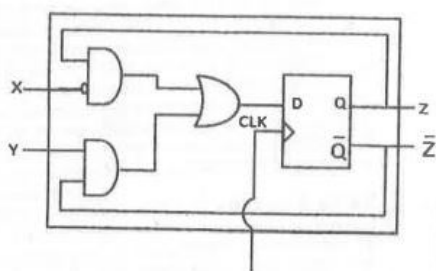
- ☐ a. S1,S2,R1
- ☒ b. J1,J2,R1
- ☐ c. S2,R1,S1
- ☐ d. S1,R1,S2

Question 3

Complete

Marked out of 1.00

A sequential circuit using D flip-flop and logic gates is shown in Figure, where  $X$  and  $Y$  are the inputs and  $Z$  is the output. The circuit is



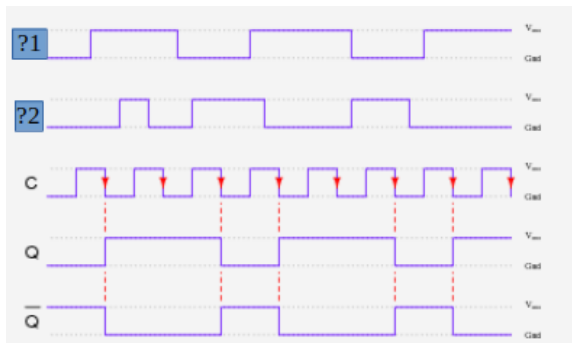
- ☐ a. J-K FF with input  $X=J$  and  $Y=K$
- ☐ b. S-R FF with input  $X=S$  and  $Y=R$
- ☒ c. J-K FF with input  $X=K$  and  $Y=J$
- ☐ d. S-R FF with input  $X=R$  and  $Y=S$

Question 4

Complete

Marked out of 1.00

Select the correct option for the waveform given below?



- ☐ a. J, K Flip Flop
- ☐ b. T Flip Flop
- ☒ c. S, R Flip Flop
- ☐ d. D Flip Flop

Question 5

Complete

Marked out of 1.00

Select the correct option ?

- ☒ a. J-K Flip Flop is universal flip flop
- ☐ b. All of the mentioned
- ☐ c. S-R Flip Flop is universal flip flop
- ☐ d. T Flip Flop is universal flip flop

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